

## Product Overview

### MC14076B: 4-Bit D Register with Three-State Outputs

For complete documentation, see the data sheet.

The MC14076B 4-Bit Register consists of four D-type flip-flops operating synchronously from a common clock. OR gated output-disable inputs force the outputs into a high-impedance state for use in bus organized systems. OR gated data-disable inputs cause the Q outputs to be fed back to the D inputs of the flip-flops. Thus they are inhibited from changing state while the clocking process remains undisturbed. An asynchronous master reset is provided to clear all four flip-flops simultaneously independent of the clock or disable inputs.

### Features

- Three-State Outputs with Gated Control Lines
- Fully Independent Clock Allows Unrestricted Operation for the Two Modes: Parallel Load and Do Nothing
- Asynchronous Master Reset
- Four Bus Buffer Registers
- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- Capable of Driving Two Low-Power TTL Loads or One Low-Power Schottky TTL Load Over the Rated Temperature Range
- Pb-Free Packages are Available\*

### Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Channels	V <sub>CC</sub> Min (V)	V <sub>CC</sub> Max (V)	t <sub>pd</sub> Max (ns)	I <sub>O</sub> Max (mA)	Package Type
MC14076BDR2G	0.308	Pb-free Halide free non AEC-Q and PPAP	Active	Register	4	3	18	250	2.25	SOIC-16
NLV14076BDR2G	0.2933	Pb-free Halide free non AEC-Q and PPAP	Active	Register	4	3	18	250	2.25	SOIC-16

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