

Product Overview

MC14013B: Dual D Flip-Flop

For complete documentation, see the data sheet.

The MC14013B dual type D flip-flop is constructed with MOS P-channel and N-channel enhancement mode devices in a single monolithic structure. Each flip-flop has independent Data, (D), Direct Set, (S), Direct Reset, (R), and Clock (C) inputs and complementary outputs (Q and Qbar). These devices may be used as shift register elements or as type T flip-flops for counter and toggle applications.

Features

- Static Operation
- Diode Protection on All Inputs
- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- Logic Edge-Clocked Flip-Flop Design Logic state is retained indefinitely with clock level either high or low; information is transferred to the output only on the positive-going edge of the clock pulse
- Capable of Driving Two Low-power TTL Loads or One Low-power Schottky TTL Load Over the Rated Temperature Range
- Pin-for-Pin Replacement for CD4013B
- Pb-Free Packages are Available*

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC14013BDG	0.1933	Pb-free	Active	D-Type	2	3	18	150	2.25	SOIC-14
		Halide free non AEC-Q and PPAP								
MC14013BDR2G	0.12	Pb-free	Active	D-Type	2	3	18	150	2.25	SOIC-14
		Halide free non AEC-Q and PPAP								
MC14013BDTR2G	0.14	Pb-free	Active	D-Type	2	3	18	150	2.25	TSSOP-14
		Halide free non AEC-Q and PPAP								
NLV14013BDR2G	0.198	AEC Qualified	Active	D-Type	2	3	18	150	2.25	SOIC-14
		PPAP Capable								
		Pb-free								
		Halide free								
NLV14013BDTR2G	0.198	AEC Qualified	Active	D-Type	2	3	18	150	2.25	TSSOP-14
		PPAP Capable								
		Pb-free								
		Halide free								

For more information please contact your local sales support at www.onsemi.com.

Created on: 8/1/2021