

Product Overview

MC74HCT245A: Octal 3-State Non-Inverting Bus Transceiver

For complete documentation, see the data sheet.

High-Performance Silicon-Gate CMOS The MC74HCT245A is identical in pinout to the LS245. This device may be used as a level converter for interfacing TTL or NMOS outputs to High Speed CMOS inputs. The MC74HCT245A is a 3-state noninverting transceiver that is used for 2-way asynchronous communication between data buses. The device has an active-low Output Enable pin, which is used to place the I/O ports into high-impedance states. The Direction control determines whether data flows from A to B or from B to A.

Features

- Output Drive Capability: 15 LSTTL Loads
- TTL/NMOS Compatible Input Levels
- Outputs Directly Interface to CMOS, NMOS and TTL
- Operating Voltage Range: 4.5 to 5.5 V
- Low Input Current: 1.0 mA
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 304 FETs or 76 Equivalent Gates
- Pb-Free Packages are Available

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Channels	Output	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HCT245ADTG	0.28	Pb-free Halide free non AEC-Q and PPAP	Active	8	3-State	4.5	5.5	22	6	TSSOP-20
MC74HCT245ADTR2G	0.28	Pb-free Halide free non AEC-Q and PPAP	Active	8	3-State	4.5	5.5	22	6	TSSOP-20
MC74HCT245ADWG	0.2267	Pb-free Halide free non AEC-Q and PPAP	Active	8	3-State	4.5	5.5	22	6	SOIC-20W
MC74HCT245ADWR2G	0.2	Pb-free Halide free non AEC-Q and PPAP	Active	8	3-State	4.5	5.5	22	6	SOIC-20W
NLVHCT245ADTR2G	0.28	AEC Qualified PPAP Capable Pb-free Halide free	Active	8	3-State	4.5	5.5	22	6	TSSOP-20

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Created on: 1/17/2021