

## Product Overview

### NLU1GT50: Single Non-Inverting Buffer with TTL-Compatible Inputs

For complete documentation, see the data sheet.

The NLU1GT50 MiniGate™ is an advanced CMOS high speed non inverting buffer in ultra small footprint. The device input is compatible with TTL type input thresholds and the output has a full 5.0 V CMOS level output swing. The NLU1GT50 input and output structures provide protection when voltages up to 7.0 volts are applied, regardless of the supply voltage.

#### Features

- Designed for 1.65 to 5.5 V VCC Operation
- Low Power Dissipation: ICC = 1 A (Max) at TA = 25C
- Power Down Protection Provided on inputs
- Ultra Small Pb Free Package
- High Speed: tPD = 3.5 ns (Typ) at VCC = 5.0 V
- TTL Compatible Input: VIL = 0.8 V; VIH = 2.0 V, VCC = 5.0 V
- Balanced Propagation Delays

#### Applications

- Portable, wireless, computing, automotive
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- General Purpose Logic

#### Benefits

- Wide operating range allows for use across multiple platforms.
- Improved battery life.
- Extends battery life.
- 1.0mm x 1.2mm package allows a designer to fit this device in tight areas.

#### End Products

- PDA, Cell Phones, Notebooks

### Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Channels	Output	V <sub>CC</sub> Min (V)	V <sub>CC</sub> Max (V)	t <sub>pd</sub> Max (ns)	I <sub>O</sub> Max (mA)	Package Type
NLU1GT50MUTCG	0.1835	Pb-free Halide free non AEC-Q and PPAP	Active	1	CMOS	1.65	5.5	6.7	8	UDFN-6

For more information please contact your local sales support at [www.onsemi.com](http://www.onsemi.com).

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