

Product Overview

MC74VHC126: Quad Bus Buffer, 3-State

For complete documentation, see the data sheet.

The MC74VHC126 is a high speed CMOS quad bus buffer fabricated with silicon gate CMOS technology. It achieves noninverting high speed operation similar to equivalent Bipolar Schottky TTL while maintaining CMOS low power dissipation. The MC74VHC126 requires the 3-state control input (OE) to be set Low to place the output into high impedance. The internal circuit is composed of three stages, including a buffer output which provides high noise immunity and stable output. The inputs tolerate voltages up to 7V, allowing the interface of 5V systems to 3V systems.

Features

- High Speed: $t_{PD} = 3.8\text{ns}$ (Typ) at $V_{CC} = 5\text{V}$
- Low Power Dissipation: $I_{CC} = 4\text{mA}$ (Max) at $T_A = 25^\circ\text{C}$
- High Noise Immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$
- Power Down Protection Provided on Inputs
- Balanced Propagation Delays
- Designed for 2V to 5.5V Operating Range
- Low Noise: $V_{OLP} = 0.8\text{V}$ (Max)
- Pin and Function Compatible with Other Standard Logic Families
- Latchup Performance Exceeds 300mA
- ESD Performance: HBM > 2000V; Machine Model > 200V

For more features, see the data sheet

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
MC74VHC126DR2G	Pb-free	Active	4	3-State	2	5.5	7.5	8	SOIC-14
	Halide free								
MC74VHC126DTR2G	Pb-free	Active	4	3-State	2	5.5	7.5	8	TSSOP-14
	Halide free								
NLV74VHC126DR2G	Pb-free	Active	4	3-State	2	5.5	7.5	8	SOIC-14
	Halide free								
NLV74VHC126DTR2G	AEC Qualified	Active	4	3-State	2	5.5	7.5	8	TSSOP-14
	PPAP Capable								
	Pb-free								
	Halide free								

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Created on: 9/22/2019