

Product Overview

MC74HC240A: Octal Inverting Buffer/Line Driver/Line Receiver, 3-State

For complete documentation, see the data sheet.

High-Performance Silicon-Gate CMOS The MC74HC240A is identical in pinout to the LS240. The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs. This octal noninverting buffer/line driver/line receiver is designed to be used with 3-state memory address drivers, clock drivers, and other sub-oriented systems. The device has inverting outputs and two active-low output enables. The HC240A is similar in function to the HC241A and HC244A.

Features

- Output Drive Capability: 15 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 2 to 6 V
- Low Input Current: 1 mA
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 120 FETs or 30 Equivalent Gates
- Pb-Free Packages are Available*

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HC240ADTR2G	Pb-free	Active	8	3-State	2	6	22	6	TSSOP-20
	Halide free								
MC74HC240ADWG	Pb-free	Active	8	3-State	2	6	22	6	SOIC-20W
	Halide free								
MC74HC240ADWR2G	Pb-free	Active	8	3-State	2	6	22	6	SOIC-20W
	Halide free								
NLV74HC240ADTR2G	AEC Qualified	Active	8	3-State	2	6	22	6	TSSOP-20
	PPAP Capable								
	Pb-free								
	Halide free								
NLV74HC240ADWG	Pb-free	Active	8	3-State	2	6	22	6	SOIC-20W
	Halide free								
NLV74HC240ADWR2G	Pb-free	Active	8	3-State	2	6	22	6	SOIC-20W
	Halide free								

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