

Product Overview

74VCX162244: Low Voltage 16-Bit Buffer/Line Driver with 3.6V Tolerant Inputs and Outputs and 26 Ohm Series Resistor in Outputs

For complete documentation, see the data sheet.

The VCX162244 contains sixteen non-inverting buffers with 3-STATE outputs to be employed as a memory and address driver, clock driver, or bus oriented transmitter/receiver. The device is nibble (4-bit) controlled. Each nibble has separate 3-STATE control inputs which can be shorted together for full 16-bit operation. The 74VCX162244 is designed for low voltage (1.2V to 3.6V) VCC applications with I/O capability up to 3.6V. The 74VCX162244 is also designed with 26ohm series resistors in the outputs. This design reduces line noise in applications such as memory address drivers, clock drivers, and bus transceivers/transmitters. The 74VCX162244 is fabricated with an advanced CMOS technology to achieve high speed operation while maintaining low CMOS power dissipation.

Features

- 1.2V to 3.6V VCC supply operation
- 3.6V tolerant inputs and outputs
- 26ohm series resistors in outputs
- tPD 3.3 ns max for 3.0V to 3.6V VCC
- Power-off high impedance inputs and outputs
- Supports live insertion and withdrawal
- Static Drive (IOH/IOL) ± 12 mA @ 3.0V VCC
- Uses patented noise/EMI reduction circuitry
- Latch-up performance exceeds 300 mA
- ESD performance: Human body model > 2000V Machine model > 200V

For more features, see the data sheet

Applications

- This product is general usage and suitable for many different applications.

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
74VCX162244MTD	Pb-free	Active	16	3-State	1.2	3.6	3.3	12	TSSOP-48
	Halide free								
74VCX162244MTDX	Pb-free	Active	16	3-State	1.2	3.6	3.3	12	TSSOP-48
	Halide free								

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