

MC33272A

Operational Amplifiers, Single Supply, High Slew Rate, Low Input Offset Voltage, Dual

Product Overview

For complete documentation, see the data sheet.







The MC33272/74 series of monolithic op-amps are quality fabricated with innovative Bipolar design concepts. This dual and quad operational amplifier series incorporates Bipolar inputs along with a patented Zip-R-Trim element for input offset voltage reduction. The MC33272/74 series of op-amps exhibits low input offset voltage and high gain bandwidth product. Dual-doublet frequency compensation is used to increase the slew rate while maintaining low input noise characteristics. Its all NPN output stage exhibits no deadband crossover distortion, large output voltage swing, and an excellent phase and gain margin. It also provides a low open loop high frequency output impedance with symmetrical source and sink AC frequency performance.

Features

- Input Offset Voltage Trimmed to 100 μ V (Typ)
- Low Input Bias Current: 300 nA
- Low Input Offset Current: 3.0 nA
- High Input Resistance: 16 M Ω
- Low Noise: 18 nV/ (sq. root Hz) @ 1.0 kHz
- High Gain Bandwidth Product: 24 MHz @ 100 kHz
- High Slew Rate: 10 V/ μ s
- Power Bandwidth: 160 kHz
- Excellent Frequency Stability
- Unity Gain Stable: w/Capacitance Loads to 500 pF

For more features, see the data sheet

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Rail to Rail	Channels	V _s Min (V)	V _s Max (V)	I _q Typ (mA)	V _{os} Max (mV)	GBW Typ (MHz)	SR Typ (V/ μ s)	I _o Typ (mA)	$\Delta V_{os}/\Delta T$ (μ V/C)	e _N (nV/ \sqrt Hz)	I _{bias} Typ (pA)	CMRR Typ (dB)	Architecture	Temperature Range (°C)	Package Type
MC33272ADR2G	0.4268	 	Active	No	2	3	36	2.15	1	24	10	37	2	18	30000	100	Bipolar	-40 to 85	SOIC-8
NCV33272ADR2G	0.5397	   	Active	No	2	3	36	2.15	1	24	10	37	2	18	30000	100	Bipolar	-40 to 125	SOIC-8