

FAN53880

One Buck, One Boost and Four LDO PMIC

Product Overview

For complete documentation, see the data sheet.

The FAN53880 is a low quiescent current PMIC for mobile power applications. The PMIC contains one buck, one boost, and four low noise LDOs.

Features

- Wafer-Level Chip-Scale Package (WLCSP)
- Programmable Output Voltages
- Soft-Start (SS) Inrush Current Limiting
- Programmable Start-Up/Down Sequencing
- Fault Protection with Interrupt Reporting
- Low Current Standby and Shutdown Modes
- Buck Converter: 1.2A, VIN Range: 2.5V to 5.5V, VOUT Range: 0.6V to 3.3V
- Boost Converter: 1.0A, VIN Range: 2.5V to 5.5V, VOUT Range: 3.0V to 5.7V
- Four LDOs: 300mA, VIN Range: 1.9V to 5.5V, VOUT Range: 0.8V to 3.3V

Applications

- Battery and USB powered devices

End Products

- Smartphones
- Tablets
- Compact Camera Modules

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Topology	Control Mode	V _{CC} Min (V)	V _{CC} Max (V)	V _O Typ (V)	I _O Typ (A)	Efficiency (%)	f _{SW} Typ (kHz)	Package Type
FAN53880UC002X	0.74		Active									WLCSP-25

Application Diagram

