

NCP1632

Power Factor Controller, Interleaved, 2-Phase

Product Overview

For complete documentation, see the data sheet.

The NCP1632 integrated a dual MOSFET driver for interleaved PFC applications. Interleaving consists of paralleling two small stages in lieu of a bigger one, more difficult to design. This approach has several merits like the ease of implementation, the use of smaller components or a better distribution of the heating. Also, Interleaving extends the power range of Critical Conduction Mode that is an efficient and cost-effective technique (no need for low trr diodes). In addition, the NCP1632 drivers are 180o phase shifted for a significantly reduced current ripple. Housed in a SOIC16 package, the circuit incorporates all the features necessary for building robust and compact interleaved PFC stages, with a minimum of external components.

Features

- 180° out of phase operation
- Frequency Clamped Critical conduction Mode (FCCrM) operation
- Frequency foldback at low power
- Integrated Overcurrent protection, Overvoltage protection, UVLO and brown-out protection

Benefits

- Low EMI and reduces RMS current in the bulk capacitor
- Optimize efficiency over the load range
- Improve light load efficiency
- Increase system reliability

Applications

- All Off Line Appliance Requiring Power Factor Correction
- SMPS
- Flat panel TVs

End Products

- Computer Power Supplies
- LCD TV
- Server power, networking power

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	PFC Mode	Frequency Operation	Control Mode	Topology	f _{sw} Typ (kHz)	V _{cc} Max (V)	Drive Cap. (mA)	UVLO (V)	Latch	UVP	Inhibition	Package Type
NCP1632DR2G	0.605		Active												SOIC-16