

Product Overview

CAT25640: EEPROM Serial 64-Kb SPI

For complete documentation, see the data sheet.

The CAT25640 is a EEPROM Serial 64-Kb SPI device internally organized as 8kx8 bits. This features a 64-byte page write buffer and supports the Serial Peripheral Interface (SPI) protocol. The device is enabled through a Chip Select (CS) input. In addition, the required bus signals are clock input (SCK), data input (SI) and data output (SO) lines. The HOLD input may be used to pause any serial communication with the CAT25640 device. The device features software and hardware write protection, including partial as well as full array protection.

Features

- 20 MHz (5 V) SPI Compatible
- 1.8 V to 5.5 V Supply Voltage Range
- SPI Modes (0,0) & (1,1)
- 64-byte Page Write Buffer
- Self-timed Write Cycle
- Hardware and Software Protection
- Block Write Protection - Protect 1/4, 1/2 or Entire EEPROM Array
- Low Power CMOS Technology
- 1,000,000 Program/Erase Cycles
- 100 Year Data Retention

For more features, see the data sheet

Applications

- Automotive Systems
- Communications Systems
- Computer Systems
- Consumer Systems
- Industrial Systems

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Density	Organization	Data Transmission Standard	f _{cycle} Max (kHz)	t _{acc} Max ns	V _{cc} Min (V)	V _{cc} Max (V)	I _{standby} Max (μA)	I _{act} Max (mA)	T Min (°C)	T Max (°C)	Package Type
CAT25640HU4I-GT3	0.32	Pb-free Halide free	Active	Serial	64 kb	8k x 8	SPI	10000	40	1.8	5.5	2	2	-40	85	UDFN-8
CAT25640VI-GT3	0.2133	Pb-free Halide free	Active	Serial	64 kb	8k x 8	SPI	10000	40	1.8	5.5	2	2	-40	85	SOIC-8
CAT25640YI-GT3	0.2133	Pb-free Halide free	Active	Serial	64 kb	8k x 8	SPI	10000	40	1.8	5.5	2	2	-40	85	TSSOP-8

For more information please contact your local sales support at www.onsemi.com.

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