

Product Overview

CAT1025: EEPROM Serial 2-Kb CPU Supervisor

For complete documentation, see the data sheet.

The CAT1025 is a complete memory and supervisory solution for microcontroller-based systems. A EEPROM Serial 2-Kb CPU Supervisor and a system power supervisor with brown-out protection are integrated together in low power CMOS technology. Memory interface is via a 400 kHz I²C bus. The CAT1025 provides a precision VCC sense circuit and two open drain outputs: one (RESET) drives high and the other (RESETP) drives low whenever VCC falls below the reset threshold voltage. The CAT1025 also has a Write Protect input (WP). Write operations are disabled if WP is connected to a logic high. The power supply monitor and reset circuit protect memory and system controllers during power up/down and against brownout conditions. Five reset threshold voltages support 5.0 V, 3.3 V and 3.0 V systems. If power supply voltages are out of tolerance reset signals become active, preventing the system microcontroller, ASIC or peripherals from operating. Reset signals become inactive typically 200 ms after the supply voltage exceeds the reset threshold level. With both active high and low reset signals, interface to microcontrollers and other ICs is simple. In addition, the RESET pin or a separate input, MR, can be used as an input for push-button manual reset capability. The CAT1025 memory features a 16-byte page. In addition, hardware data protection is provided by a VCC sense circuit that prevents writes to memory whenever VCC falls below the reset threshold or until VCC reaches the reset threshold during power up. Available packages include an 8-pin DIP, 8-pin SOIC, 8-pin TSSOP, 8-pin TDFN and 8-pin MSOP. The TDFN package thickness is 0.8 mm maximum. TDFN footprint is 3 x 3 mm.

Features

- Precision power supply voltage supervisor 5.0 V, 3.3 V and 3.0 V systems Five threshold voltage options
- Active high or low reset — Valid reset guaranteed at VCC = 1V
- 400 kHz I²C bus
- 3.0 V to 5.5 V operation
- Low power CMOS technology
- 16-Byte page write buffer
- Built-in inadvertent write protection — WP pin
- 1,000,000 Program/Erase cycles
- Manual reset input
- 100 year data retention

For more features, see the data sheet

Applications

- Industrial control
- Medical systems
- Printers
- Mass storage

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Type	Density	Organization	Data Transmission Standard	f _{cycle} Max (kHz)	t _{ACC} Max ns	V _{CC} Min (V)	V _{CC} Max (V)	I _{standby} Max (μA)	I _{act} Max (mA)	T Min (°C)	T Max (°C)	Package Type
CAT1025WI-25-GT3	0.3243	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	SOIC-8
CAT1025WI-28-GT3	0.5575	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	SOIC-8
CAT1025WI-30-GT3	0.78	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	SOIC-8
CAT1025WI-42-GT3	0.2975	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	SOIC-8
CAT1025WI-45-GT3	0.78	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	SOIC-8
CAT1025YI-25-GT3	0.78	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	TSSOP-8
CAT1025YI-28-GT3	0.78	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	TSSOP-8
CAT1025YI-30-GT3	0.78	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	TSSOP-8
CAT1025YI-42-GT3	0.78	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	TSSOP-8
CAT1025YI-45-GT3	0.78	Pb-free Halide free	Active	Serial	2 kb	256 x 8	I2C	400	900	2.7	5.5	40	3	-40	85	TSSOP-8

For more information please contact your local sales support at www.onsemi.com.

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