NCV7451

System Basis Chip with CAN FD, LDO Regulator and HS Driver

Product Overview

For complete documentation, see the data sheet.

The system basis chip (SBC) NCV7451 integrates +5 V / 250 mA LDO regulator with a highspeed CAN FD transceiver and local wakeup comparator, directly controlled by dedicated pins.

Features

- 5 V ±2% / 250 mA LDO Current Limitation with foldback Output Voltage Monitoring
- One High-Speed CAN FD Transceiver Compliant to ISO11898-2:2016 CAN FD Timing Specified up to 5 Mbps Current Limitation, Reverse Current Protected TxDC Timeout
- Local Wake-up Comparator Integrated Pull-up / Pull-down Current Source
- Low Current Quiescent Consumption
- Window Watchdog
- Direct Control
- Thermal Shutdown Protection
- Wettable Flank Package for Enhanced Optical Inspection
- AEC-Q100 Qualified and PPAP Capable

Benefits

- High output current;
Reduced system consumption in case of LDO output short

Applications

- Automotive
- Industrial Networks

End Products

- Car

Part Electrical Specifications

Product	Pricing ($/Unit)	Compliance	Status	Data Transmission Standard	Data Rate	Number of Drivers	Number of Receivers	V_{CC} Min (V)	V_{CC} Max (V)	t_{PLH} Max (µs)	I_O Max (µA)	I_{IH} Max (mA)	Package Type
NCV7451MW0R2G	0.5364	A P Pb H	Active	CAN	5 Mbps	1	1	6	18				DFNW-14