

Product Overview

ESD8024: LVDS ESD Protection Array

For complete documentation, see the data sheet.

The ESD8024 is designed specifically to protect Low Voltage Differential Signals (LVDS) for LCD panels. Low capacitance and low ESD clamping voltage make this device an ideal solution for protecting voltage sensitive data lines. The integrated 24 lines of protection offers a simplified solution with premier performance for LVDS applications.

Features

- Space efficient 24pin package integrated protection solution
- Low clamping voltage of 3V at 10A 8/20 μ s

Benefits

- PCB space saving, reduced BOM
- Protects chipsets with small geometries used in LVDS applications

Applications

- LVDS

End Products

- LCD Panel TCON

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Interface	Number of Lines	Direction	C Max (pF)	V _(BR) Min (V)	V _{RWM} Max (V)	I _R Max (μ A)	P _{PK} Max (W)	Package Type
ESD8024MNTAG	0.55	Pb-free Halide free non AEC-Q and PPAP	Active	Display Port General I/O TCON LVDS	24	Unidirectional	2 4	5.5	2.5	0.5	-	QFN-24

For more information please contact your local sales support at www.onsemi.com.

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