

## Product Overview

### NB3N5573: Clock Generator, Crystal to 25 MHz, 100 MHz, 125 MHz, 200 MHz, 3.3 V, with Dual HCSL

For complete documentation, see the data sheet.

The NB3N5573 is a high precision, low phase noise clock generator that supports PCI Express and Ethernet requirements. The device is part of the PureEdge™ product family and takes a 25 MHz fundamental mode parallel resonant crystal and generates differential HCSL output at 25 MHz, 100 MHz, 125 MHz or 200 MHz clock frequencies.

#### Features

- Typical Period RMS jitter of 1.5 ps
- HCSL Differential Output
- Uses 25 MHz Fundamental Mode Parallel Resonant Crystal
- Industrial Temperature Range -40C to +85C
- Operating Range 3.3 V +/- 10%
- PCIe Gen1, Gen2, Gen3, Gen4, QPI, UPI Jitter Compliant

#### Benefits

- Best in Class Jitter Performance

#### Applications

- Gigabit Ethernet
- FB DIMM
- PCIe Clock Generation Gen 1, Gen 2, Gen 3, Gen 4

#### End Products

- Servers
- Networking Equipment
- Set Top Box

### Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Input Level	Output Level	V <sub>S</sub> Typ (V)	f <sub>in</sub> Typ (MHz)	f <sub>out</sub> Typ (MHz)	t <sub>jitter</sub> (Cy-Cy) Typ (ps)	t <sub>jitter</sub> (Period) Typ (ps)	t <sub>jitter</sub> (Φ) Typ (ps)	t <sub>R</sub> & t <sub>F</sub> Typ (ps)	t <sub>R</sub> & t <sub>F</sub> Max (ps)	T <sub>A</sub> Min (°C)	T <sub>A</sub> Max (°C)	Package Type
NB3N5573DTG		Pb-free Halide free	Active	Crystal	HCSL	3.3	25	25-200	2	1.5	0.4	340	700	-40	85	TSSOP-16
				LVC MOS		2.97										
				Crystal		3.63										
NB3N5573DTR2G		Pb-free Halide free	Active	LVC MOS	HCSL	2.97	25	25-200	2	1.5	0.4	340	700	-40	85	TSSOP-16
				LVC MOS		3.3										
				Crystal		3.63										

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