

## Product Overview

### MC100LVELT23: Translator, Dual Differential LVPECL to LVTTTL

For complete documentation, see the data sheet.

The MC100LVELT23 is a dual differential LVPECL to LVTTTL translator. Because LVPECL (Positive ECL) levels are used only +3.3V and ground are required. The small outline 8-lead SOIC package and the dual gate design of the LVELT23 makes it ideal for applications which require the translation of a clock and a data signal. The LVELT23 is available in only the ECL 100K standard. Since there are no LVPECL outputs or an external VBB reference, the LVELT23 does not require both ECL standard versions. The LVPECL inputs are differential; there is no specified difference between the differential input 10H and 100K standards. Therefore, the MC100LVELT23 can accept any standard differential LVPECL input referenced from a VCC of 3.3V.

### Features

- 2.0ns Typical Propagation Delay
- Maximum Frequency > 180 MHz
- Differential LVPECL Inputs
- PECL Mode Operating Range: VCC= 3.0 V to 3.8 V with GND= 0 V
- 24 mA LVTTTL Outputs
- Flow Through Pinouts
- Internal Pulldown Resistors
- Q Output will default LOW with inputs open or at GND
- ESD Protection: >1.5 KV HBM, >100 V MM
- Meets or Exceeds JEDEC Spec EIA/JESD78 IC Latchup Test

For more features, see the data sheet

### Part Electrical Specifications

Product	Compliance	Status	Channels	Input Level	Output Level	V <sub>CC</sub> Typ (V)	f <sub>Max</sub> Typ (MHz)	t <sub>pd</sub> Typ (ns)	t <sub>r</sub> & t <sub>f</sub> Max (ps)	Package Type
MC100LVELT23DG	Pb-free	Active	2	ECL	TTL	3.3	180	1.7	900	SOIC-8
	Halide free									
MC100LVELT23DR2G	Pb-free	Active	2	ECL	TTL	3.3	180	1.7	900	SOIC-8
	Halide free									
MC100LVELT23DTG	Pb-free	Active	2	ECL	TTL	3.3	180	1.7	900	TSSOP-8
	Halide free									
MC100LVELT23DTRG	Pb-free	Active	2	ECL	TTL	3.3	180	1.7	900	TSSOP-8
	Halide free									
MC100LVELT23MNR G	Pb-free	Active	2	ECL	TTL	3.3	180	1.7	900	DFN-8
	Halide free									

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