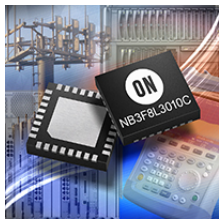
Product Overview



NB3F8L3010C: Clock / Data Fanout Buffer, 3:1:10 Differential, LVCMOS, 3.3 V / 2.5 V / 1.8 V / 1.5 V

For complete documentation, see the data sheet.

The NB3F8L3010C is a 3:1:10 Clock or Data fanout buffer operating on a 3.3 V or 2.5 V Core VDD and a flexible 3.3 V or 2.5 V or 1.8 V or 1.5 V VDDO supply which must be equal or less than VDD.

Features

- Ten CMOS / LVTTL Outputs up to 200 MHz
- Differential Inputs Accept LVPECL, LVDS, HCSL, or SSTL
- Crystal Oscillator Interface
- Crystal Input Frequency Range: 10 MHz to 40 MHz
- Output Skew: 10 ps Typical
- Additive RMS Phase Jitter @ 125 MHz, (12 kHz 20 MHz): 0.03 ps(Typical)
- Synchronous Output Enable
- Output Defined Level When Input is Floating
- Pure 3.3 V or 2.5 V Operating Mode, 3.3 V Core with 2.5 V/1.8 V/1.5 V Output Supply; or 2.5 V Core with 1.8 V/1.5 V Output Supply.

Applications

- Wireless and Wired Infrastructure
- Networking and Data Communications
- High-End Computing
- Clock Distribution

End Products

- Ethernet Switches / Routers
- Servers
- Test and Measurement
- ATE

Part Electrical Specifications

Product	Pricing ($/Unit)	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V_{CC} Typ (V)	t_{Jitter} RMS Typ (ps)	$t_{skew(o-o)}$ Max (ps)	t_{pd} Typ (ns)	t_R & t_F Max (ps)	f_{max} Clock Typ (MHz)	f_{max} Data Typ (Mbps)	Package Type
NB3F8L3010CMNG		Pb-free Halide free	Active	Buffer	1	3:1:10	LVDS HCSL LVCMOS LVTTL Crystal SSTL LVPECL	LVCMOS	3.3 2.5	0.27 0.13 0.18 0.08 0.14 0.19	50		500 600	100		QFN-32
NB3F8L3010CMNR4G		Pb-free Halide free	Active	Buffer	1	3:1:10	HCSL SSTL LVPECL LVDS LVCMOS LVTTL Crystal	LVCMOS	3.3 2.5	0.18 0.08 0.14 0.13 0.19 0.27	50		500 600	100		QFN-32
NB3F8L3010CMNTWG		Pb-free Halide free	Active													QFN-32

For more information please contact your local sales support at www.onsemi.com.

Created on: 4/5/2020