

Product Overview

MC10LVEP11: Clock / Data Fanout Buffer, 1:2 Differential, ECL, 2.5 V / 3.3 V

For complete documentation, see the data sheet.

The MC10LVEP11 is a differential 1:2 fanout buffer. The device is pin and functionally equivalent to the EP11 device. With AC performance the same as the EP11 device, the LVEP11 is ideal for applications requiring lower voltage. Single ended CLK input operation is limited to a VCC \geq 3.0 V in PECL mode, or VEE. The 100 Series contains temperature compensation.

Features

- 240 ps Typical Propagation Delay
- Maximum Frequency > 3 GHz Typical
- PECL Mode Operating Range: VCC= 2.375 V to 3.8 V with VEE= 0 V
- NECL Mode Operating Range: VCC= 0 V with VEE= -2.375 V to -3.8 V
- Open Input Default State
- Q Outputs will default LOW with inputs open or at VEE
- LVDS Input Compatible

Applications

- GbE or Fibre Channel Redundant Fan-out

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V _{CC} Typ (V)	t _{jitter} RMS Typ (ps)	t _{skew(c-to-c)} Max (ps)	t _{pd} Typ (ns)	t _p & t _f Max (ps)	f _{max} Clock Typ (MHz)	f _{max} Data Typ (Mbps)	Package Type
MC10LVEP11DR2G	Pb-free	Active	Buffer	1	1:2	ECL	ECL	3.3	1.42	20	0.24	170	3000		SOIC-8
	Halide free					CML		2.5							
						LVDS									
MC10LVEP11DTG	Pb-free Halide free	Active	Buffer	1	1:2	CML ECL LVDS	ECL	3.3 2.5	1.42	20	0.24	170	3000		TSSOP-8

For more information please contact your local sales support at www.onsemi.com.

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