

Product Overview

NB6N239S: 3.3 V Any Differential Clock to LVDS, $\div 1/2/4/8$ and $\div 2/4/8/16$ Clock Divider

For complete documentation, see the data sheet.

The NB6N239S is a high-speed, low skew clock divider with two divider circuits, each having selectable clock divide ratios; Div 1/2/4/8 and Div 2/4/8/16. Both divider circuits drive LVDS compatible outputs. The NB6N239S is a member of the ECLinPS MAXTM family of high performance clock products.

Features

- Maximum Clock Input Frequency, 3.0 GHz (1.5 GHz with Div 1)
 - Input Compatible with LVDS/LVPECL/CML/HSTL
 - 120ps Typical Rise/Fall Times
 - < 5 ps Typical Within Device Output Skew
 - Example; 622.08 MHz Input Generates 38.88 MHz to 622.08 MHz outputs
 - Internal 50Ω Termination Provided
 - < 2ps RMS Random Clock Jitter
 - Divide-by-1 Edge of QA Aligned to QB Divided Output
 - Operating Range: VCC = 3.0 V to 3.465V with GND = 0
 - Master Reset for Synchronization of Multiple Chips
- For more features, see the data sheet

Applications

- SONET/SDH Equipment Clocking

For more information please contact your local sales support at www.onsemi.com.

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