

## Product Overview

### NB6L239: 2.5 V / 3.3 V Any Differential Clock In to Differential LVPECL Out

For complete documentation, see the data sheet.

The NB6L239 is a high-speed, low skew clock divider with two divider circuits, each having selectable clock divide ratios; divide 1/2/4/8 and divide 2/4/8/16. Both divider circuits drive a pair of LVPECL outputs

#### Features

- Maximum Clock Input Frequency;  $\geq 3\text{GHz}$
- Input compatibility with LVDS/LVPECL/CML/HSTL
- 70 ps Typical Rise/Fall Times
- 5 ps Typical Output-to-Output Skew
- Ex. 622.08MHz Input Generates 38.88MHz to 622.08 MHz Outputs
- Internal 50  $\Omega$  Termination Provided
- Random Clock Jitter  $\leq 1$  ps RMS
- Divide-by-1 Edge of QA Aligned to QB divided Output
- Operating Range: VCC = 2.375 V to 3.465 V with VEE = 0 V
- Master Reset for Synchronization of Multiple Chips

For more features, see the data sheet

#### Applications

- Analog-to-Digital Converter Precision Timing
- SONET/SDH Reference Clock Division

For more information please contact your local sales support at [www.onsemi.com](http://www.onsemi.com).

Created on: 9/15/2019