

## Product Overview

### MC34072: Operational Amplifier, Single Supply 3.0 V to 44 V, Dual

For complete documentation, see the data sheet.

Quality bipolar fabrication with innovative design concepts are employed for the MC33071/72/74, MC34071/72/74, NCV33072/74A series of monolithic op-amps. These op-amps offer 4.5 MHz of gain bandwidth product, 13 V/ $\mu$ s slew rate and fast settling time without the use of JFET device technology. Although this series can be operated from split supplies, it is particularly suited for single supply operation, since the common mode input voltage range includes ground potential (VEE). With a Darlington input stage, this series exhibits high input resistance, low input offset voltage and high gain. The all NPN output stage, characterized by no deadband crossover distortion and large output voltage swing, provides high capacitance drive capability, excellent phase and gain margins, low open loop high frequency output impedance and symmetrical source/sink AC frequency response. The MC33071/72/74, MC34071/72/74, NCV33072/74, A series of devices are available in standard or prime performance (A Suffix) grades and are specified over the commercial, industrial/vehicular or military temperature ranges. The complete series of single, dual and quad operational amplifiers are available in plastic DIP, SOIC and TSSOP surface mount packages.

### Features

- Wide Bandwidth: 4.5 MHz
- High Slew Rate: 13 V/ $\mu$ s
- Fast Settling Time: 1.1  $\mu$ s to 0.1%
- Wide Single Supply Operation: 3.0 V to 44 V
- Wide Input Common Mode Voltage Range: Includes Ground (VEE)
- Low Input Offset Voltage: 3.0 mV Maximum (A Suffix)
- Large Output Voltage Swing: -14.7 V to +14 V (with +/-15 V Supplies)
- Large Capacitance Drive Capability: 0 pF to 10,000 pF
- Low Total Harmonic Distortion: 0.02%
- Excellent Phase Margin: 60°

For more features, see the data sheet

### Applications

- Instrumentation and Control

### Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Rail to Rail	Channels	V <sub>S</sub> Min (V)	V <sub>S</sub> Max (V)	I <sub>a</sub> Typ (mA)	V <sub>OS</sub> Max (mV)	GB W Typ (MHz)	SR Typ (V/ $\mu$ s)	I <sub>o</sub> Typ (mA)	$\Delta V_o$ s/ $\Delta T$ ( $\mu$ V/C)	e <sub>N</sub> (nV/ $\sqrt$ Hz)	I <sub>bias</sub> Typ (pA)	CM RR Typ (dB)	Architecture	Temperature Range (°C)	Package Type
MC34072DR2G	0.3032	Pb-free Halide free non AEC-Q and PPAP	Active	No	2	3	44	1.9	5	4.5	10	30	10	32	100000	97	Bipolar	0 to 70	SOIC-8
MC34072VDR2G	0.3141	AEC Qualified Pb-free Halide free non AEC-Q and PPAP	Active	No	2	3	44	1.9	5	4.5	10	30	10	32	100000	97	Bipolar	0 to 70	SOIC-8

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