

ESD Protection - In-Vehicle Networks

Automotive Qualified Low Capacitance High Speed Data Network Protection

SZESD9901

The SZESD9901 protects sensitive automotive electronics from ESD, Surge, and other harmful transient events. This device is designed for compliance to OPEN Alliance 100/1000 BASE-T1 Ethernet, and other high speed data networks. Device is suitable for ESD protection on the connector side of the transceiver PHY.

Features

- High Trigger Voltage ≥ 100 V
- X2DFN 1.0 x 0.6 mm Package
- Wettable Flank Package for Optimal Automated Optical Inspection (AOI)
- SZ Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- Open Alliance 100/1000 BASE-T1 Ethernet
- In Vehicle Networking (IVN)
- High Speed Data Networks

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Operating Junction Temperature Range	T_J	-55 to +150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to +150	$^\circ\text{C}$
Lead Solder Temperature – Maximum (10 Seconds)	T_L	260	$^\circ\text{C}$
IEC 61000-4-2 Contact (ESD) IEC 61000-4-2 Air (ESD) ISO 10605 150 pF / 330 Ω Contact* ISO 10605 330 pF / 330 Ω Contact ISO 10605 330 pF / 2 k Ω Contact ISO 10605 150 pF / 2 k Ω Contact * minimum number of discharges > 1000	ESD	± 30 ± 30 ± 30 ± 30 ± 30 ± 30	kV
Maximum Peak Pulse Current 8/20 μs	I_{pp}	2.2	A

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Complies with the Following Standards:

- ISO7637-2, Jumpstart, Load Dump
- Open Alliance 100/1000 BASE-T1 Ethernet
- ISO7637-3, Pulse 2a 85 V, 3a 3b 150 V



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MARKING DIAGRAM



X2DFNW2
CASE 711BG



DA = Specific Device Code
M = Date Code

PIN CONNECTIONS

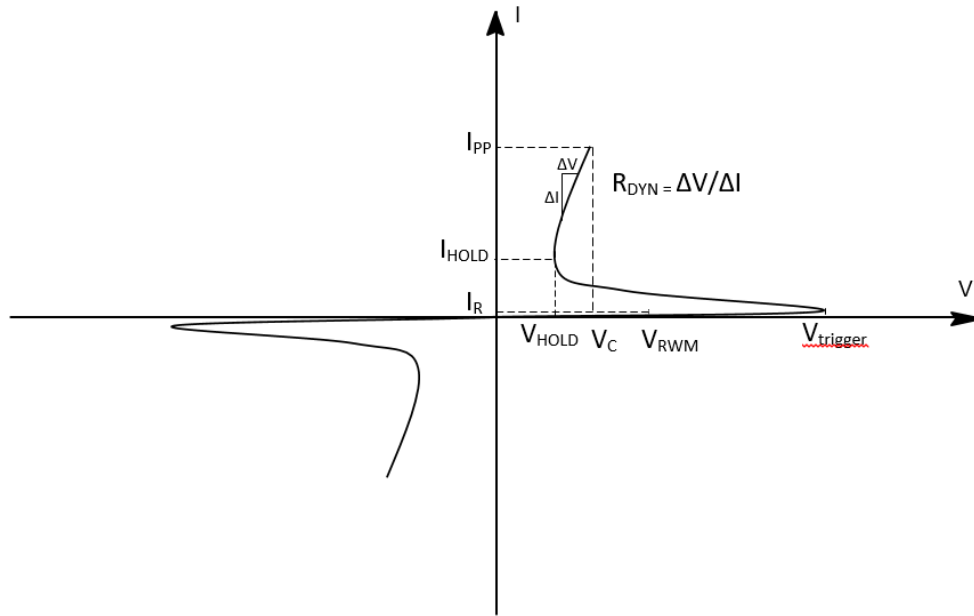


ORDERING INFORMATION

Device	Package	Shipping†
SZESD9901MX2WT5G	X2DFNW2 (Pb-Free)	8,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}	I/O Pin to GND			25	V
Reverse Leakage Current	I_R	$V_{RWM} = 25\text{ V}$		1	200	nA
Reverse Holding Voltage	V_{HOLD}	I/O Pin to GND	25			V
ESD Trigger Voltage (Note 2)	$V_{trigger}$		100			V
Reverse Peak Pulse Current	I_{PP}	per IEC61000-4-5 (8x20 μs)			2.2	A
Channel Capacitance	$C_{Jio-gnd}$	$V_R = 0\text{ V}$, $f = 1\text{ MHz}$		2.3	2.6	pF
Dynamic Resistance (Note 2)	R_{DYN}	I/O to GND, GND to I/O		0.4		Ω

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- For test procedure see Figure 6 and application note AND8307/D.
- ANSI/ESD STM5.5.1 – Electrostatic Discharge Sensitivity Testing using Transmission Line Pulse (TLP) Model.
TLP conditions: $Z_0 = 50\ \Omega$, $t_p = 100\text{ ns}$, $t_r = 1\text{ ns}$, averaging window: $t_1 = 70\text{ ns}$ to $t_2 = 90\text{ ns}$.

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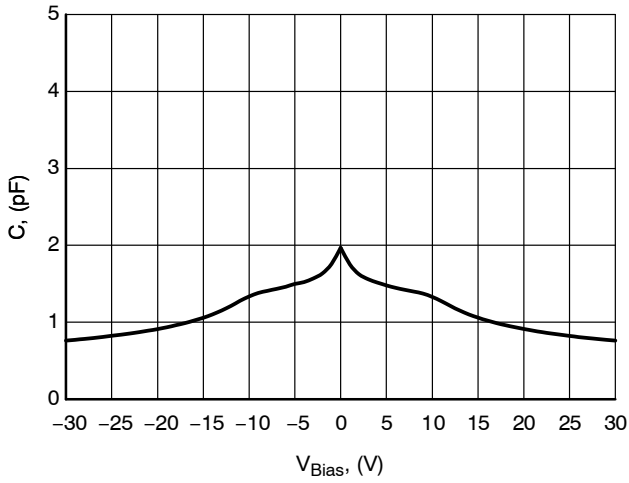


Figure 2. CV Characteristics

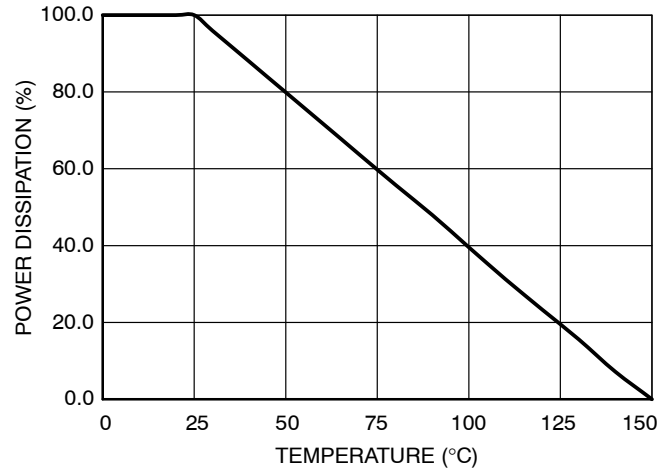


Figure 3. Steady State Power Derating

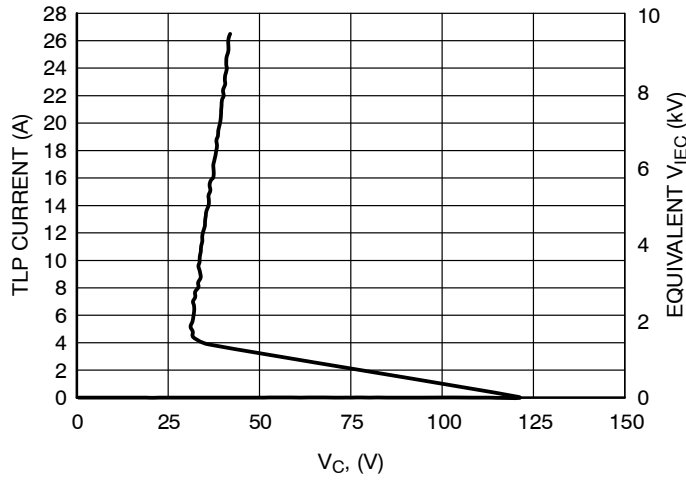


Figure 4. Positive TLP IV Curve

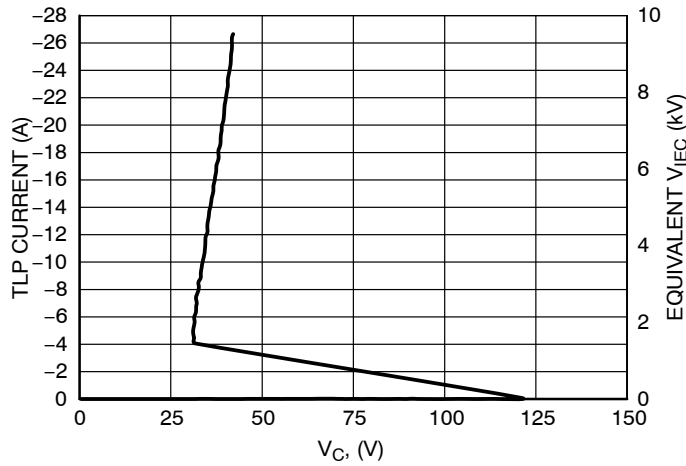


Figure 5. Negative TLP IV Curve

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IEC 61000-4-2 Spec.

Level	Test Voltage (kV)	First Peak Current (A)	Current at 30 ns (A)	Current at 60 ns (A)
1	2	7.5	4	2
2	4	15	8	4
3	6	22.5	12	6
4	8	30	16	8

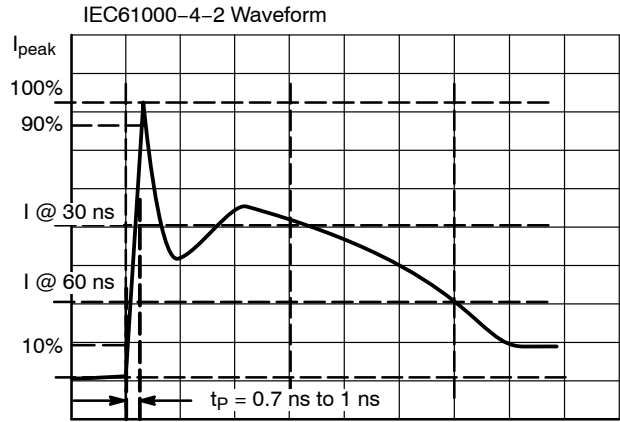


Figure 6. IEC61000-4-2 Spec

Transmission Line Pulse (TLP) Measurement

Transmission Line Pulse (TLP) provides current versus voltage (I-V) curves in which each data point is obtained from a 100 ns long rectangular pulse from a charged transmission line. A simplified schematic of a typical TLP system is shown in Figure 7. TLP I-V curves of ESD protection devices accurately demonstrate the product's ESD capability because the 10s of amps current levels and under 100 ns time scale match those of an ESD event. This is illustrated in Figure 8 where an 8 kV IEC 61000-4-2 current waveform is compared with TLP current pulses at 8 A and 16 A. A TLP I-V curve shows the voltage at which the device turns on as well as how well the device clamps voltage over a range of current levels.

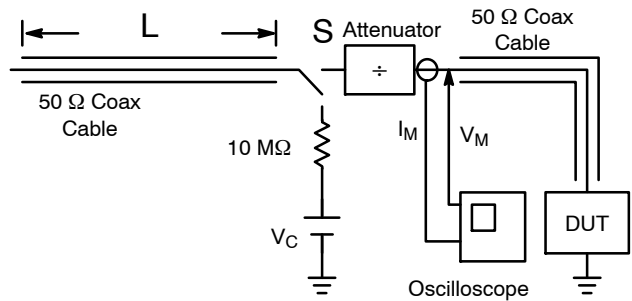


Figure 7. Simplified Schematic of a Typical TLP System

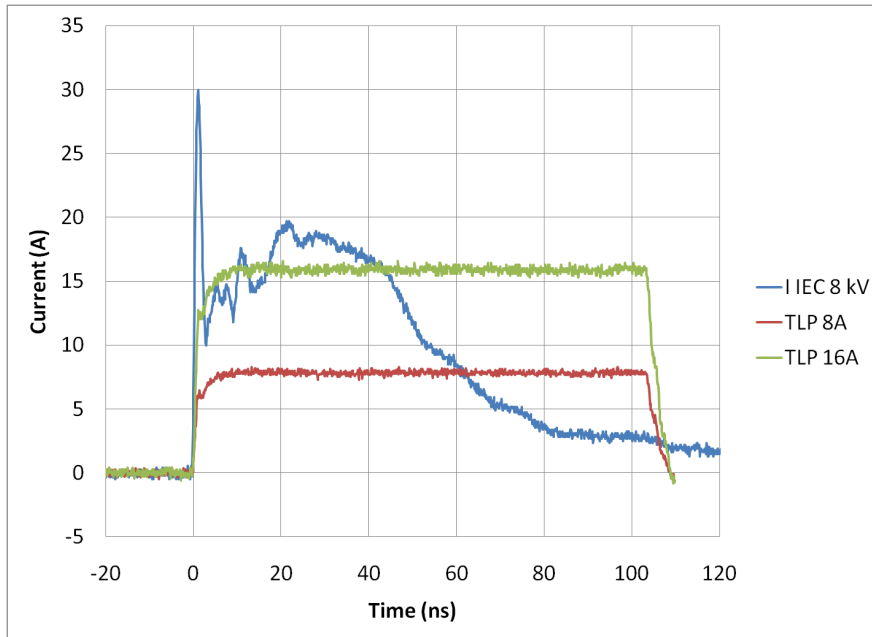
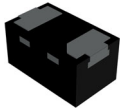


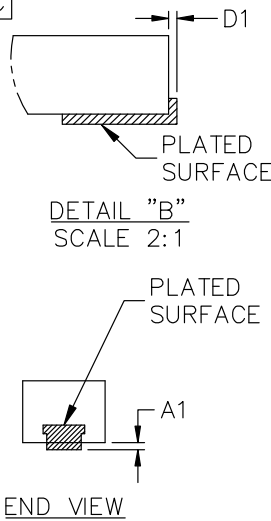
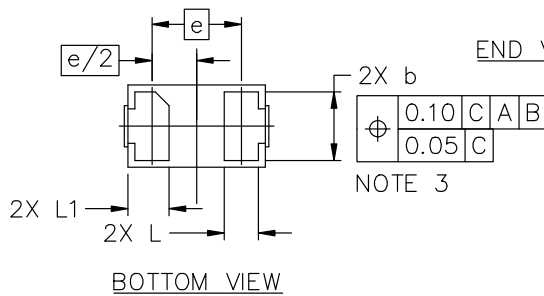
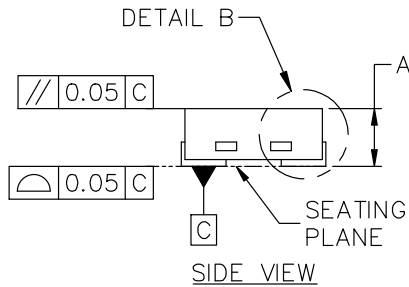
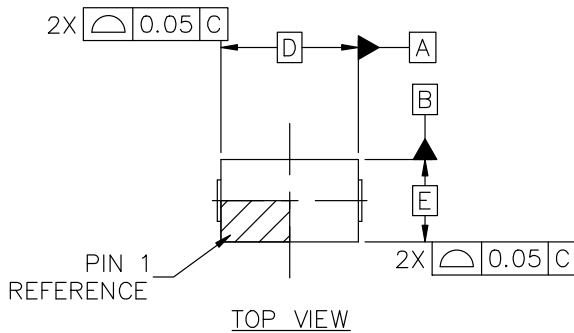
Figure 8. Comparison Between 8 kV IEC 61000-4-2 and 8 A and 16 A TLP Waveforms

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

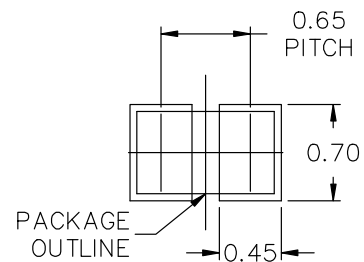


X2DFNW2 1.00x0.60x0.37, 0.65P
CASE 711BG
ISSUE D

DATE 29 FEB 2024



DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.34	0.37	0.40
A1	---	---	0.05
b	0.45	0.50	0.55
D	1.00 BSC		
D1	---	---	0.05
E	0.60 BSC		
e	0.65 BSC		
L	0.22 REF		
L1	0.24	0.28	0.34



RECOMMENDED MOUNTING FOOTPRINT*

* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



XX = Specific Device Code
M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	X2DFNW2 1.00x0.60x0.37, 0.65P	PAGE 1 OF 1

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