ON Semiconductor

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MOSFET – Power, N-Channel, SUPERFET[®] III, FRFET[®] 650 V, 40 A, 82 mΩ

NVH082N65S3F

Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET is very suitable for the various power system for miniaturization and higher efficiency.

SUPERFET III FRFET MOSFET's optimized reverse recovery performance of body diode can remove additional component and improve system reliability.

Features

- 700 V @ T_J = 150°C
- Typ. $R_{DS(on)} = 64 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 81 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 722 pF)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable

Applications

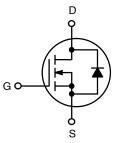
- Automotive On Board Charger HEV-EV
- Automotive DC/DC converter for HEV-EV



ON Semiconductor®

www.onsemi.com

V _{DSS}	R _{DS(ON)} MAX	I _D MAX
650 V	82 mΩ @ 10 V	40 A



POWER MOSFET



TO-247-3LD CASE 340CK

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Data Code (Year & Week)

&K = Lot

NVH082N65S3F = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$, Unless otherwise noted)

Symbol	Parameter	Value	Unit	
V _{DSS}	Drain to Source Voltage	age		V
V_{GSS}	Gate to Source Voltage	- DC		V
		- AC (f > 1 Hz)	±30	
I _D	I_D Drain Current – Continuous ($T_C = 25^{\circ}C$)		40	Α
		- Continuous (T _C = 100°C)	25.5	
I _{DM}	Drain Current	Single Pulsed Avalanche Energy (Note 2)		Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)			mJ
E _{AR}	Repetitive Avalanche Energy (Note 1)			mJ
dv/dt	MOSFET dv/dt		100	V/ns
	Peak Diode Recovery dv/dt (Note 3)	dt (Note 3)		
P_{D}	Power Dissipation $(T_C = 25^{\circ}C)$		313	W
		- Derate Above 25°C	2.5	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T_L	Maximum Lead Temperature for Soldering, 1/8"	300	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse–width limited by maximum junction temperature.

2. $I_{AS} = 4.8 \text{ A}$, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}C$.

3. $I_{SD} \le 20 \text{ A}$, $d/dt \le 200 \text{ A}/\mu s$, $V_{DD} \le 400 \text{ V}$, starting $T_J = 25^{\circ}C$.

THERMAL CHARACTERISTICS

Symbol	Symbol Parameter		Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	0.4	°C/W
$R_{ heta JA}$	R ₀ JA Thermal Resistance, Junction to Ambient, Max.		

PACKAGE MARKING AND ORDERING INFORMATION

	Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
Ī	NVH082N65S3F	NVH082N65S3F	TO-247	Tube	N/A	N/A	30 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS		-	-	<u>-</u>	-
BV _{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 25^{\circ}\text{C}$	650	_	_	V
		V _{GS} = 0 V, I _D = 10 mA, T _J = 150°C	700	1 –	_	V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 20 mA, Referenced to 25°C	-	0.7	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	-	1 –	10	μΑ
		V _{DS} = 520 V, T _C = 125°C	-	127	_	
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	-	_	±100	nA
ON CHARACTE	RISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1 \text{ mA}$	3.0	_	5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 20 A	-	64	82	mΩ
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 20 A	-	24	_	S
DYNAMIC CHAI	RACTERISTICS					
C _{iss}	Input Capacitance	V _{DS} = 400 V, V _{GS} = 0 V, f = 1 MHz	-	3410	_	pF
C _{oss}	Output Capacitance		_	70	_	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	-	722	_	pF
C _{oss(er.)}	Energy Related Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	-	126	_	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 400 V, I _D = 20 A, V _{GS} = 10 V	-	81	_	nC
Q_{gs}	Gate to Source Gate Charge	(Note 4)	-	24	_	nC
$Q_{\sf gd}$	Gate to Drain "Miller" Charge		-	32	_	nC
ESR	Equivalent Series Resistance	f = 1 MHz	-	1.9	_	Ω
SWITCHING CH	IARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 400 V, I _D = 20 A,	-	31	_	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_g = 4.7 \Omega$ (Note 4)	-	29	_	ns
t _{d(off)}	Turn-Off Delay Time		_	76	_	ns
t _f	Turn-Off Fall Time		-	16	_	ns
SOURCE-DRAII	N DIODE CHARACTERISTICS	•	-			•
I _S	Maximum Continuous Source to Drain Diode Forward Current		-	_	40	Α
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current		-	_	100	Α
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 20 A	-	_	1.3	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 20 A,	-	108	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/μs	_	410	_	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL CHARACTERISTICS

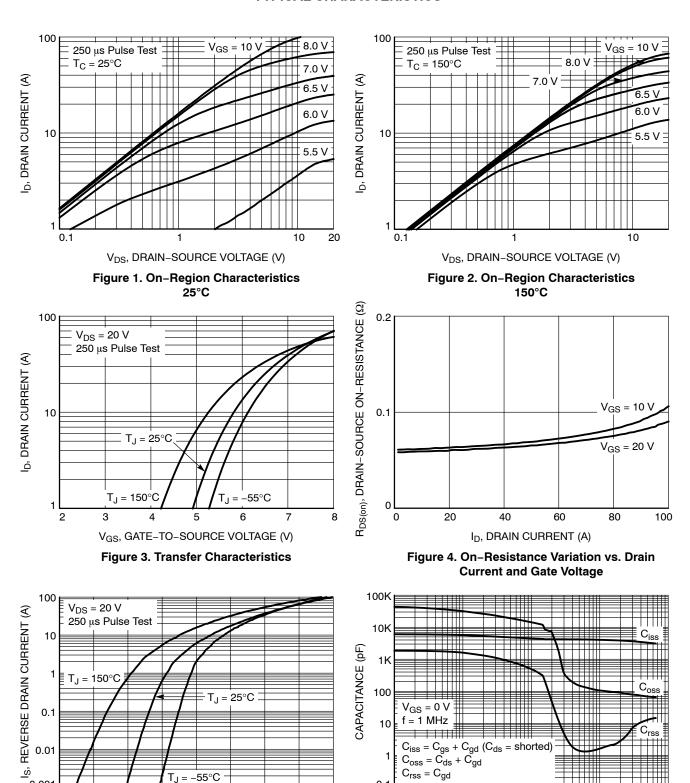


Figure 5. Body Diode Forward Voltage Variation vs. Source Current and Temperature

0.8

V_{SD}, BODY DIODE FORWARD VOLTAGE (V)

1.2

0

0.4

V_{DS}, DRAIN-TO-SOURCE VOLTAGE (V)

Figure 6. Capacitance Characteristics

10

100

1K

1.6

0.1

TYPICAL CHARACTERISTICS

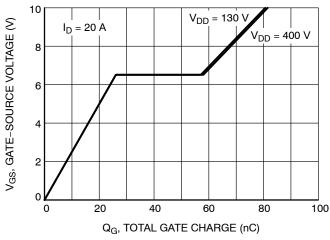


Figure 7. Gate Charge Characteristics

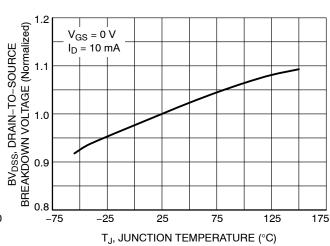


Figure 8. Breakdown Voltage Variation vs. Temperature

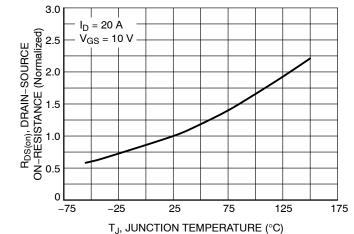


Figure 9. On-Resistance Variation vs. **Temperature**

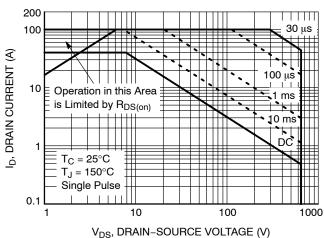


Figure 10. Maximum Safe Operating Area

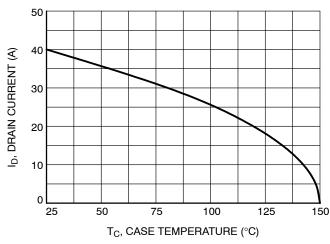


Figure 11. Maximum Drain Current vs. Case **Temperature**

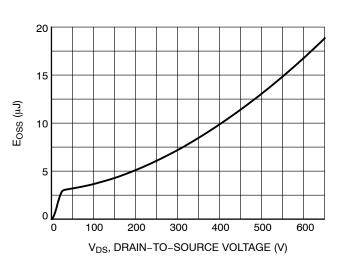
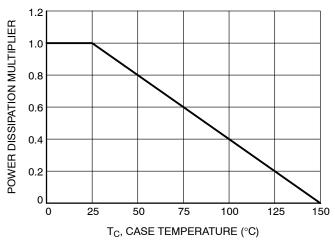


Figure 12. E_{OSS} vs. Drain-to-Source Voltage

TYPICAL CHARACTERISTICS

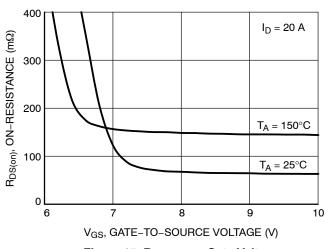
1000



100 Current Limited Max 100 A Current Limited Max 100 A t, RECTANGULAR PULSE (s)

Figure 13. Normalized Power Dissipation vs. Case Temperature

Figure 14. Peak Current Capability



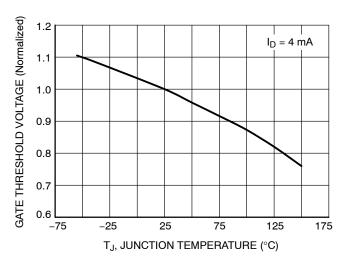
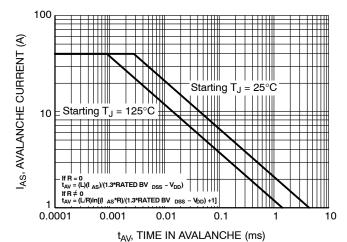


Figure 15. R_{DS(on)} vs. Gate Voltage

Figure 16. Normalized Gate Threshold Voltage vs. Temperature



NOTE: Refer to Fairchild Application Notes AN7514 and AN7515

Figure 17. Unclamped Inductive Switching Capability

TYPICAL CHARACTERISTICS

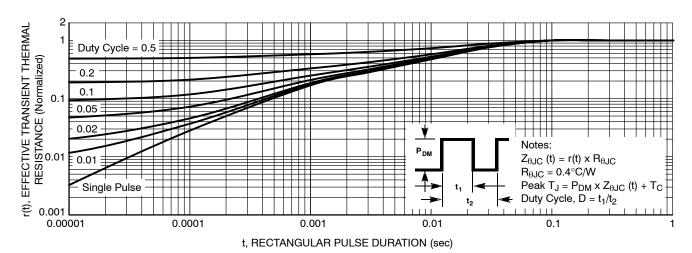


Figure 18. Transient Thermal Response

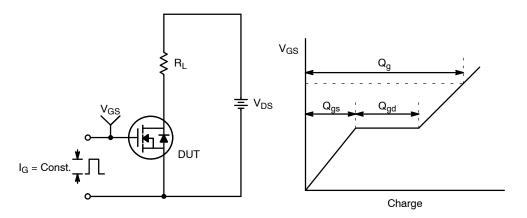


Figure 19. Gate Charge Test Circuit & Waveform

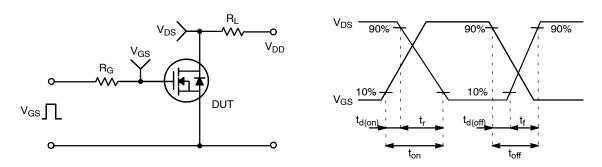


Figure 20. Resistive Switching Test Circuit & Waveforms

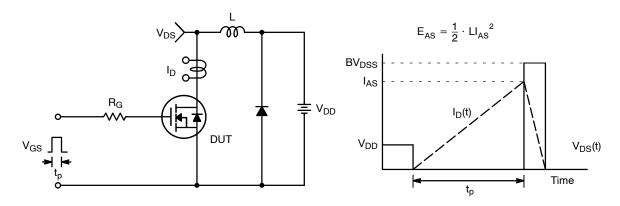


Figure 21. Unclamped Inductive Switching Test Circuit & Waveforms

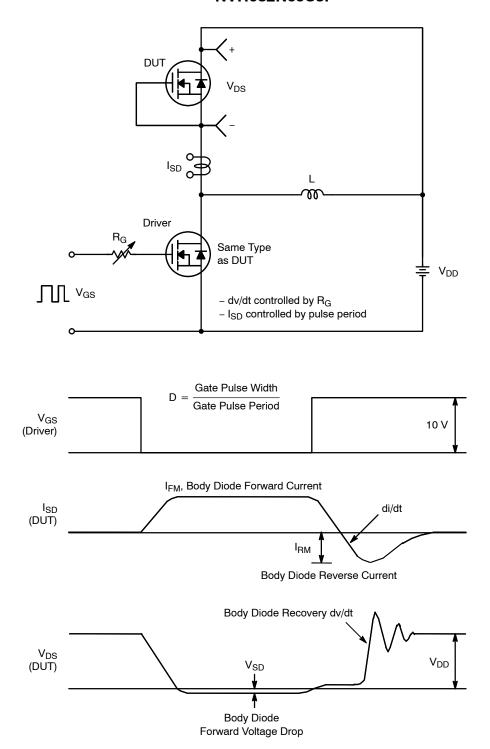
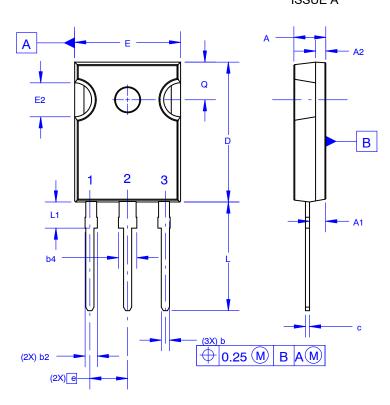


Figure 22. Peak Diode Recovery dv/dt Test Circuit & Waveforms

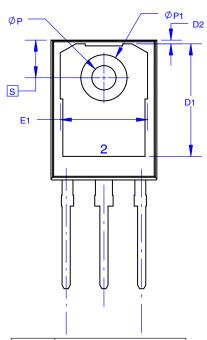
PACKAGE DIMENSIONS

TO-247-3LD SHORT LEAD CASE 340CK ISSUE A



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.



DIM	MILLIMETERS					
DIM	MIN NOM N		MAX			
Α	4.58	4.70	4.82			
A 1	2.20	2.40	2.60			
A2	1.40	1.50	1.60			
b	1.17	1.26	1.35			
b2	1.53	1.65	1.77			
b4	2.42	2.54	2.66			
С	0.51	0.61	0.71			
D	20.32	20.57	20.82			
D1	13.08	?	~			
D2	0.51	0.93	1.35			
E	15.37	15.62	15.87			
E1	12.81	?	~			
E2	4.96	5.08	5.20			
е	~	5.56	~			
L	15.75	16.00	16.25			
L1	3.69	3.81	3.93			
ØΡ	3.51	3.58	3.65			
ØP1	6.60	6.80	7.00			
Q S	5.34	5.46	5.58			
S	5.34	5.46	5.58			

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