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# **MOSFET** - Power, Single N-Channel, TOLL 80 V, 1.7 mΩ, 241.3 A

# **NVBLS1D7N08H**

#### **Features**

- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Q<sub>G</sub> and Capacitance to Minimize Driver Losses
- AEC-Q101 Qualified and PPAP Capable
- Lowers Switching Noise/EMI
- These Devices are Pb-Free and are RoHS Compliant

#### MAXIMUM RATINGS (T<sub>.I</sub> = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V <sub>DSS</sub>	80	٧
Gate-to-Source Voltage	Э		V <sub>GS</sub>	±20	٧
Continuous Drain		T <sub>C</sub> = 25°C	I <sub>D</sub>	241.3	Α
Current R <sub>θJC</sub> (Notes 1, 3)	Steady	T <sub>C</sub> = 100°C		170.6	
Power Dissipation	State	T <sub>C</sub> = 25°C	P <sub>D</sub>	237.5	W
R <sub>θJC</sub> (Note 1)		T <sub>C</sub> = 100°C	1	118.7	
Continuous Drain		T <sub>A</sub> = 25°C		33	Α
Current R <sub>0JA</sub> (Notes 1, 2, 3)	Steady	T <sub>A</sub> = 100°C		23.3	
Power Dissipation	State			4.4	W
R <sub>θJA</sub> (Notes 1, 2)		T <sub>A</sub> = 100°C		2.2	
Pulsed Drain Current	$T_A = 25^{\circ}C, t_p = 10 \mu s$		I <sub>DM</sub>	900	Α
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Source Current (Body Diode)			Is	197.9	Α
Single Pulse Drain-to-Source Avalanche Energy (I <sub>L(pk)</sub> = 21 A)			E <sub>AS</sub>	1172	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State (Note 1)	$R_{ heta JC}$	0.63	°C/W
Junction-to-Ambient - Steady State (Notes 1, 2)	$R_{\theta JA}$	33.8	

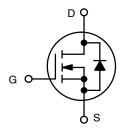
- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Surface-mounted on FR4 board using a 650 mm<sup>2</sup>, 2 oz. Cu pad.
- Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.



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V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX	
80 V	1.7 m $\Omega$ @ 10 V	241.3 A	



**N-CHANNEL MOSFET** 



TOLL CASE 100CU

#### **MARKING DIAGRAM**



A = Assembly Location Y = Year WW = Work Week ZZ = Lot Traceability 1D7N08H = Specific Device Code

#### **ORDERING INFORMATION**

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

# **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS					•		•
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		80			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /				57		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25°C			10	
		V <sub>DS</sub> = 80 V	T <sub>J</sub> = 125°C			250	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = 20 V				100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_D =$	= 479 μA	2.0	2.9	4.0	V
Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>	I <sub>D</sub> = 479 μA, ref	to 25°C		-7.3		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 80 A		1.29	1.7	mΩ
Forward Transconductance	9 <sub>FS</sub>	V <sub>DS</sub> =5 V, I <sub>D</sub>	= 80 A		271		S
CHARGES, CAPACITANCES & GATE RE	SISTANCE						
Input Capacitance	C <sub>ISS</sub>				7675		
Output Capacitance	C <sub>OSS</sub>	$V_{GS} = 0 \text{ V, f} = 1 \text{ MHz, V}_{DS} = 40 \text{ V}$			1059		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>				41		
Total Gate Charge	Q <sub>G(TOT)</sub>				121		
Threshold Gate Charge	Q <sub>G(TH)</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 40 V; I <sub>D</sub> = 80 A			19		nC
Gate-to-Source Charge	Q <sub>GS</sub>				32		
Gate-to-Drain Charge	$Q_{GD}$				29		
Plateau Voltage	$V_{GP}$				4.5		V
SWITCHING CHARACTERISTICS (Note 4	1)						
Turn-On Delay Time	t <sub>d(ON)</sub>				29		
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub>	s = 40 V,		25		1
Turn-Off Delay Time	t <sub>d(OFF)</sub>	$V_{GS}$ = 10 V, $V_{DS}$ = 40 V, $I_D$ = 80 A, $R_G$ = 6 $\Omega$			89		ns
Fall Time	t <sub>f</sub>				35		1
DRAIN-SOURCE DIODE CHARACTERIS	STICS						
Forward Diode Voltage	$V_{SD}$	$V_{GS} = 0 \text{ V},$ $I_{S} = 80 \text{ A}$ $T_{J} = 25^{\circ}\text{C}$ $T_{J} = 125^{\circ}\text{C}$			0.82	1.2	.,
				I <sub>S</sub> = 80 A T <sub>J</sub> =	T <sub>J</sub> = 125°C		0.69
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = 0 \text{ V, dIS/dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 43 \text{ A}$			73		ns
Reverse Recovery Charge	Q <sub>RR</sub>				138		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Switching characteristics are independent of operating junction temperatures.

#### TYPICAL CHARACTERISTICS

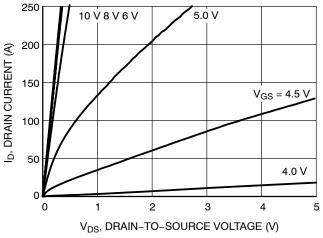
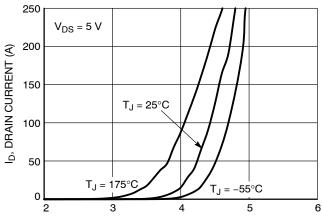


Figure 1. On-Region Characteristics



V<sub>GS</sub>, GATE-TO-SOURCE VOLTAGE (V)

Figure 2. Transfer Characteristics

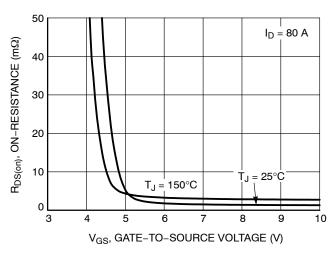


Figure 3. On-Resistance vs. Gate-to-Source Voltage

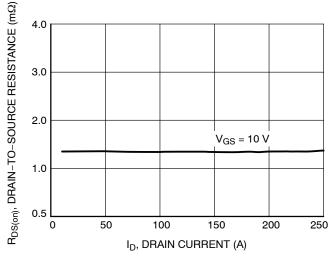


Figure 4. On-Resistance vs. Drain Current and **Gate Voltage** 

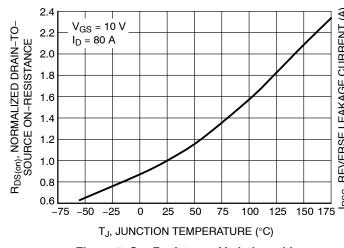


Figure 5. On-Resistance Variation with **Temperature** 

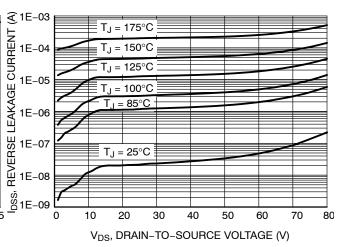


Figure 6. Drain-to-Source Leakage Current vs. Voltage

#### **TYPICAL CHARACTERISTICS**

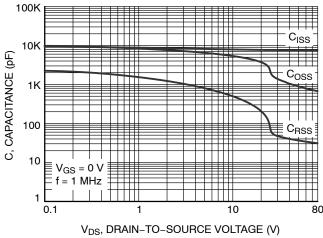


Figure 7. Capacitance Variation

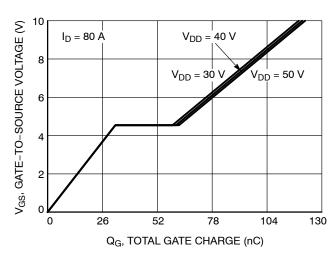


Figure 8. Gate-to-Source Voltage vs. Total Charge

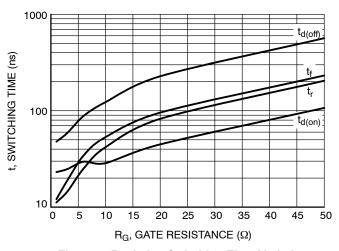


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

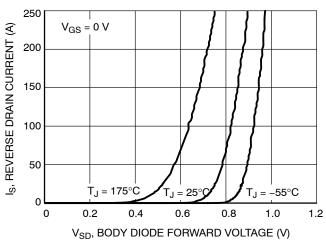


Figure 10. Diode Forward Voltage vs. Current

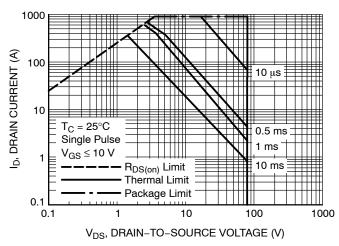


Figure 11. Maximum Rated Forward Biased Safe Operating Area

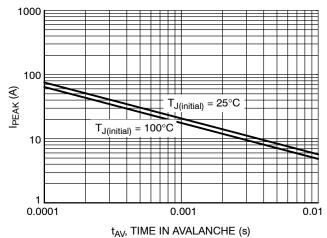


Figure 12. Maximum Drain Current vs. Time in Avalanche

#### **TYPICAL CHARACTERISTICS**

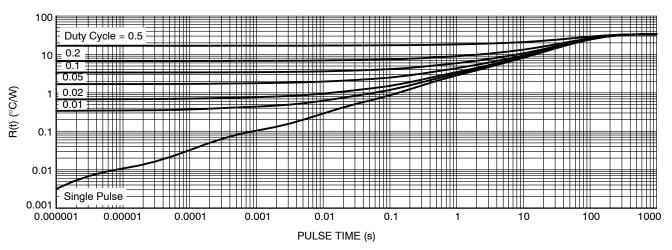


Figure 13. Transient Thermal Impedance

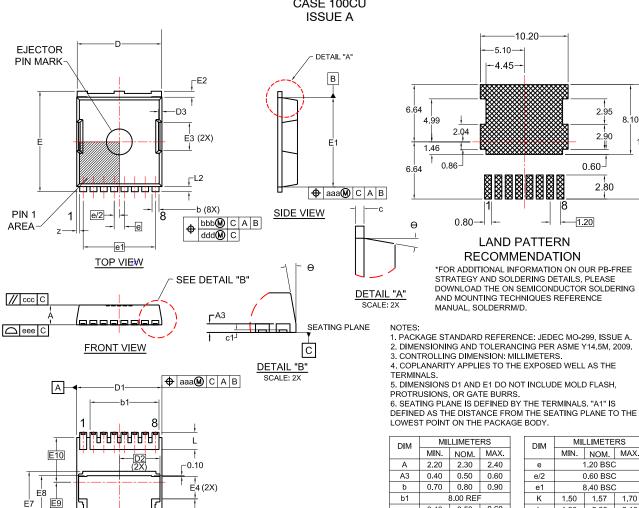
## **DEVICE ORDERING INFORMATION**

Device	Marking	Package	Shipping <sup>†</sup>
NVBLS1D7N08H	1D7N08H	H-PSOF8L (Pb-Free)	2000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### **PACKAGE DIMENSIONS**

# H-PSOF8L 11.68x9.80 CASE 100CU



E5 (2X)

-D7 **BOTTOM VIEW**  \_E6 (2X)

É7

DIM	MILLIMETERS			
5,,,,	MIN.	NOM.	MAX.	
Α	2.20	2.30	2.40	
A3	0.40	0.50	0.60	
b	0.70	0.80	0.90	
b1		8.00 REF	•	
С	0.40	0.50	0.60	
c1	0.10			
D	9.70	9.80	9.90	
D1	9.80	9.90	10.00	
D2		4.73 BSC	)	
D3	0.40 REF			
D4	3.75 BSC			
D5	_	1.20		
D6	7.40	7.50	7.60	
D7	(8.30)			
E	11.58	11.68	11.78	
E1	10.28	10.38	10.48	
E2	0.60	0.70	0.80	
E3	3.30 REF			
E4	_	2.60		

DIM	MILLIMETERS			
Divi	MIN.	NOM.	MAX.	
е	1.20 BSC			
e/2	(	0.60 BSC		
e1		3.40 BSC		
K	1.50	1.57	1.70	
L	1.90	2.00	2.10	
L2	0.50	0.60	0.70	
Z		0.35 REF	-	
θ	0°		12°	
aaa	0.20			
bbb	0.25			
ccc	0.20			
ddd	0.20			
eee	0.10			
E5		3.30	_	
E6		0.65	_	
E7	7.15 REF			
E8	6.55	6.65	6.75	
E9	5.89 BSC			
E10	5.19 BSC			

8.10

13.28

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