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MOSFET - Power, Single N-Channel, D²PAK7

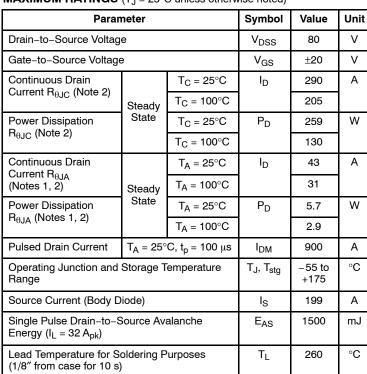
80 V, 1.34 mΩ, 290 A

NVBGS1D2N08H

Features

- Low R_{DS(on)} to Minimize Conduction Losses
- Low Q_G and Capacitance to Minimize Driver Losses
- AEC-Q101 Qualification
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T, I = 25°C unless otherwise noted)



Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State (Note 2)	$R_{\theta JC}$	0.58	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	26.2	

1. Surface-mounted on FR4 board using a 1 in² pad size, 2 oz. Cu pad.

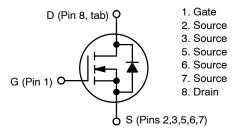
2. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.



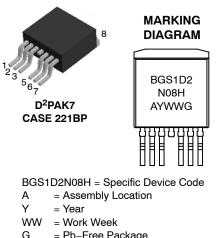
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V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
80 V	1.34 mΩ @ 10 V	290 A



N-CHANNEL MOSFET



⁼ Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
NVBGS1D2N08H	D ² PAK7 (Pb–Free)	800 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

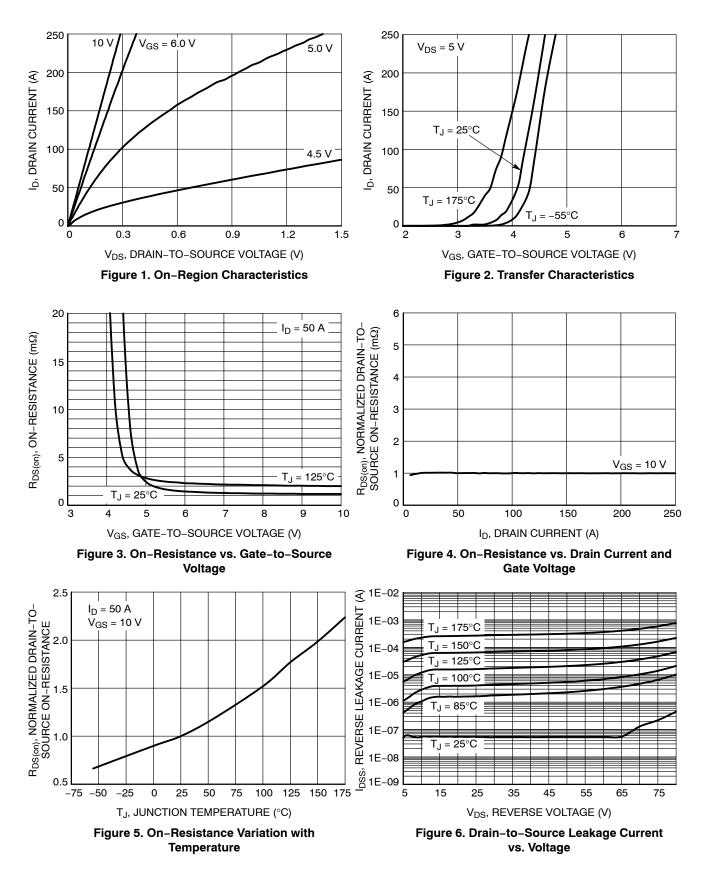
Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250 μA		80			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} / T _J	$I_D = 250 \ \mu A$, ref to $25^{\circ}C$			56		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V, T_{J} = 25^{\circ}C$	$T_J = 25^{\circ}C$			10	μA
	$V_{DS} = 80 V$ $T_J = 125^{\circ}C$	T _J = 125°C			250	μΑ	
Gate-to-Source Leakage Current	I _{GSS}	V_{DS} = 0 V, V_{GS}	= ±20 V			±100	nA
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = 650 μA		2.0	2.9	4.0	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J	I _D = 650 μA, re	f to 25°C		-7.5		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V, I _E	₀ = 50 A		1.1	1.34	mΩ
Forward Transconductance	9 _{FS}	$V_{DS} = 5 \text{ V}, \text{ I}_D$	= 50 A		213		S
Gate-Resistance	R _G				0.5		Ω
CHARGES, CAPACITANCES & GATE RESIS	TANCE						
Input Capacitance	C _{ISS}	V_{GS} = 0 V, V_{DS} = 40 V, f = 1 MHz			10830		pF
Output Capacitance	C _{OSS}				1605		
Reverse Transfer Capacitance	C _{RSS}				45		
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V _{DS} = 40 V; I _D = 50 A			160		nC
Threshold Gate Charge	Q _{G(TH)}				28		
Gate-to-Source Charge	Q _{GS}				43		
Gate-to-Drain Charge	Q _{GD}				33		
Plateau Voltage	V _{GP}				4.3		V
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	t _{d(ON)}	V_{GS} = 10 V, V_{DS} = 64 V, I_{D} = 50 A, R_{G} = 6 Ω			40		ns
Rise Time	t _r				34		-
Turn-Off Delay Time	t _{d(OFF)}				134		
Fall Time	t _f				45		
DRAIN-SOURCE DIODE CHARACTERISTIC	s				•	-	•
Forward Diode Voltage	V _{SD}	$V_{GS} = 0 V$, $T_J = 25^{\circ}C$			0.8	1.3	V
	lo = 50 A	T _J = 125°C		0.65		1	
Reverse Recovery Time	t _{RR}	V_{GS} = 0 V, dI _S /dt = 100 A/µs, I _S = 50 A			78		ns
Reverse Recovery Charge	Qpp				122		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 3. Pulse Test: pulse width \leq 300 μ s, duty cycle \leq 2%.

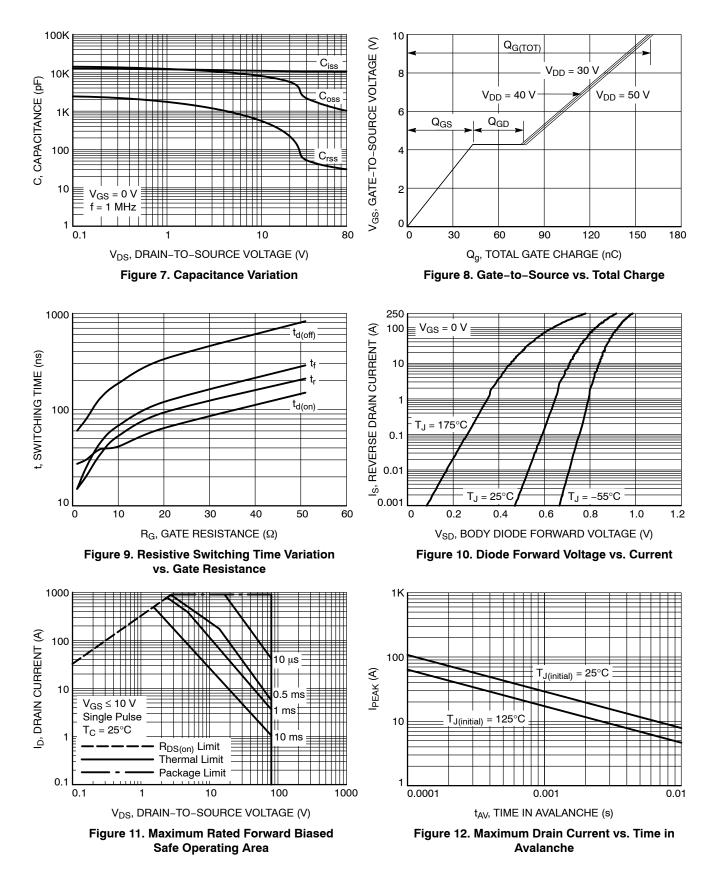
 $\mathsf{Q}_{\mathsf{R}\mathsf{R}}$

Reverse Recovery Charge

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

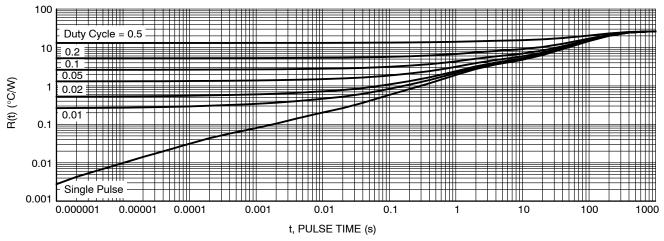
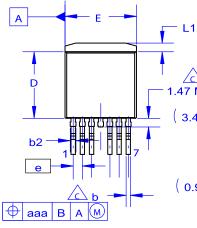
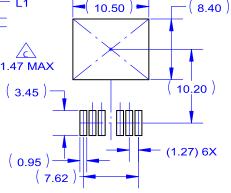


Figure 13. Transient Thermal Impedance

PACKAGE DIMENSIONS

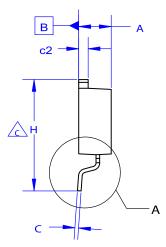
D2PAK7 (TO-263-7LD) 15.4x9.9x4.5 CASE 221BP **ISSUE A**





LAND PATTERN RECOMMENDATION

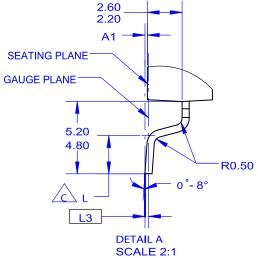
- E1 \bigcirc \square D1 8 T H 7 Ē



NOTES:

- A. PACKAGE CONFORMS TO JEDEC TO-263 VARIATION CB EXCEPT WHERE NOTED.
 B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C OUT OF JEDEC STANDARD VALUE. D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.
- Y14.5-2009.
 E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
 F. LAND PATTERN RECOMMENDATION PER IPC. TO127P1524X465-8N.

	MILLIMETERS			
DIM	MIN	NOM	MAX	
А	4.30	4.50	4.70	
A1	0.00	0.10	0.20	
b2	0.60	0.70	0.80	
b	0.50	0.60	0.70	
С	0.40	0.50	0.60	
c2	1.20	1.30	1.40	
D	9.00	9.20	9.40	
D1	7.30	7.80	8.20	
Е	9.70	9.90	10.20	
E1	7.15	8.05	8.55	
е	~	1.27	~	
Н	15.10	15.40	15.70	
L	2.44	2.64	2.84	
L1	1.00	1.20	1.40	
L3	~	0.25	~	
aaa	~	~	0.25	



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