

NTUD3127C

Small Signal MOSFET

20 V, 200 mA / -180 mA, Complementary,
1.0 x 1.0 mm SOT-963 Package



ON Semiconductor®

<http://onsemi.com>

Features

- Complementary MOSFET Device
- 1.5 V Gate Voltage Rating
- Ultra Thin Profile (< 0.5 mm) Allows It to Fit Easily into Extremely Thin Environments such as Portable Electronics.
- These are Pb-Free Devices

Applications

- Load Switch with Level Shift
- Optimized for Power Management in Ultra Portable Equipment

MAXIMUM RATINGS (T_J = 25°C unless otherwise specified)

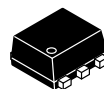
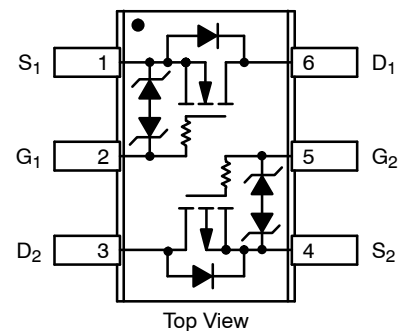
| Parameter | | | Symbol | Value | Unit |
|--|-----------------|------------------------|--------------------------------------|---------------|------|
| Drain-to-Source Voltage | | | V _{DSS} | 20 | V |
| Gate-to-Source Voltage | | | V _{GS} | ±8 | V |
| N-Channel Continuous Drain Current (Note 1) | Steady State | T _A = 25°C | I _D | 160 | mA |
| | | T _A = 85°C | | 115 | |
| | t ≤ 5 s | T _A = 25°C | | 200 | |
| P-Channel Continuous Drain Current (Note 1) | Steady State | T _A = 25°C | | -140 | |
| | | T _A = 85°C | | -100 | |
| | t ≤ 5 s | T _A = 25°C | | -180 | |
| Power Dissipation (Note 1) | Steady State | T _A = 25°C | P _D | 125 | mW |
| | | | | t ≤ 5 s | |
| Pulsed Drain Current | N-Channel | t _p = 10 μs | I _{DM} | 800 | mA |
| | P-Channel | | | -600 | |
| Operating Junction and Storage Temperature | | | T _J , T _{STG} | -55 to 150 | °C |
| Source Current (Body Diode) (Note 2) | | | I _S | 200 | mA |
| Lead Temperature for Soldering Purposes (1/8" from case for 10 s) | | | T _L | 260 | °C |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Surface-mounted on FR4 board using the minimum recommended pad size, 1 oz. Cu.
2. Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%

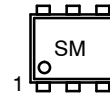
| V _{(BR)DSS} | R _{DS(on)} Max | I _D Max |
|----------------------|-------------------------|--------------------|
| P-Channel -20 V | 5.0 Ω @ -4.5 V | -0.18 A |
| | 7.0 Ω @ -2.5 V | |
| | 10 Ω @ -1.8 V | |
| | 14 Ω @ -1.5 V | |
| N-Channel 20 V | 3.0 Ω @ 4.5 V | 0.20 A |
| | 4.0 Ω @ 2.5 V | |
| | 6.0 Ω @ 1.8 V | |
| | 10 Ω @ 1.5 V | |

PINOUT: SOT-963



SOT-963
CASE 527AA

MARKING DIAGRAM



S = Specific Device Code
M = Date Code

ORDERING INFORMATION

| Device | Package | Shipping† |
|--------------|----------------------|--------------------|
| NTUD3127CT5G | SOT-963 (Pb-Free) | 8000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NTUD3127C

THERMAL RESISTANCE RATINGS

| Parameter | Symbol | Max | Unit |
|--|-----------------|------|------|
| Junction-to-Ambient – Steady State, Minimum Pad (Note 3) | $R_{\theta JA}$ | 1000 | °C/W |
| Junction-to-Ambient – $t \leq 5$ s (Note 3) | | 600 | |

3. Surface-mounted on FR4 board using the minimum recommended pad size, 1 oz. Cu.

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | N/P | Test Condition | Min | Typ | Max | Unit |
|-----------------------------------|---------------|-----|--|---------------------------|-----|------|------|
| OFF CHARACTERISTICS | | | | | | | |
| Drain-to-Source Breakdown Voltage | $V_{(BR)DSS}$ | N | $V_{GS} = 0\text{ V}$ | $I_D = 250\ \mu\text{A}$ | 20 | | V |
| | | P | | $I_D = -250\ \mu\text{A}$ | -20 | | |
| Zero Gate Voltage Drain Current | I_{DSS} | N | $V_{GS} = 0\text{ V}, V_{DS} = 5.0\text{ V}$ | $T_J = 25^\circ\text{C}$ | | 50 | nA |
| | | | | $T_J = 85^\circ\text{C}$ | | 200 | |
| | | P | | $T_J = 25^\circ\text{C}$ | | -50 | |
| | | | | $T_J = 85^\circ\text{C}$ | | -200 | |
| Zero Gate Voltage Drain Current | I_{DSS} | N | $V_{GS} = 0\text{ V}, V_{DS} = 16\text{ V}$ | $T_J = 25^\circ\text{C}$ | | 100 | nA |
| | | P | $V_{GS} = 0\text{ V}, V_{DS} = -16\text{ V}$ | | | -100 | |
| Gate-to-Source Leakage Current | I_{GSS} | N | $V_{DS} = 0\text{ V}, V_{GS} = \pm 5.0\text{ V}$ | | | 100 | nA |
| | | P | | | | -100 | |

ON CHARACTERISTICS (Note 4)

| | | | | | | | |
|-------------------------------|--------------|---|--|---------------------------|------|------|----------|
| Gate Threshold Voltage | $V_{GS(TH)}$ | N | $V_{GS} = V_{DS}$ | $I_D = 250\ \mu\text{A}$ | 0.4 | 1.0 | V |
| | | P | | $I_D = -250\ \mu\text{A}$ | -0.4 | -1.0 | |
| Drain-to-Source On Resistance | $R_{DS(on)}$ | N | $V_{GS} = 4.5\text{ V}, I_D = 100\text{ mA}$ | | 1.5 | 3.0 | Ω |
| | | P | $V_{GS} = -4.5\text{ V}, I_D = -100\text{ mA}$ | | 4.0 | 5.0 | |
| | | N | $V_{GS} = 2.5\text{ V}, I_D = 50\text{ mA}$ | | 2.0 | 4.0 | |
| | | P | $V_{GS} = -2.5\text{ V}, I_D = -50\text{ mA}$ | | 5.0 | 7.0 | |
| | | N | $V_{GS} = 1.8\text{ V}, I_D = 20\text{ mA}$ | | 3.0 | 6.0 | |
| | | P | $V_{GS} = -1.8\text{ V}, I_D = -20\text{ mA}$ | | 6.5 | 10 | |
| | | N | $V_{GS} = 1.5\text{ V}, I_D = 10\text{ mA}$ | | 4.0 | 10 | |
| | | P | $V_{GS} = -1.5\text{ V}, I_D = -10\text{ mA}$ | | 7.5 | 14 | |
| | | N | $V_{GS} = 1.2\text{ V}, I_D = 1.0\text{ mA}$ | | 5.5 | | |
| | | P | $V_{GS} = -1.2\text{ V}, I_D = -1.0\text{ mA}$ | | 11.5 | | |
| Forward Transconductance | g_{FS} | N | $V_{DS} = 5.0\text{ V}, I_D = 125\text{ mA}$ | | 0.35 | | S |
| | | P | $V_{DS} = -5.0\text{ V}, I_D = -125\text{ mA}$ | | 0.26 | | |

CHARGES, CAPACITANCES AND GATE RESISTANCE

| | | | | | | | |
|------------------------------|-----------|---|--|--|-----|--|----|
| Input Capacitance | C_{ISS} | N | $f = 1\text{ MHz}, V_{GS} = 0\text{ V}$ $V_{DS} = 15\text{ V}$ | | 9.0 | | pF |
| Output Capacitance | C_{OSS} | | | | 3.0 | | |
| Reverse Transfer Capacitance | C_{RSS} | | | | 2.2 | | |
| Input Capacitance | C_{ISS} | P | $f = 1\text{ MHz}, V_{GS} = 0\text{ V}$ $V_{DS} = -15\text{ V}$ | | 12 | | |
| Output Capacitance | C_{OSS} | | | | 2.7 | | |
| Reverse Transfer Capacitance | C_{RSS} | | | | 1.0 | | |

4. Switching characteristics are independent of operating junction temperatures

NTUD3127C

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | N/P | Test Condition | Min | Typ | Max | Unit |
|-----------|--------|-----|----------------|-----|-----|-----|------|
|-----------|--------|-----|----------------|-----|-----|-----|------|

SWITCHING CHARACTERISTICS, $V_{GS} = 4.5\text{ V}$ (Note 4)

| | | | | | | | |
|---------------------|--------------|---|--|--|-----|--|----|
| Turn-On Delay Time | $t_{d(ON)}$ | N | $V_{GS} = 4.5\text{ V}, V_{DD} = 10\text{ V}, I_D = 200\text{ mA}, R_G = 2.0\ \Omega$ | | 15 | | ns |
| Rise Time | t_r | | | | 24 | | |
| Turn-Off Delay Time | $t_{d(OFF)}$ | | | | 90 | | |
| Fall Time | t_f | | | | 60 | | |
| Turn-On Delay Time | $t_{d(ON)}$ | P | $V_{GS} = -4.5\text{ V}, V_{DD} = -15\text{ V}, I_D = -180\text{ mA}, R_G = 2.0\ \Omega$ | | 20 | | |
| Rise Time | t_r | | | | 37 | | |
| Turn-Off Delay Time | $t_{d(OFF)}$ | | | | 112 | | |
| Fall Time | t_f | | | | 97 | | |

DRAIN-SOURCE DIODE CHARACTERISTICS

| | | | | | | | | |
|-----------------------|----------|---|--|--------------------------|--|-------|------|---|
| Forward Diode Voltage | V_{SD} | N | $V_{GS} = 0\text{ V}, I_S = 10\text{ mA}$ | $T_J = 25^\circ\text{C}$ | | 0.60 | 1.0 | V |
| | | P | $V_{GS} = 0\text{ V}, I_S = -10\text{ mA}$ | | | -0.65 | -1.0 | |

4. Switching characteristics are independent of operating junction temperatures

TYPICAL PERFORMANCE CURVES – N-CHANNEL

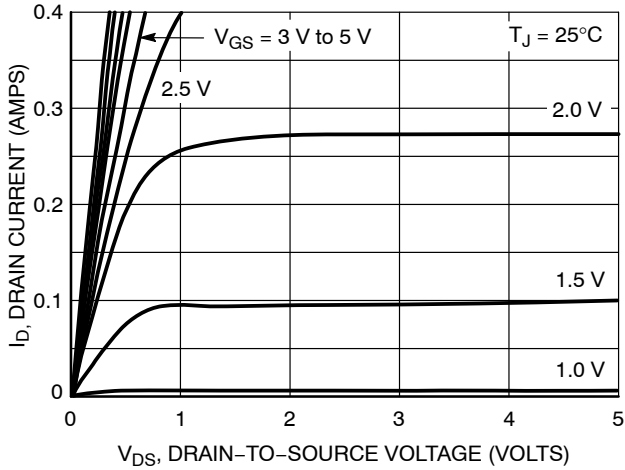


Figure 1. On-Region Characteristics

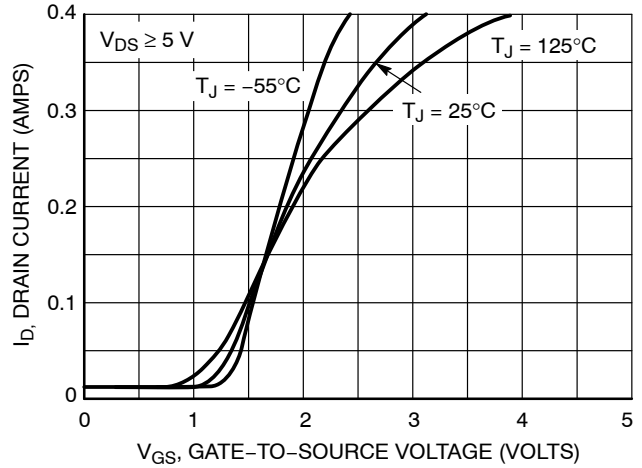


Figure 2. Transfer Characteristics

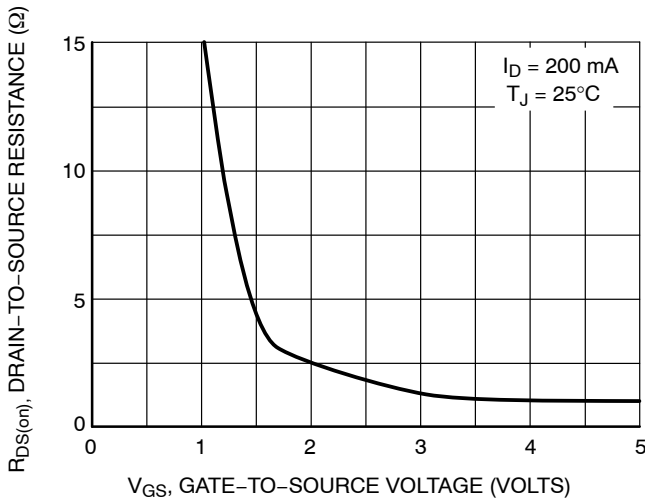


Figure 3. On-Resistance vs. Gate Voltage

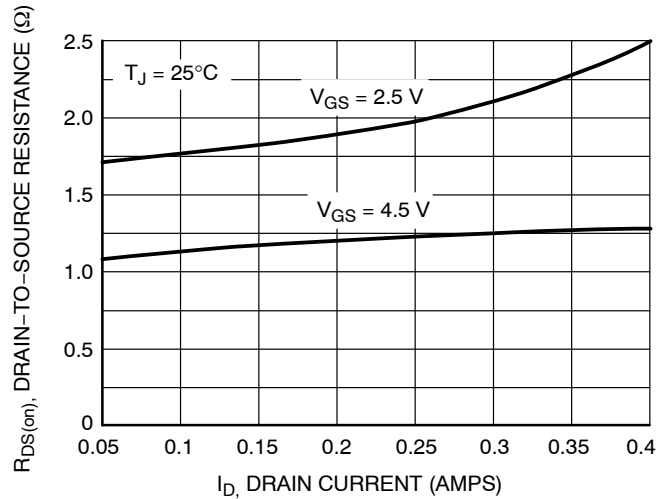


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

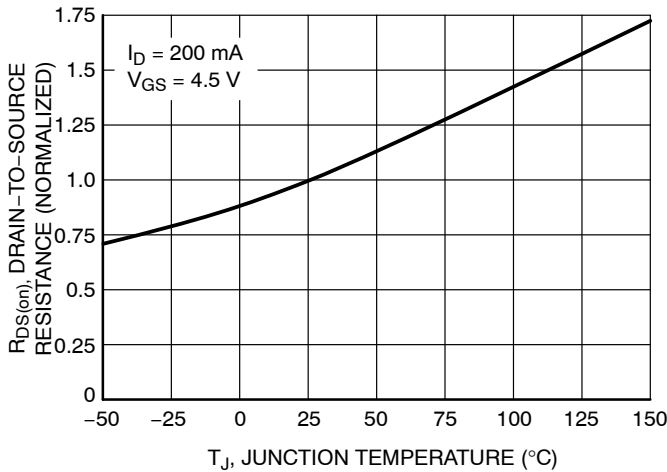


Figure 5. On-Resistance Variation with Temperature

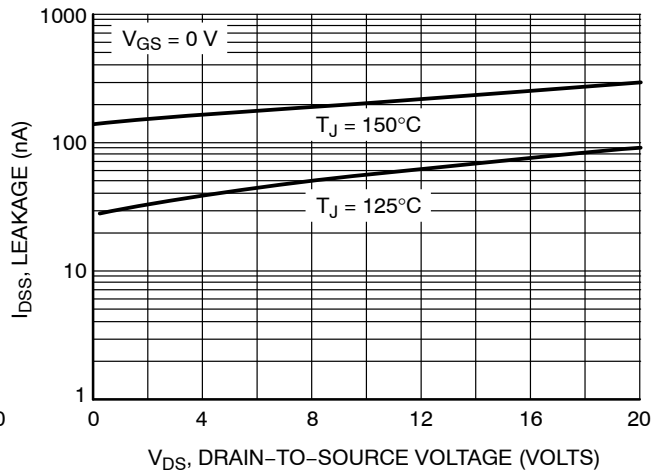


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL PERFORMANCE CURVES - N-CHANNEL

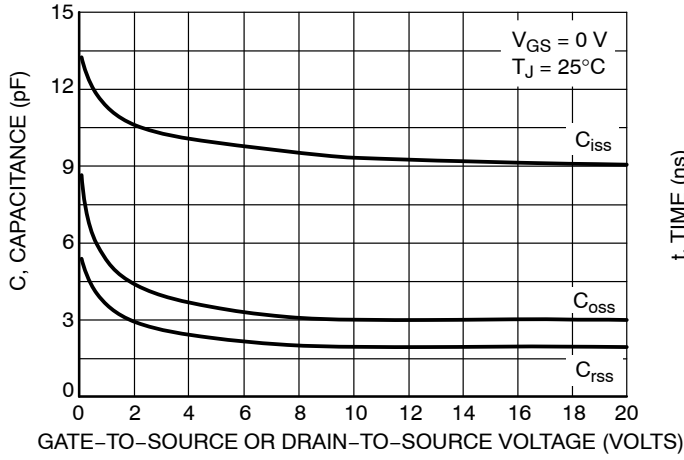


Figure 7. Capacitance Variation

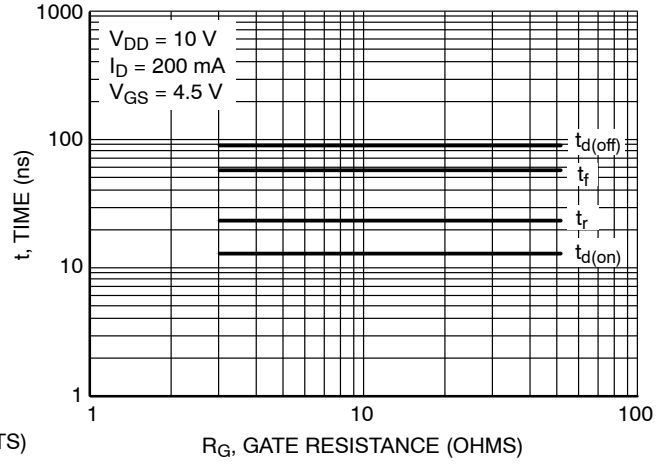


Figure 8. Resistive Switching Time Variation vs. Gate Resistance

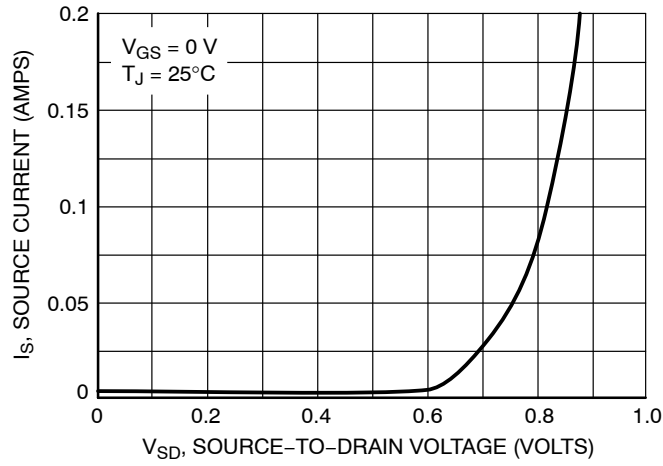


Figure 9. Diode Forward Voltage vs. Current

TYPICAL PERFORMANCE CURVES – P-CHANNEL

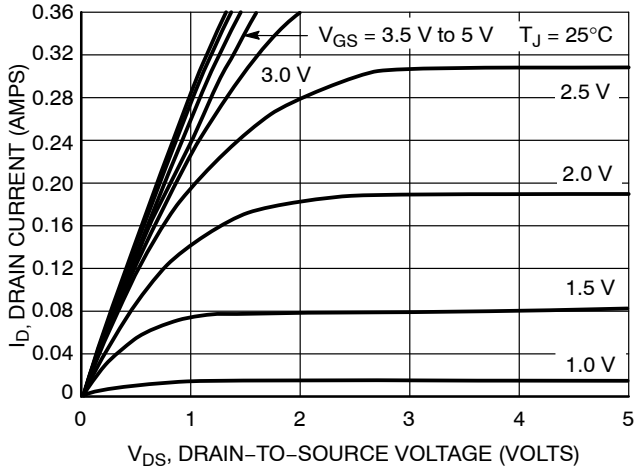


Figure 10. On-Region Characteristics

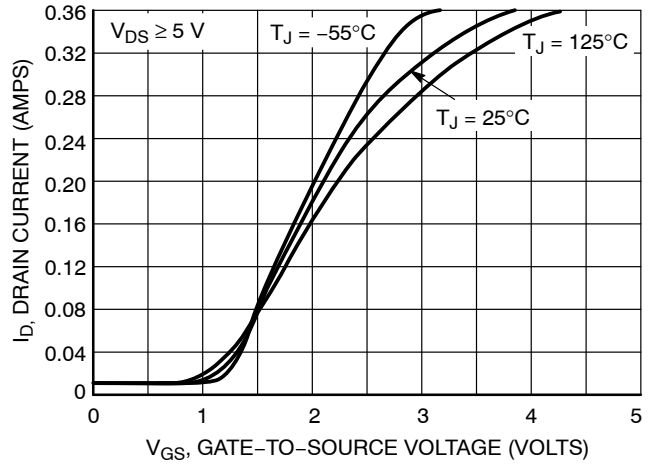


Figure 11. Transfer Characteristics

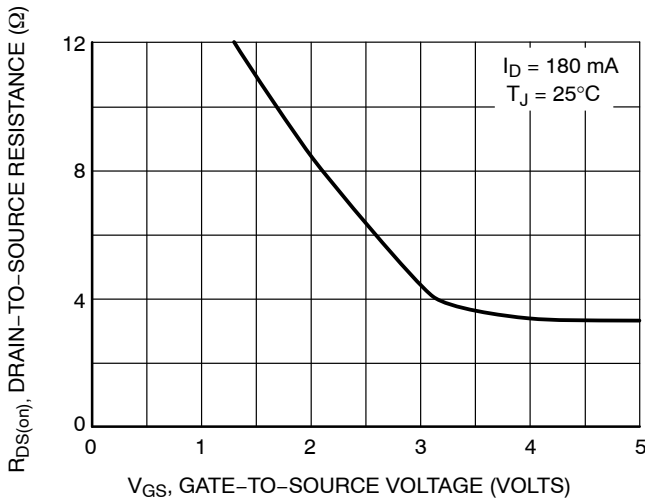


Figure 12. On-Resistance vs. Gate Voltage

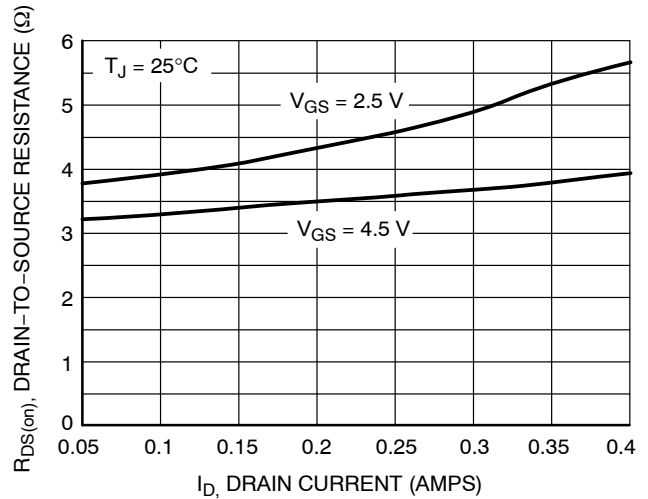


Figure 13. On-Resistance vs. Drain Current and Gate Voltage

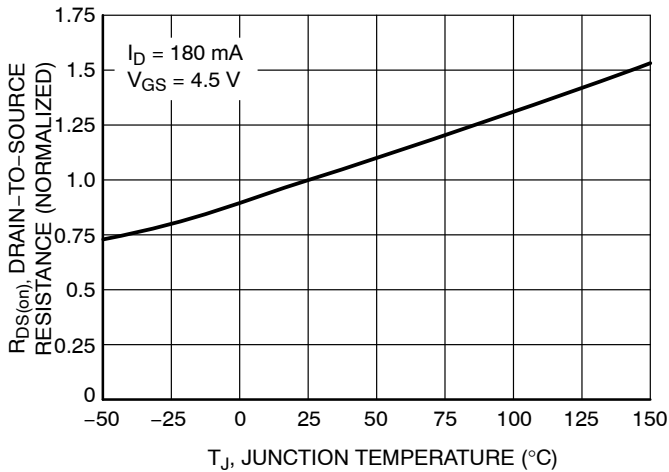


Figure 14. On-Resistance Variation with Temperature

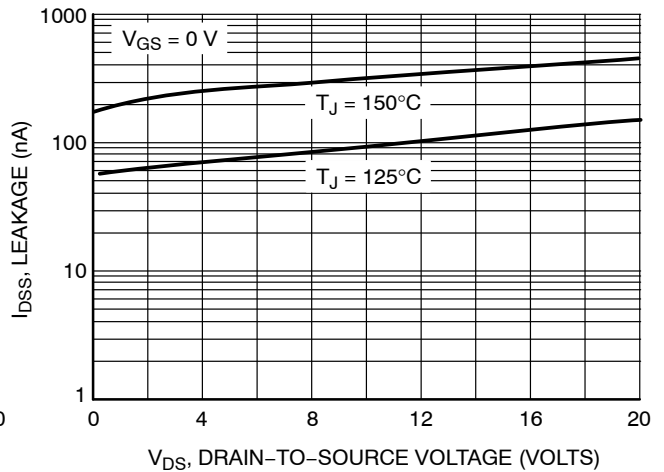


Figure 15. Drain-to-Source Leakage Current vs. Voltage

TYPICAL PERFORMANCE CURVES - P-CHANNEL

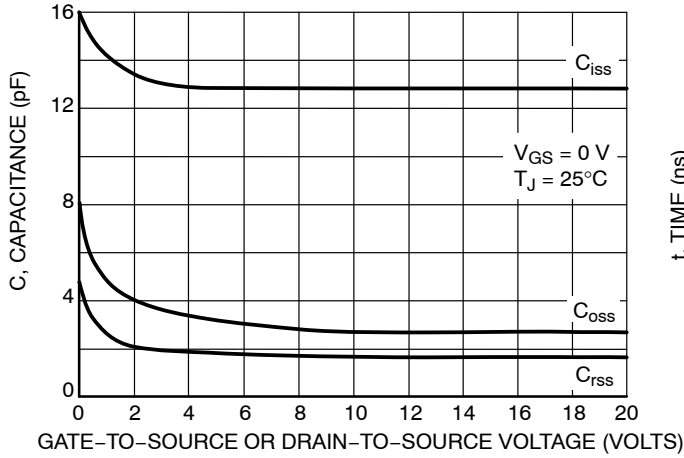


Figure 16. Capacitance Variation

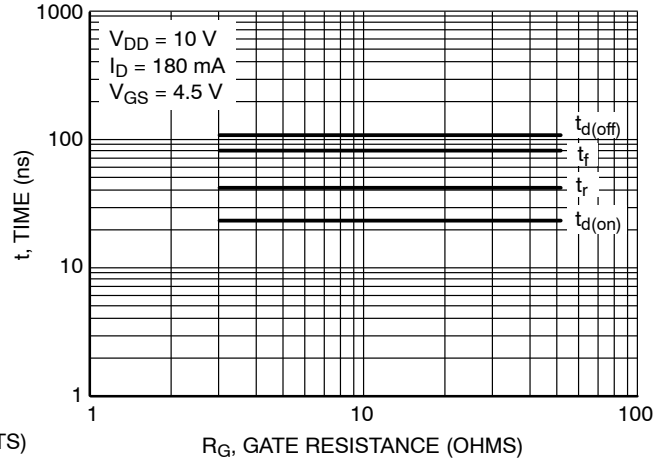


Figure 17. Resistive Switching Time Variation vs. Gate Resistance

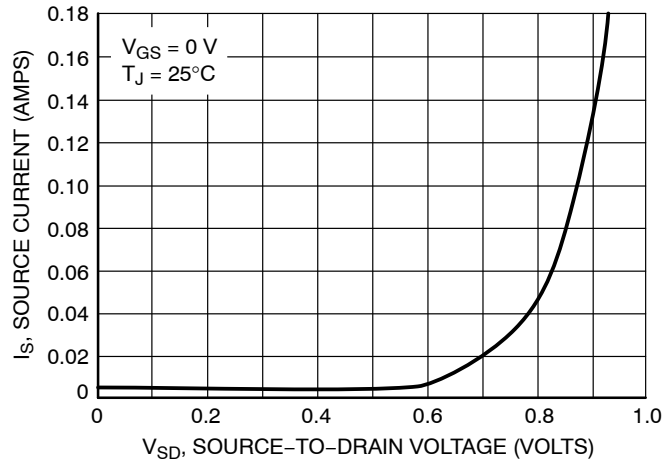


Figure 18. Diode Forward Voltage vs. Current

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

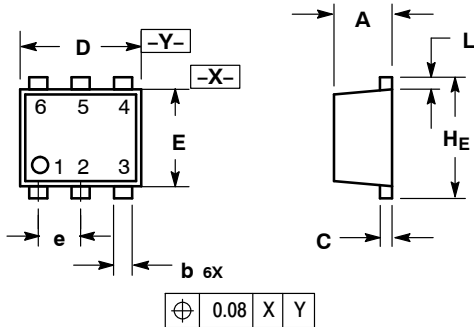
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SOT-963
CASE 527AA-01
ISSUE D

DATE 30 JUL 2008


SCALE 4:1



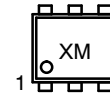
NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL

| DIM | MILLIMETERS | | | INCHES | | |
|----------------|-------------|------|------|-----------|-------|-------|
| | MIN | NOM | MAX | MIN | NOM | MAX |
| A | 0.40 | 0.45 | 0.50 | 0.016 | 0.018 | 0.020 |
| b | 0.10 | 0.15 | 0.20 | 0.004 | 0.006 | 0.008 |
| C | 0.05 | 0.10 | 0.15 | 0.002 | 0.004 | 0.006 |
| D | 0.95 | 1.00 | 1.05 | 0.037 | 0.039 | 0.041 |
| E | 0.75 | 0.80 | 0.85 | 0.03 | 0.032 | 0.034 |
| e | 0.35 BSC | | | 0.014 BSC | | |
| L | 0.05 | 0.10 | 0.15 | 0.002 | 0.004 | 0.006 |
| H _E | 0.95 | 1.00 | 1.05 | 0.037 | 0.039 | 0.041 |

- | | | |
|--|---|--|
| <p>STYLE 1: PIN 1. EMITTER 1 2. BASE 1 3. COLLECTOR 2 4. EMITTER 2 5. BASE 2 6. COLLECTOR 1</p> | <p>STYLE 2: PIN 1. EMITTER 1 2. EMITTER2 3. BASE 2 4. COLLECTOR 2 5. BASE 1 6. COLLECTOR 1</p> | <p>STYLE 3: PIN 1. CATHODE 1 2. CATHODE 1 3. ANODE/ANODE 2 4. CATHODE 2 5. CATHODE 2 6. ANODE/ANODE 1</p> |
| <p>STYLE 4: PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER 5. COLLECTOR 6. COLLECTOR</p> | <p>STYLE 5: PIN 1. CATHODE 2. CATHODE 3. ANODE 4. ANODE 5. CATHODE 6. CATHODE</p> | <p>STYLE 6: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE</p> |
| <p>STYLE 7: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. ANODE 6. CATHODE</p> | <p>STYLE 8: PIN 1. DRAIN 2. DRAIN 3. GATE 4. SOURCE 5. DRAIN 6. DRAIN</p> | <p>STYLE 9: PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1</p> |
| <p>STYLE 10: PIN 1. CATHODE 1 2. N/C 3. CATHODE 2 4. ANODE 2 5. N/C 6. ANODE 1</p> | | |

GENERIC MARKING DIAGRAM*

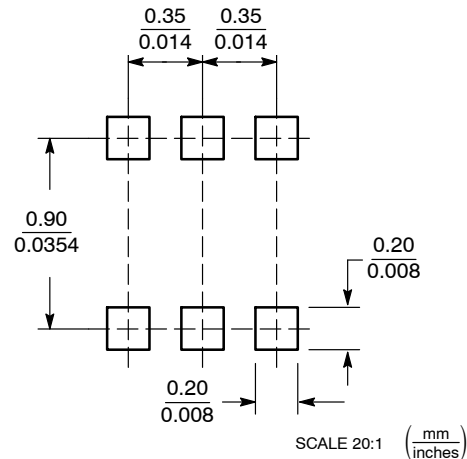


X = Specific Device Code
M = Month Code

*This information is generic. Please refer to device data sheet for actual part marking.


Pb-Free indicator, "G" or microdot "▪", may or may not be present.

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

| | | |
|-------------------------|----------------------------|--|
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| DESCRIPTION: | SOT-963, 1X1, 0.35P | PAGE 1 OF 1 |

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