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MOSFET – Power, Single N-Channel 100 V, 5.1 mΩ, 105 A

NTMFS005N10MCL

Features

- Small Footprint (5x6 mm) for Compact Design
- Low R_{DS(on)} to Minimize Conduction Losses
- Low Q_G and Capacitance to Minimize Driver Losses
- These Devices are Pb-Free, Halogen Free/BFR Free, Beryllium Free and are RoHS Compliant

Typical Applications

- Synchronous Rectification
- AC-DC and DC-DC Power Supplies
- AC-DC Adapters (USB PD) SR
- Load Switch

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-to-Source Voltage		V _{DSS}	100	V	
Gate-to-Source Voltage		V _{GS}	±20	V	
Continuous Drain Current R _{θJC} (Note 1)	Steady	T _C = 25°C	I _D	105	Α
Power Dissipation $R_{\theta JC}$ (Note 1)	State		P _D	125	W
Continuous Drain Current $R_{\theta JA}$ (Notes 1, 2)	Steady State	T _A = 25°C	I _D	16	Α
Power Dissipation R _{θJA} (Notes 1, 2)	Oldic		P _D	3	W
Pulsed Drain Current	$T_A = 25^{\circ}C, t_p = 100 \ \mu s$		I _{DM}	470	Α
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C	
Source Current (Body Diode)		Is	104	Α	
Single Pulse Drain-to-Source Avalanche Energy (L = 1 mH, I _{L(pk)} = 18.8 A)		E _{AS}	177	mJ	
Lead Temperature Soldering Reflow for Soldering Purposes (1/8" from case for 10 s)		TL	260	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State (Note 1)	$R_{\theta JC}$	1.2	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	50	

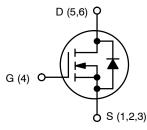
- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Surface-mounted on FR4 board using 1 in² pad size, 1 oz. Cu pad.



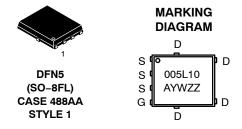
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V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
100 V	5.1 mΩ @ 10 V	105 A
100 V	7.1 mΩ @ 4.5 V	105 A



N-CHANNEL MOSFET



A = Assembly Location

Y = Year
W = Work Week
ZZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping†		
NTMFS005N10MCLT1G	DFN5 (Pb-Free)	1500 / Tape & Reel		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D =$	250 μΑ	100			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /	I _D = 250 μA, ref to 25°C			52		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 100 V	T _J = 25°C			1	μΑ
			T _J = 125°C			100	
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS}$; = 20 V			100	nA
ON CHARACTERISTICS	•				•		•
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D =$	= 192 μΑ	1		3	V
Threshold Temperature Coefficient	V _{GS(TH)} /T _J	I _D = 250 μA, ref to 25°C			-5.6		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 34 A			4.2	5.1	mΩ
		V _{GS} = 4.5 V, I _D = 27 A			5.6	7.1	
Forward Transconductance	9FS	V _{DS} = 10 V, I _D = 50 A			155		S
Gate-Resistance	R_{G}	T _A = 25°C			0.85		Ω
CHARGES & CAPACITANCES	•						
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 50 V			4100		pF
Output Capacitance	C _{OSS}				1350		-
Reverse Transfer Capacitance	C _{RSS}				22		-
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 4.5 V, V _{DS} = 50 V, I _D = 34 A			26		nC
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V _{DS} = 50 V, I _D = 34 A			55		nC
Gate-to-Source Charge	Q _{GS}				11		1
Gate-to-Drain Charge	Q_{GD}				5		1
Plateau Voltage	V_{GP}				3		V
Output Charge	Q _{OSS}	V _{GS} = 0 V, V _{DD} = 50 V			87		nC
SWITCHING CHARACTERISTICS (Note	3)						
Turn-On Delay Time	t _{d(ON)}	V _{GS} = 10 V, V _{DS}	_S = 50 V,		17		ns
Rise Time	t _r	I_D = 34 A, R_G = 6 Ω			6.7		- - -
Turn-Off Delay Time	t _{d(OFF)}				57		
Fall Time	t _f				12.3		
DRAIN-SOURCE DIODE CHARACTERI							
Forward Diode Voltage	V_{SD}	V _{GS} = 0 V, I _S = 34 A	T _J = 25°C		0.85	1.3	V
-			T _J = 125°C		0.73		1
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V, dI}_{S}/\text{dt} = 100 \text{ A}/\mu\text{s,}$			56		ns
Reverse Recovery Charge	Q _{RR}	I _S = 17 A	4		54		nC
Charge Time	ta				25		ns
Discharge Time	t _b				31		ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Switching characteristics are independent of operating junction temperatures

TYPICAL CHARACTERISTICS

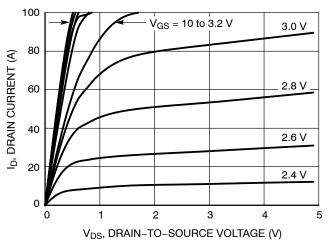


Figure 1. On-Region Characteristics

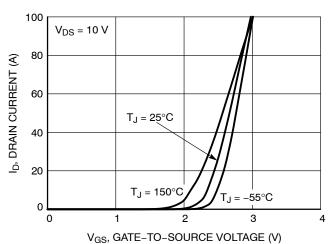


Figure 2. Transfer Characteristics

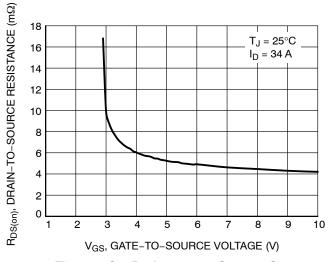


Figure 3. On-Resistance vs. Gate-to-Source Voltage

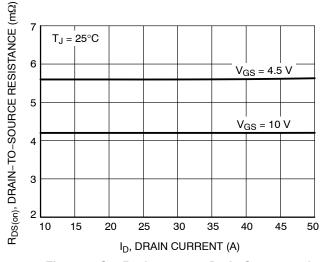


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

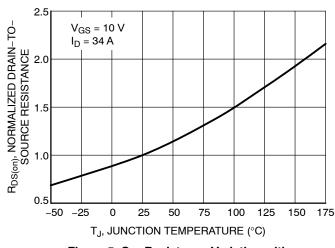


Figure 5. On–Resistance Variation with Temperature

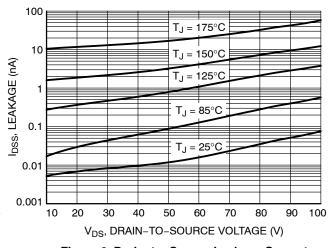


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL CHARACTERISTICS

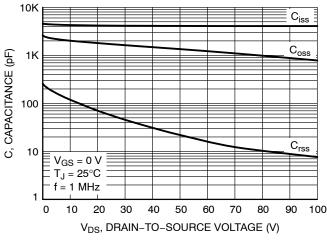


Figure 7. Capacitance Variation

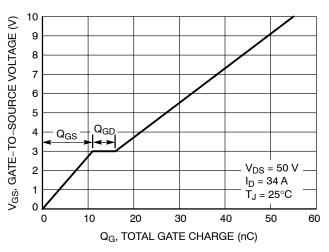


Figure 8. Gate-to-Source vs. Total Charge

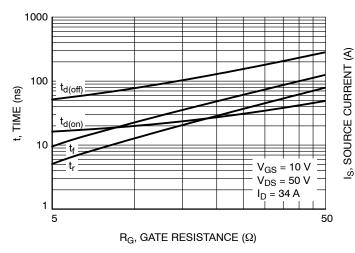


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

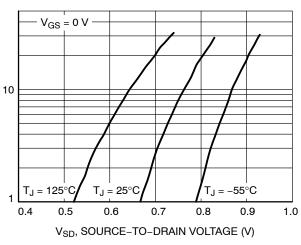


Figure 10. Diode Forward Voltage vs. Current

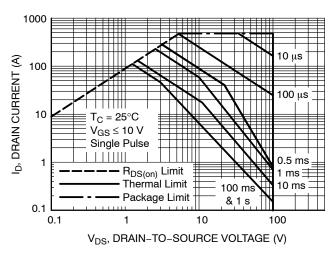


Figure 11. Maximum Rated Forward Biased Safe Operating Area

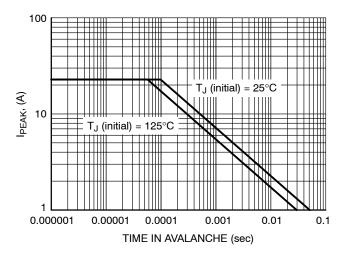


Figure 12. Maximum Drain Current vs. Time in Avalanche

TYPICAL CHARACTERISTICS

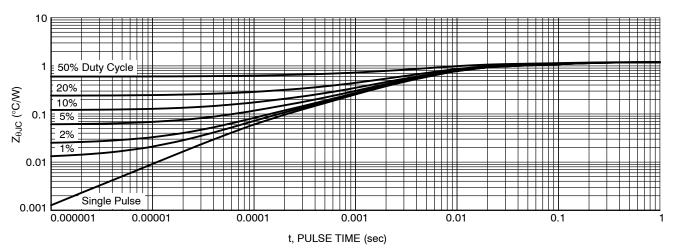
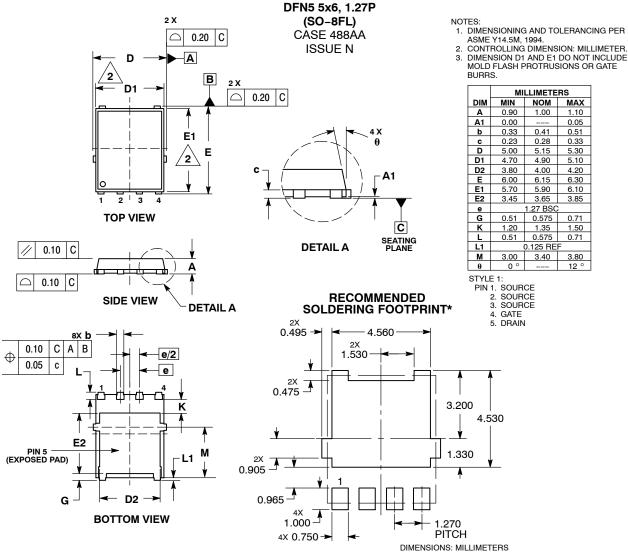


Figure 13. Thermal Response

PACKAGE DIMENSIONS



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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