ON Semiconductor

Is Now



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N-Channel Power MOSFET 100 V, 77 A, 14 m Ω

Features

- Low R_{DS(on)}
- High Current Capability
- 100% Avalanche Tested
- NVB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C Unless otherwise specified)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	100	V
Gate-to-Source Voltage	ge – Conti	nuous	V _{GS}	±20	V
Continuous Drain	Steady State	T _C = 25°C	I _D	77	Α
Current R _{θJC}	State	T _C = 100°C		54	
Power Dissipation $R_{\theta JC}$	Steady State	T _C = 25°C	P _D	217	W
Pulsed Drain Current	ulsed Drain Current $t_p = 10 \mu s$			285	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	ů
Source Current (Body Diode)			Is	77	Α
Single Pulse Drain-to-Source Avalanche Energy (V_{DD} = 50 Vdc, V_{GS} = 10 Vdc, $I_{L(pk)}$ = 56 A, L = 0.3 mH, R_G = 25 Ω)			E _{AS}	470	mJ
Lead Temperature for Soldering Purposes, 1/8" from Case for 10 Seconds			TL	260	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit	
Junction-to-Case (Drain) Steady State	$R_{\theta JC}$	0.69	°C/W	
Junction-to-Ambient (Note 1)	$R_{\theta JA}$	33		

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

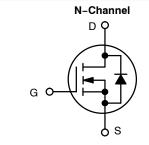
 Surface mounted on FR4 board using 1 sq in pad size, (Cu Area 1.127 sq in [2 oz] including traces).

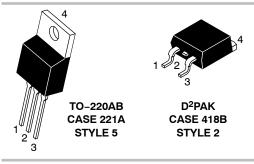


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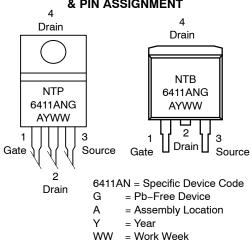
http://onsemi.com

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX (Note 1)
100 V	14 m Ω @ 10 V	77 A





MARKING DIAGRAM & PIN ASSIGNMENT



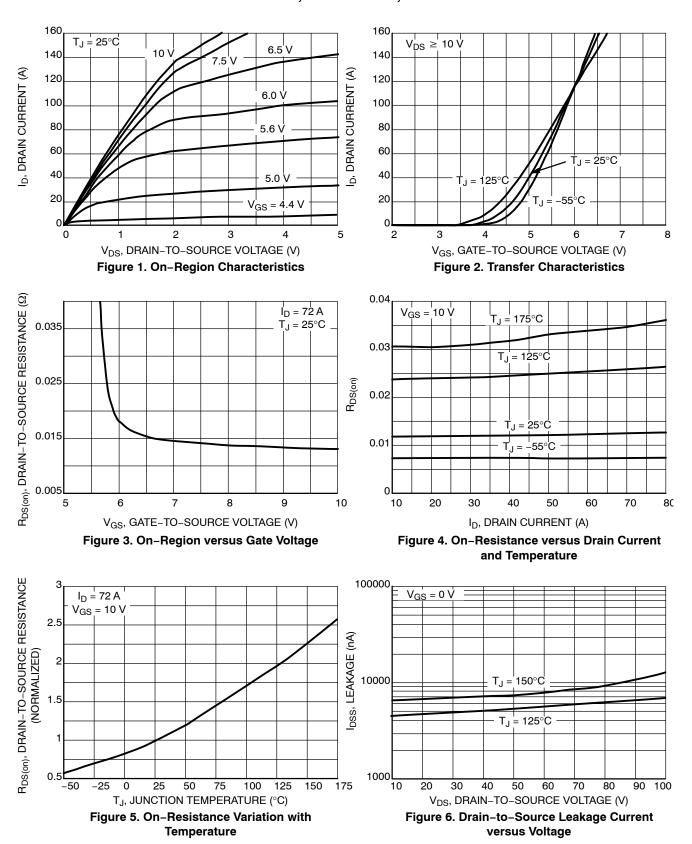
ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ Unless otherwise specified)

Characteristics	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•			•	•	•	•
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V,	I _D = 250 μA	100			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				113		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	T _J = 25°C			1.0	μΑ
		V _{DS} = 100 V	T _J = 125°C			100	1
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V	/ _{GS} = ±20 V			±100	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(th)}	$V_{GS} = V_{DS}$	I _D = 250 μA	2.0		4.0	V
Negative Threshold Temperature Coefficient	V _{GS(th)} /T _J				8.6		mV/°C
Drain-to-Source On-Resistance	R _{DS(on)}	V _{GS} = 10 \	V, I _D = 72 A		12.7	14	mΩ
Forward Transconductance	9FS	V _{DS} = 5 V	′, I _D = 10 A		24		S
CHARGES, CAPACITANCES & GATE RESIST	ANCE	•					
Input Capacitance	C _{iss}				3700		pF
Output Capacitance	C _{oss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1 MHz			550		1 !
Reverse Transfer Capacitance	C _{rss}				200		1
Total Gate Charge	Q _{G(TOT)}				100		nC
Threshold Gate Charge	Q _{G(TH)}				4.0		1
Gate-to-Source Charge	Q_{GS}	$V_{GS} = 10 \text{ V}, V_{DS} = 80 \text{ V},$ $I_{D} = 72 \text{ A}$			16		1
Gate-to-Drain Charge	Q_{GD}				47		1
Plateau Voltage	V_{GP}				5.2		V
Gate Resistance	R_{G}				3.1		Ω
SWITCHING CHARACTERISTICS, V _{GS} = 10 V	(Note 3)						
Turn-On Delay Time	t _{d(on)}				16		ns
Rise Time	t _r	V _{GS} = 10 V,	Vpp = 80 V.		144		1
Turn-Off Delay Time	t _{d(off)}	I _D = 72 A,	$R_G = 6.2 \Omega$		107		1
Fall Time	t _f	1			157		1
DRAIN-SOURCE DIODE CHARACTERISTICS	-	-		-	-	-	-
Forward Diode Voltage	V _{SD}	I _S = 72 A	T _J = 25°C		0.92	1.3	V
			T _J = 125°C		0.86		
Reverse Recovery Time	t _{rr}				94		ns
Charge Time	t _a	$V_{GS} = 0 \text{ V, } I_{S} = 72 \text{ A,}$ $dI_{S}/dt = 100 \text{ A/}\mu\text{s}$			64		
Discharge Time	t _b				30		1
Reverse Recovery Charge	Q _{RR}				330		nC

^{2.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%. 3. Switching characteristics are independent of operating junction temperatures.



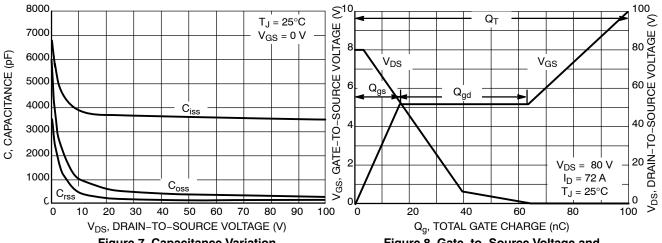


Figure 7. Capacitance Variation

Figure 8. Gate-to-Source Voltage and Drain-to-Source Voltage versus Total Charge

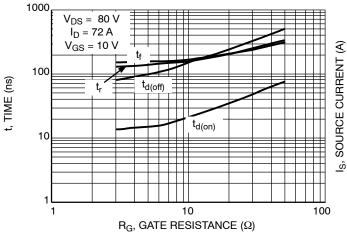


Figure 9. Resistive Switching Time Variation versus Gate Resistance

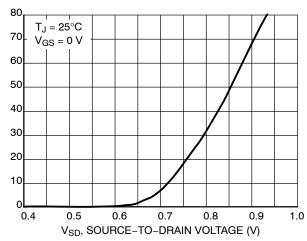


Figure 10. Diode Forward Voltage versus Current

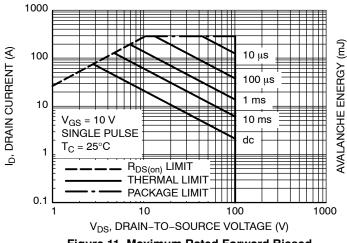


Figure 11. Maximum Rated Forward Biased Safe Operating Area

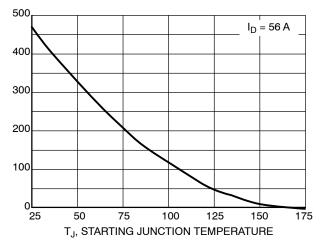


Figure 12. Maximum Avalanche Energy versus **Starting Junction Temperature**

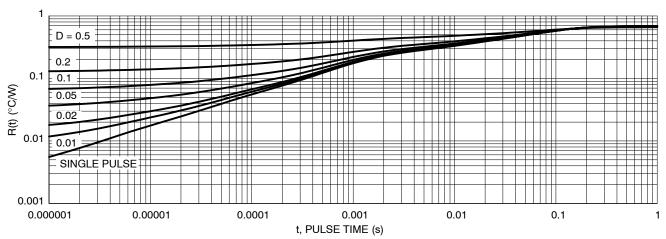


Figure 13. Thermal Response

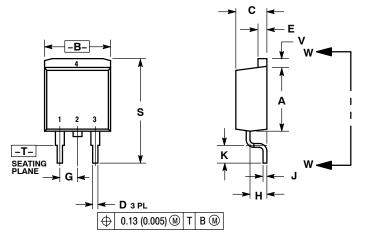
ORDERING INFORMATION

Device	Package	Shipping [†]
NTB6411ANG	D ² PAK (Pb-Free)	50 Units / Rail
NTB6411ANT4G	D ² PAK (Pb-Free)	800 / Tape & Reel
NTP6411ANG	TO-220 (Pb-Free)	50 Units / Rail
NVB6411ANT4G	D ² PAK (Pb-Free)	800 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

D²PAK 3 CASE 418B-04 ISSUE K



NOTES:

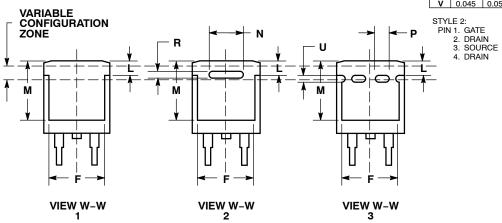
- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

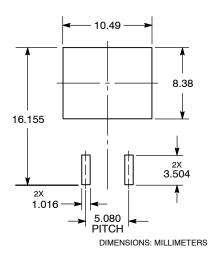
 2. CONTROLLING DIMENSION: INCH.

 3. 418B-01 THRU 418B-03 OBSOLETE, NEW STANDARD 418B-04.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.340	0.380	8.64	9.65	
В	0.380	0.405	9.65	10.29	
С	0.160	0.190	4.06	4.83	
D	0.020	0.035	0.51	0.89	
Е	0.045	0.055	1.14	1.40	
F	0.310	0.350	7.87	8.89	
G	0.100	BSC	2.54 BSC		
Н	0.080	0.110	2.03	2.79	
J	0.018	0.025	0.46	0.64	
K	0.090	0.110	2.29	2.79	
L	0.052	0.072	1.32	1.83	
M	0.280	0.320	7.11	8.13	
N	0.197 REF		5.00 REF		
Р	0.079 REF		2.00 REF		
R	0.039 REF		0.99 REF		
S	0.575	0.625	14.60	15.88	
V	0.045	0.055	1.14	1 40	



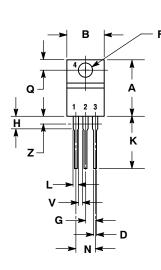
SOLDERING FOOTPRINT*

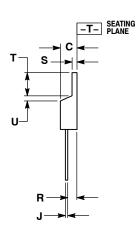


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AG**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 DIMENSION Z DEFINES A ZONE WHERE ALL
 BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.570	0.620	14.48	15.75	
В	0.380	0.405	9.66	10.28	
С	0.160	0.190	4.07	4.82	
D	0.025	0.036	0.64	0.91	
F	0.142	0.161	3.61	4.09	
G	0.095	0.105	2.42	2.66	
Н	0.110	0.161	2.80	4.10	
J	0.014	0.025	0.36	0.64	
K	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.15	1.52	
N	0.190	0.210	4.83	5.33	
Q	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	
T	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
٧	0.045		1.15		
Z		0.080		2.04	

STYLE 5:

PIN 1 GATE DRAIN 2.

3. SOURCE

DRAIN

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