

60 V, 4.0 A, Low V_{CE(sat)} **NPN Transistor** NSS60201LT1G

onsemi e²PowerEdge family of low V_{CE(sat)} transistors are miniature surface mount devices featuring ultra low saturation voltage (V_{CE(sat)}) and high current gain capability. These are designed for use in low voltage, high speed switching applications where affordable efficient energy control is important.

Typical applications are DC-DC converters and power management in portable and battery powered products such as cellular and cordless phones, PDAs, computers, printers, digital cameras and MP3 players. Other applications are low voltage motor controls in mass storage products such as disc drives and tape drives. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e²PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS $(T_A = 25 \, ^{\circ}C)$

Rating	Symbol	Max	Unit	
Collector-Emitter Voltage	V _{CEO}	60	Vdc	
Collector-Base Voltage	V _{CBO}	140	Vdc	
Emitter-Base Voltage	V _{EBO}	8.0	Vdc	
Collector Current - Continuous	I _C	2.0	Α	
Collector Current - Peak	I _{CM}	4.0	Α	
Electrostatic Discharge	ESD	HBM Class 3B MM Class C		

THERMAL CHARACTERISTICS

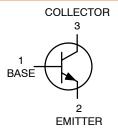
Characteristic	Symbol	Max	Unit
Total Device Dissipation T _A = 25 °C Derate above 25 °C	P _D (Note 1)	460 3.7	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA} (Note 1)	270	°C/W
Total Device Dissipation T _A = 25 °C Derate above 25 °C	P _D (Note 2)	540 4.3	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA} (Note 2)	230	°C/W
Total Device Dissipation (Single Pulse < 10 sec.)	P _{Dsingle} (Note 3)	710	mW
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1

- 1. FR-4 @ 100 mm², 1 oz. copper traces. 2. FR-4 @ 500 mm², 1 oz. copper traces.
- 3. Thermal response.

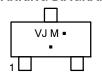
60 VOLTS, 4.0 AMPS NPN LOW $V_{CE(sat)}$ TRANSISTOR EQUIVALENT R_{DS(on)} 70 mΩ





SOT-23 (TO-236) **CASE 318** STYLE 6

MARKING DIAGRAM



VJ = Specific Device Code

= Date Code* = Pb-Free Package

(Note: Microdot may be in either location)

Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
NSS60201LT1G	SOT-23 (Pb-Free)	3000/Tape & Reel
NSV60201LT1G	SOT-23 (Pb-Free)	3000/Tape & Reel

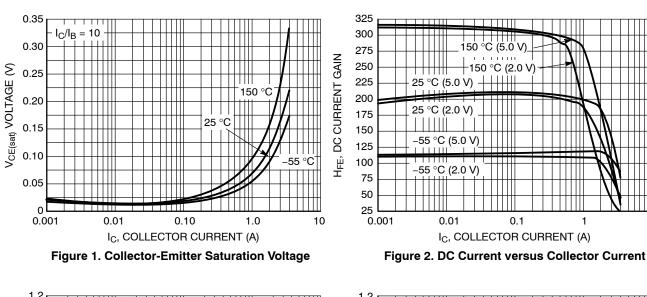
† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

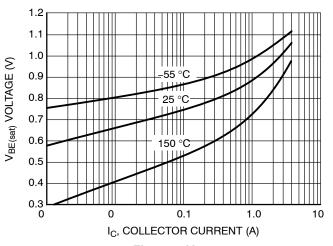
ELECTRICAL CHARACTERISTICS (T_A = 25 $^{\circ}C$ unless otherwise noted)

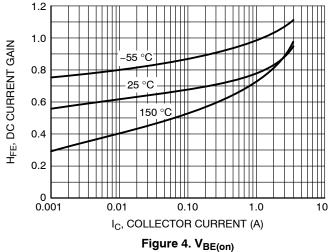
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			•	•	•
Collector-Emitter Breakdown Voltage $(I_C = 10 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	60	-	-	Vdc
Collector-Base Breakdown Voltage ($I_C = 0.1 \text{ mAdc}, I_E = 0$)	V _(BR) CBO	140	-	-	Vdc
Emitter-Base Breakdown Voltage $(I_E = 0.1 \text{ mAdc}, I_C = 0)$	V _{(BR)EBO}	8.0	-	-	Vdc
Collector Cutoff Current (V _{CB} = 60 Vdc, I _E = 0)	I _{CBO}	-	-	0.1	μAdc
Emitter Cutoff Current (V _{EB} = 6.0 Vdc)	I _{EBO}	-	-	0.1	μAdc
ON CHARACTERISTICS			•		
DC Current Gain (Note 4) ($I_C = 10 \text{ mA}, V_{CE} = 2.0 \text{ V}$) ($I_C = 500 \text{ mA}, V_{CE} = 2.0 \text{ V}$) ($I_C = 1.0 \text{ A}, V_{CE} = 2.0 \text{ V}$) ($I_C = 1.0 \text{ A}, V_{CE} = 2.0 \text{ V}$) ($I_C = 2.0 \text{ A}, V_{CE} = 2.0 \text{ V}$)	h _{FE}	160 160 150 100	- - - -	- - 350 -	
Collector – Emitter Saturation Voltage (Note 4) ($I_C = 0.1 \text{ A}, I_B = 0.010 \text{ A}$) ($I_C = 1.0 \text{ A}, I_B = 0.100 \text{ A}$) ($I_C = 2.0 \text{ A}, I_B = 0.200 \text{ A}$)	V _{CE(sat)}	- - -	- - -	0.020 0.075 0.140	V
Base – Emitter Saturation Voltage (Note 4) $(I_C = 1.0 \text{ A}, I_B = 10 \text{ mA})$	V _{BE(sat)}	-	0.790	0.900	٧
Base – Emitter Turn–on Voltage (Note 4) (I _C = 1.0 A, V _{CE} = 2.0 V)	V _{BE(on)}	-	0.760	0.900	٧
Cutoff Frequency ($I_C = 100 \text{ mA}$, $V_{CE} = 5.0 \text{ V}$, $f = 100 \text{ MHz}$)	f⊤	100	-	-	MHz
Input Capacitance (V _{EB} = 0.5 V, f = 1.0 MHz)	Cibo	-	<u> </u>	380	pF
Output Capacitance (V _{CB} = 3.0 V, f = 1.0 MHz)	Cobo	-	-	45	pF
SWITCHING CHARACTERISTICS					
Delay ($V_{CC} = 30 \text{ V}, I_{C} = 750 \text{ mA}, I_{B1} = 15 \text{ mA}$)	t _d	_	-	55	ns
Rise ($V_{CC} = 30 \text{ V}, I_{C} = 750 \text{ mA}, I_{B1} = 15 \text{ mA}$)	t _r	_	_	100	ns
Storage (V _{CC} = 30 V, I _C = 750 mA, I _{B1} = 15 mA)	t _s	-	-	1100	ns
Fall (V _{CC} = 30 V, I _C = 750 mA, I _{B1} = 15 mA)	t _f	_	-	120	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

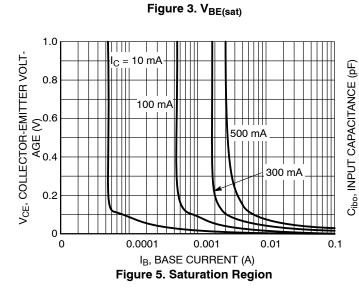
4. Pulsed Condition: Pulse Width = 300 msec, Duty Cycle ≤ 2%.

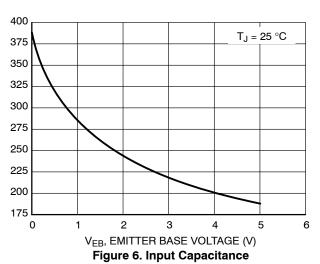






10





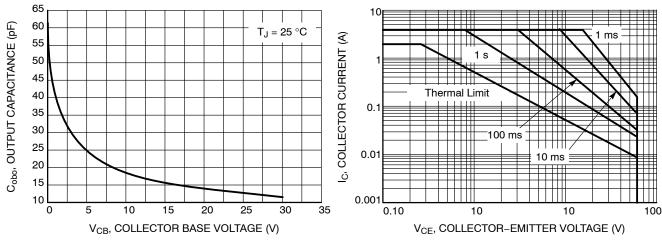


Figure 7. Output Capacitance

Figure 8. Safe Operating Area Single Pulse Test @ T_{amb} = 25 °C

REVISION HISTORY

Revision	Description of Changes	Date
5	Rebranded the Data Sheet to onsemi format.	10/14/2025

This document has undergone updates prior to the inclusion of this revision history table. The changes tracked here only reflect updates made on the noted approval dates.

MILLIMETERS

MIN

0.89

0.01

0.37

0.08

2.80

1.20

1.78

0.30

0.35

2.10

O°

NOM

1.00

0.06

0.44

0.14

2.90

1.30

1.90

0.43

0.54

2.40





SOT-23 (TO-236) 2.90x1.30x1.00 1.90P **CASE 318 ISSUE AU**

DATE 14 AUG 2024

MAX

1.11

0.10

0.50

0.20

3.04

1.40

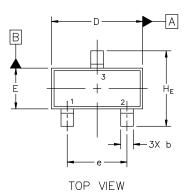
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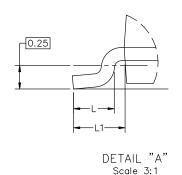
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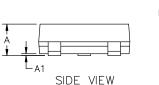
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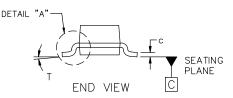
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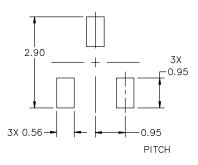
10°











NOTES:

DIM

Α

Α1

b

С

D

Ε

е L

L1

HE

Τ

- DIMENSIONING AND TOLERANCING 1. PER ASME Y14.5M, 2018. CONTROLLING DIMENSIONS:
- MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package



* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

SOT-23 (TO-236) 2.90x1.30x1.00 1.90P CASE 318 ISSUE AU

DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE	1	
STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 13:	STYLE 14:
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE		PIN 1. SOURCE	PIN 1. CATHODE
2. ANODE	2. SOURCE	2. CATHODE		2. DRAIN	2. GATE
3. CATHODE	3. GATE	3. CATHODE-ANODE		3. GATE	3. ANODE
STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	N PIN 1. CATHODE	
2. CATHODE	2. CATHODE	2. ANODE	2. CATHODE	2. ANODE	
3. ANODE	3. CATHODE	3. CATHODE	3. ANODE	3. CATHODE-ANODI	
STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
2. SOURCE	2. OUTPUT	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
3. DRAIN	3. INPUT	3. CATHODE	3. SOURCE	3. GATE	3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE				

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