## Trench-based Schottky Diode, 500 mA, 30 V

## NSR05301MX4

These Schottky diodes are optimized for low forward voltage drop and low leakage current that offers the most optimal power dissipation in applications. They are housed in space saving micro-packaging ideal for space constrained applications.

## Features

- Smallest Package Available (01005); 0.445mm x 0.24 mm
- 500 mA of Continuous Forward Current
- Low Forward Voltage Drop - 430 mV (Typical) @ $\mathrm{I}_{\mathrm{F}}=200 \mathrm{~mA}$
- Low Reverse Current - $25 \mu \mathrm{~A}$ (Typical) @ $\mathrm{V}_{\mathrm{R}}=30 \mathrm{~V}$
- Low Reverse Recovery Time - 9 ns Typical
- Low Capacitance - 19 pF Typical

Typical Applications

- Mobile and Wearable Devices
- LED Boost Converters
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping \& Protection


## MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Forward Current (DC) | $\mathrm{I}_{\mathrm{F}}$ | 500 | mA |
| Reverse Voltage | $\mathrm{V}_{\mathrm{R}}$ | 30 | V |
| Repetitive Peak Forward Current <br> (Pulse Wave $=1$ sec, Duty Cycle $=66 \%)$ | $\mathrm{I}_{\mathrm{FRM}}$ | 1.0 | A |
| ESD Rating:Human Body Model <br> Machine Model | ESD | $>8.0$ <br> $>400$ | kV <br> V |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

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MARKING DIAGRAM

X4DFN2 CASE 718AA

## DM

D = Specific Device Code
M = Date Code

## ORDERING INFORMATION

| Device | Package | Shipping $\dagger$ |
| :---: | :---: | :---: |
| NSR05301MX4T5G | X4DFN2 <br> (Pb-Free) | $10000 /$ <br> Tape \& Reel |

$\dagger$ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
| :---: | :---: | :---: | :---: |
| Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ | $R_{\theta J A}$ | $\begin{gathered} 614.9 \\ 203 \end{gathered}$ | $\begin{gathered} { }^{\circ} \mathrm{C} / \mathrm{W} \\ \mathrm{mWW} \end{gathered}$ |
| Thermal Resistance Junction-to-Ambient (Note 2) Total Power Dissipation @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ | $\mathrm{R}_{\theta \mathrm{JA}}$ | $\begin{gathered} 239.4 \\ 522 \end{gathered}$ | $\begin{gathered} { }^{\circ} \mathrm{C} / \mathrm{W} \\ \mathrm{mWW} \end{gathered}$ |
| Junction Temperature Range | $\mathrm{T}_{J}$ | -55 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\text {STG }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Lead Solder Temperature - Maximum (10 seconds) | $\mathrm{T}_{\mathrm{L}}$ | 260 | ${ }^{\circ} \mathrm{C}$ |

1. Mounted onto a $4 \mathrm{in}^{2}$ FR-4 board $10 \mathrm{~mm}^{2} 1 \mathrm{oz}$. Cu 0.06 ' thick single-sided. Operating to steady state.
2. Mounted onto a $4 \mathrm{in}^{2}$ FR-4 board $2 \mathrm{~cm}^{2} 1 \mathrm{oz}$. Cu 0.06 ' thick single-sided. Operating to steady state.


Figure 1. Thermal Response (Note 1)

t, PULSE TIME (s)
Figure 2. Thermal Response (Note 2)

ELECTRICAL CHARACTERISTICS $\left(\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted)

| Characteristic | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{gathered} \text { Reverse Leakage } \\ \left(\mathrm{V}_{\mathrm{R}}=10 \mathrm{~V}\right) \\ \left(\mathrm{V}_{\mathrm{R}}=30 \mathrm{~V}\right) \end{gathered}$ | $\mathrm{I}_{\mathrm{R}}$ |  | $\begin{aligned} & 15 \\ & 25 \end{aligned}$ | $\begin{gathered} 50 \\ 100 \end{gathered}$ | $\mu \mathrm{A}$ |
| $\begin{array}{r} \text { Forward Voltage } \\ \left(I_{F}=100 \mathrm{~mA}\right) \\ \left(I_{F}=200 \mathrm{~mA}\right) \\ \left(I_{F}=500 \mathrm{~mA}\right) \end{array}$ | $V_{F}$ |  | $\begin{aligned} & 355 \\ & 430 \\ & 640 \end{aligned}$ | $\begin{aligned} & 480 \\ & 540 \\ & 800 \end{aligned}$ | mV |
| Total Capacitance $\left(\mathrm{V}_{\mathrm{R}}=5.0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}\right)$ | $\mathrm{C}_{\text {T }}$ |  | 19 |  | pF |
| $\begin{aligned} & \text { Reverse Recovery Time } \\ & \qquad\left(I_{F}=I_{R}=10 \mathrm{~mA}, I_{R(R E C)}=1.0 \mathrm{~mA}\right) \end{aligned}$ | $\mathrm{trr}_{\text {r }}$ |  | 9.0 | 11 | ns |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS


Figure 3. Forward Voltage


Figure 5. Total Capacitance

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X4DFN2, 0.445x0.24, 0.27P
    CASE 718AA
    ISSUE A
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DATE 21 MAR 2017


BOTTOM VIEW

NOTES:

1. DIMENSIONING AND TOLERANCING PER

ASME Y14.5M, 1994.
ASME Y14.5M, 1994.
2. EXPOSED COPPER ALLOWED AS SHOWN.

|  | MILLIMETERS |  |  |
| :---: | :---: | :---: | :---: |
| DIM | MIN | NOM | MAX |
| A | 0.15 | 0.18 | 0.21 |
| A1 | --- | --- | 0.03 |
| b | 0.170 | 0.185 | 0.200 |
| D | 0.415 | 0.445 | 0.475 |
| E | 0.210 | 0.240 | 0.270 |
| e | 0.270 BSC |  |  |
| L | 0.105 | 0.120 | 0.135 |

GENERIC
MARKING DIAGRAMS*

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0 x
$$

X

X = Specific Device Code
*This information is generic. Please refer to device data sheet for actual part marking. Some products may not follow the Generic Marking.

RECOMMENDED MOUNTING FOOTPRINT*


See Application Note AND8398/D for more mounting details
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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| ---: | :--- | :--- | :--- |
| DESCRIPTION: | X4DFN2, 0.445X0.24, 0.27P | PAGE 1 OF 1 |

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