

# Dual Schmitt-Trigger Inverter

## NL27WZ14

The NL27WZ14 is a high performance dual inverter with Schmitt-Trigger inputs operating from a 1.65 to 5.5 V supply.

### Features

- Designed for 1.65 V to 5.5 V  $V_{CC}$  Operation
- 3.2 ns  $t_{PD}$  at  $V_{CC} = 5$  V (Typ)
- Inputs/Outputs Overvoltage Tolerant up to 5.5 V
- $I_{OFF}$  Supports Partial Power Down Protection
- Sink 32 mA at 4.5 V
- Available in SC-88, SC-74 and UDFN6 Packages
- Chip Complexity < 100 FETs
- -Q Suffix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

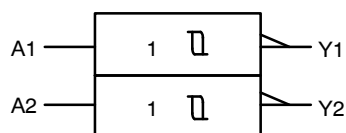


Figure 1. Logic Symbol



SC-88  
DF SUFFIX  
CASE 419B-02



SC-74  
CASE 318F-05

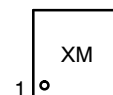
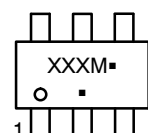
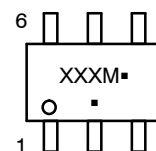


UDFN6  
1.45x1.0, 0.5P  
CASE 517AQ



UDFN6  
1x1, 0.35P  
CASE 517BX

### MARKING DIAGRAMS



X, XXX = Specific Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\* Date Code orientation and/or position may vary depending upon manufacturing location.

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

# NL27WZ14

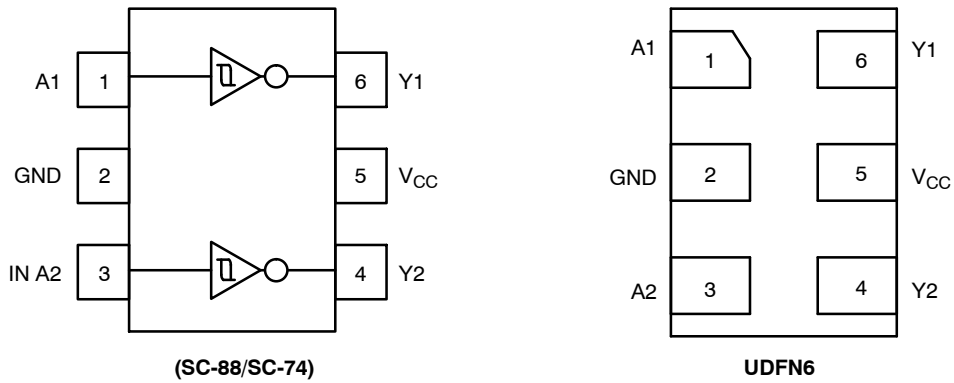


Figure 2. Pinout (Top View)

## PIN ASSIGNMENT

Pin	Function
1	A1
2	GND
3	A2
4	Y2
5	V <sub>CC</sub>
6	Y1

## FUNCTION TABLE

A Input	Y Output
L	H
H	L

## MAXIMUM RATINGS

Symbol	Characteristics	Value	Units
V <sub>CC</sub>	DC Supply Voltage	-0.5 to +6.5	V
V <sub>IN</sub>	DC Input Voltage	-0.5 to +6.5	V
V <sub>OUT</sub>	DC Output Voltage Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V <sub>CC</sub> = 0 V)	-0.5 to V <sub>CC</sub> + 0.5 -0.5 to +6.5 -0.5 to +6.5	V
I <sub>IK</sub>	DC Input Diode Current, V <sub>IN</sub> < GND	-50	mA
I <sub>OK</sub>	DC Output Diode Current, V <sub>OUT</sub> < GND	-50	mA
I <sub>OUT</sub>	DC Output Source/Sink Current	±50	mA
I <sub>CC</sub> or I <sub>GND</sub>	DC Supply Current per Supply Pin or Ground Pin	±100	mA
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
T <sub>L</sub>	Lead Temperature, 1 mm from Case for 10 secs	260	°C
T <sub>J</sub>	Junction Temperature under Bias	+150	°C
θ <sub>JA</sub>	Thermal Resistance (Note 2) SC-88 SC-74 UDFN6	377 320 154	°C/W
P <sub>D</sub>	Power Dissipation in Still Air SC-88 SC-74 UDFN6	332 390 812	mW
MSL	Moisture Sensitivity	Level 1	-
F <sub>R</sub>	Flameability Rating Oxygen Index: 28 to 34	UL 94-V-0 @ 0.125 in	-

# NL27WZ14

V <sub>ESD</sub>	ESD Withstand Voltage (Note 3) Charged Device Model	Human Body Model	2000 1000	V
I <sub>LATCHUP</sub>	Latchup Performance (Note 4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Applicable to devices with outputs that may be tri-stated.
2. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow per JESD51-7.
3. HBM tested to ANSI/ESDA/JEDEC JS-001-2017. CDM tested to EIA/JESD22-C101-F. JEDEC recommends that ESD qualification to EIA/JESD22-A115-A (Machine Model) be discontinued per JEDEC/JEP172A.
4. Tested to EIA/JESD78 Class II.

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage	1.65	5.5	V
V <sub>IN</sub>	DC Input Voltage	0	5.5	V
V <sub>OUT</sub>	DC Output Voltage Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V <sub>CC</sub> = 0 V)	0 0 0	V <sub>CC</sub> 5.5 5.5	V
T <sub>A</sub>	Operating Temperature Range	-55	+125	°C
t <sub>r</sub> , t <sub>f</sub>	Input Transition Rise or Fall Rate V <sub>CC</sub> = 1.65 V to 1.95 V V <sub>CC</sub> = 2.3 V to 2.7 V V <sub>CC</sub> = 3.0 V to 3.6 V V <sub>CC</sub> = 4.5 V to 5.5 V	0 0 0 0	No Limit No Limit No Limit No Limit	ns

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

## DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25 °C			-40 °C ≤ T <sub>A</sub> ≤ 85 °C		-55 °C ≤ T <sub>A</sub> ≤ 125 °C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
V <sub>T+</sub>	Positive Input Threshold Voltage		1.65	–	1.0	1.4	–	1.4	–	1.4	V
			2.3	–	1.5	1.8	–	1.8	–	1.8	
			2.7	–	1.7	2.0	–	2.0	–	2.0	
			3.0	–	1.9	2.2	–	2.2	–	2.2	
			4.5	–	2.7	3.1	–	3.1	–	3.1	
			5.5	–	3.3	3.6	–	3.6	–	3.6	
V <sub>T–</sub>	Negative Input Threshold Voltage		1.65	0.2	0.5	–	0.2	–	0.2	–	V
			2.3	0.4	0.75	–	0.4	–	0.4	–	
			2.7	0.5	0.87	–	0.5	–	0.5	–	
			3.0	0.6	1.0	–	0.6	–	0.6	–	
			4.5	1.0	1.5	–	1.0	–	1.0	–	
			5.5	1.2	1.9	–	1.2	–	1.2	–	
V <sub>H</sub>	Input Hysteresis Voltage		1.65	0.1	0.48	0.9	0.1	0.9	0.1	0.9	V
			2.3	0.25	0.75	1.1	0.25	1.1	0.25	1.1	
			2.7	0.3	0.83	1.15	0.3	1.15	0.3	1.15	
			3.0	0.4	0.93	1.2	0.4	1.2	0.4	1.2	
			4.5	0.6	1.2	1.5	0.6	1.5	0.6	1.5	
			5.5	0.7	1.4	1.7	0.7	1.7	0.7	1.7	

# DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25 °C			-40 °C ≤ T <sub>A</sub> ≤ 85 °C		-55 °C ≤ T <sub>A</sub> ≤ 125 °C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
V <sub>OH</sub>	High-Level Output Voltage V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -100 μA	1.65 to 5.5	V <sub>CC</sub> - 0.1	V <sub>CC</sub>	-	V <sub>CC</sub> - 0.1	-	V <sub>CC</sub> - 0.1	-	V
		I <sub>OH</sub> = -4 mA	1.65	1.29	1.52	-	1.29	-	1.29	-	
		I <sub>OH</sub> = -8 mA	2.3	1.9	2.1	-	1.9	-	1.9	-	
		I <sub>OH</sub> = -12 mA	2.7	2.2	2.4	-	2.2	-	2.2	-	
		I <sub>OH</sub> = -16 mA	3.0	2.4	2.7	-	2.4	-	2.4	-	
		I <sub>OH</sub> = -24 mA	3.0	2.3	2.5	-	2.3	-	2.3	-	
		I <sub>OH</sub> = -32 mA	4.5	3.8	4	-	3.8	-	3.8	-	
V <sub>OL</sub>	Low-Level Output Voltage V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 100 μA	1.65 to 5.5	-	-	0.1	-	0.1	-	0.1	V
		I <sub>OL</sub> = 4 mA	1.65	-	0.08	0.24	-	0.24	-	0.24	
		I <sub>OL</sub> = 8 mA	2.3	-	0.2	0.3	-	0.3	-	0.3	
		I <sub>OL</sub> = 12 mA	2.7	-	0.22	0.4	-	0.4	-	0.4	
		I <sub>OL</sub> = 16 mA	3.0	-	0.28	0.4	-	0.4	-	0.4	
		I <sub>OL</sub> = 24 mA	3.0	-	0.38	0.55	-	0.55	-	0.55	
		I <sub>OL</sub> = 32 mA	4.5	-	0.42	0.55	-	0.55	-	0.55	
I <sub>IN</sub>	Input Leak-age Current	V <sub>IN</sub> = 5.5 V or GND	1.65 to 5.5	-	-	±0.1	-	±1.0	-	±1.0	μA
I <sub>OFF</sub>	Power Off Leakage Current	V <sub>IN</sub> = 5.5 V or V <sub>OUT</sub> = 5.5 V	0	-	-	1	-	10	-	10	μA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = 5.5 V or GND	5.5	-	-	1	-	10	-	10	μA

# AC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25 °C			-40 °C ≤ T <sub>A</sub> ≤ 85 °C		-55 °C ≤ T <sub>A</sub> ≤ 125 °C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay, A to Y (Figures 3 and 4)	R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF	1.65 to 1.95	-	7.1	13	-	14.5	-	15.5	ns
			2.3 to 2.7	-	4.3	7.4	-	8.1	-	9.1	
		R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF	3.0 to 3.6	-	3.3	5	-	5.5	-	6.5	
			4.5 to 5.5	-	2.7	4.1	-	4.5	-	5.5	
			3.0 to 3.6	-	4	6	-	6.6	-	7.6	
		R <sub>L</sub> = 500 Ω, C <sub>L</sub> = 50 pF	4.5 to 5.5	-	3.2	4.9	-	5.4	-	6.4	

# CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Condition	Typical	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	2.5	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	4.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 5)	10 MHz, V <sub>CC</sub> = 3.3 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub> 10 MHz, V <sub>CC</sub> = 5.0 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub>	11 12.5	pF

5. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC(OPR)</sub> = C<sub>PD</sub> · V<sub>CC</sub> · f<sub>in</sub> ) I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no-load dynamic power consumption; P<sub>D</sub> = C<sub>PD</sub> · V<sub>CC</sub><sup>2</sup> · f<sub>in</sub> ) I<sub>CC</sub> · V<sub>CC</sub>.

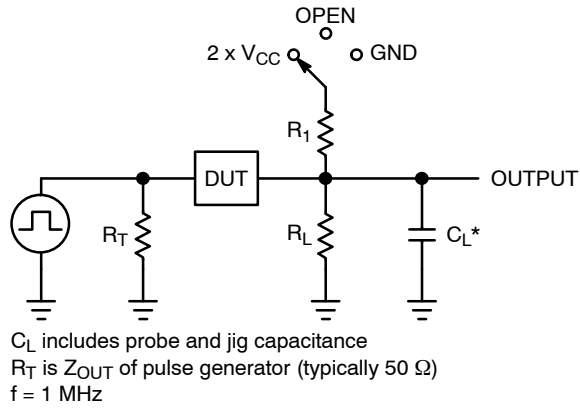


Figure 3. Test Circuit

Test	Switch Position	$C_L$ , pF	$R_L$ , $\Omega$	$R_1$ , $\Omega$
$t_{PLH} / t_{PHL}$	Open	See AC Characteristics Table		
$t_{PLZ} / t_{PZL}$	$2 \times V_{CC}$	-	-	-
		See AC Characteristics Table		
$t_{PHZ} / t_{PZH}$	GND	-	-	-
		See AC Characteristics Table		

X = Don't Care

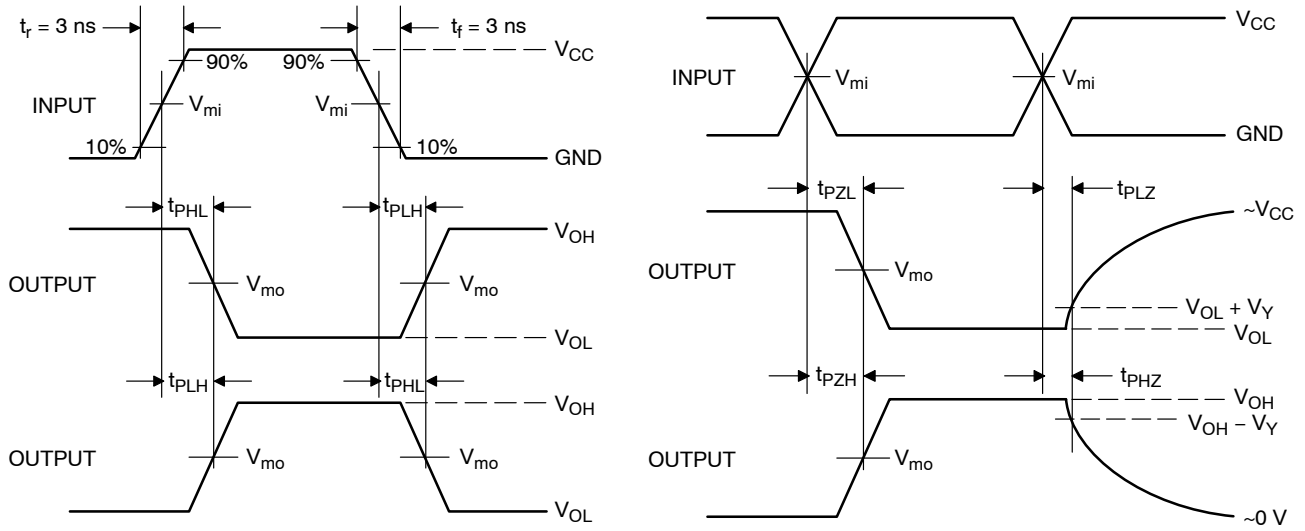


Figure 4. Switching Waveforms

$V_{CC}$ , V	$V_{mi}$ , V	$V_{mo}$ , V		$V_Y$ , V
		$t_{PLH}, t_{PHL}$	$t_{PLZ}, t_{PLZ}, t_{PZH}, t_{PZH}$	
1.65 to 1.95	$V_{CC}/2$	$V_{CC}/2$	$V_{CC}/2$	0.15
2.3 to 2.7	$V_{CC}/2$	$V_{CC}/2$	$V_{CC}/2$	0.15
3.0 to 3.6	$V_{CC}/2$	$V_{CC}/2$	$V_{CC}/2$	0.3
4.5 to 5.5	$V_{CC}/2$	$V_{CC}/2$	$V_{CC}/2$	0.3

# NL27WZ14

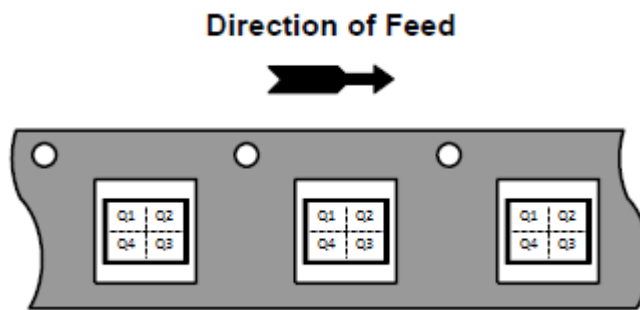
## ORDERING INFORMATION

Device	Package	Specific Device Code	Pin1 Orientation (See below)	Shipping <sup>†</sup>
NL27WZ14DFT2G	SC-88	MA	Q4	3000 / Tape & Reel
NL27WZ14DFT4G	SC-88	MA	Q4	10000 / Tape & Reel
NL27WZ14DFT2G-Q*	SC-88	MA	Q4	3000 / Tape & Reel
NL27WZ14DBVT1G	SC-74	MA	Q4	3000 / Tape & Reel
NL27WZ14MU1TCG	UDFN6, 1.45 x 1.0, 0.5P	P (Rotated 90° CW)	Q4	3000 / Tape & Reel
NL27WZ14MU3TCG	UDFN6, 1.0 x 1.0, 0.35P	3 (Rotated 90° CW)	Q4	3000 / Tape & Reel

<sup>†</sup> For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

\* -Q Suffix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

## PIN 1 ORIENTATION IN TAPE AND REEL

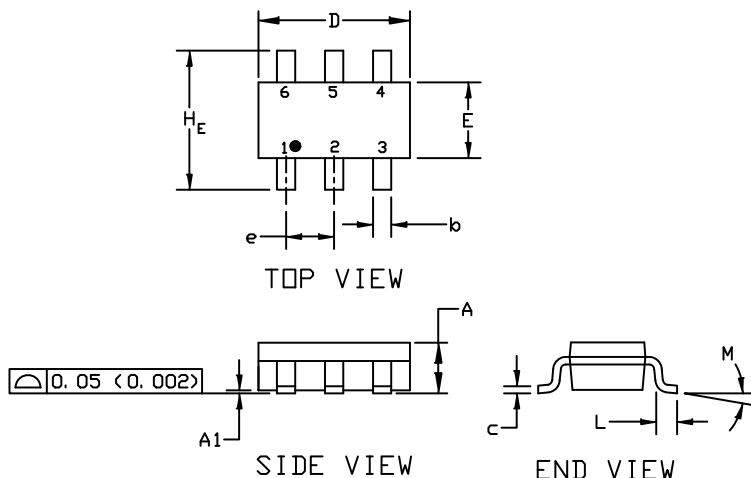




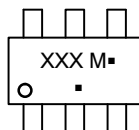
SCALE 2:1

SC-74  
CASE 318F  
ISSUE P

DATE 07 OCT 2021



GENERIC  
MARKING DIAGRAM\*



XXX = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

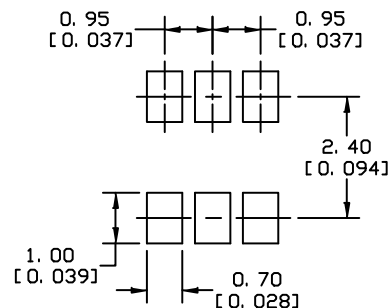
(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994
2. CONTROLLING DIMENSION: INCHES
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.

DIM	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.90	1.00	1.10	0.035	0.039	0.043
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.25	0.37	0.50	0.010	0.015	0.020
c	0.10	0.18	0.26	0.004	0.007	0.010
D	2.90	3.00	3.10	0.114	0.118	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
e	0.85	0.95	1.05	0.034	0.037	0.041
H <sub>E</sub>	2.50	2.75	3.00	0.099	0.108	0.118
L	0.20	0.40	0.60	0.008	0.016	0.024
M	0*	---	10*	0*	---	10*



\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

SOLDERING FOOTPRINT

<b>STYLE 1:</b> PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. ANODE 6. CATHODE	<b>STYLE 2:</b> PIN 1. NO CONNECTION 2. COLLECTOR 3. EMITTER 4. NO CONNECTION 5. COLLECTOR 6. BASE	<b>STYLE 3:</b> PIN 1. EMITTER 1 2. BASE 1 3. COLLECTOR 2 4. EMITTER 2 5. BASE 2 6. COLLECTOR 1	<b>STYLE 4:</b> PIN 1. COLLECTOR 2 2. EMITTER 1/EMITTER 2 3. COLLECTOR 1 4. EMITTER 3 5. BASE 1/BASE 2/COLLECTOR 3 6. BASE 3	<b>STYLE 5:</b> PIN 1. CHANNEL 1 2. ANODE 3. CHANNEL 2 4. CHANNEL 3 5. CATHODE 6. CHANNEL 4	<b>STYLE 6:</b> PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE
<b>STYLE 7:</b> PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1	<b>STYLE 8:</b> PIN 1. EMITTER 1 2. BASE 2 3. COLLECTOR 2 4. EMITTER 2 5. BASE 1 6. COLLECTOR 1	<b>STYLE 9:</b> PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	<b>STYLE 10:</b> PIN 1. ANODE/CATHODE 2. BASE 3. EMITTER 4. COLLECTOR 5. ANODE 6. CATHODE	<b>STYLE 11:</b> PIN 1. EMITTER 2. BASE 3. ANODE/CATHODE 4. ANODE 5. CATHODE 6. COLLECTOR	

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<b>DESCRIPTION:</b>	<b>SC-74</b>	<b>PAGE 1 OF 1</b>

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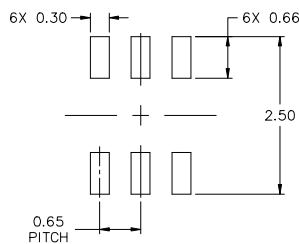
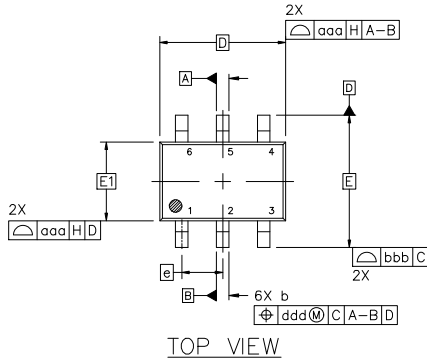


SC-88 2.00x1.25x0.90, 0.65P  
CASE 419B-02  
ISSUE Z

DATE 18 APR 2024

NOTES:

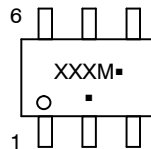
1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
2. ALL DIMENSION ARE IN MILLIMETERS.
3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
5. DATUMS A AND B ARE DETERMINED AT DATUM H.
6. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
7. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION b AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.



RECOMMENDED MOUNTING FOOTPRINT\*

\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC  
MARKING DIAGRAM\*



XXX = Specific Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	---	---	1.10
A1	0.00	---	0.10
A2	0.70	0.90	1.00
b	0.15	0.20	0.25
c	0.08	0.15	0.22
D	2.00 BSC		
E	2.10 BSC		
E1	1.25 BSC		
e	0.65 BSC		
L	0.26	0.36	0.46
L2	0.15 BSC		
aaa	0.15		
bbb	0.30		
ccc	0.10		
ddd	0.10		

STYLES ON PAGE 2

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DESCRIPTION:	SC-88 2.00x1.25x0.90, 0.65P	PAGE 1 OF 2

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SC-88 2.00x1.25x0.90, 0.65P  
CASE 419B-02  
ISSUE Z

DATE 18 APR 2024

STYLE 1: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	STYLE 2: CANCELLED	STYLE 3: CANCELLED	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. COLLECTOR 4. EMITTER 5. BASE 6. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	STYLE 6: PIN 1. ANODE 2 2. N/C 3. CATHODE 1 4. ANODE 1 5. N/C 6. CATHODE 2
STYLE 7: PIN 1. SOURCE 2 2. DRAIN 2 3. GATE 1 4. SOURCE 1 5. DRAIN 1 6. GATE 2	STYLE 8: CANCELLED	STYLE 9: PIN 1. EMITTER 2 2. EMITTER 1 3. COLLECTOR 1 4. BASE 1 5. BASE 2 6. COLLECTOR 2	STYLE 10: PIN 1. SOURCE 2 2. SOURCE 1 3. GATE 1 4. DRAIN 1 5. DRAIN 2 6. GATE 2	STYLE 11: PIN 1. CATHODE 2 2. CATHODE 2 3. ANODE 1 4. CATHODE 1 5. CATHODE 1 6. ANODE 2	STYLE 12: PIN 1. ANODE 2 2. ANODE 2 3. CATHODE 1 4. ANODE 1 5. ANODE 1 6. CATHODE 2
STYLE 13: PIN 1. ANODE 2. N/C 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	STYLE 14: PIN 1. VREF 2. GND 3. GND 4. IOUT 5. VEN 6. VCC	STYLE 15: PIN 1. ANODE 1 2. ANODE 2 3. ANODE 3 4. CATHODE 3 5. CATHODE 2 6. CATHODE 1	STYLE 16: PIN 1. BASE 1 2. EMITTER 2 3. COLLECTOR 2 4. BASE 2 5. EMITTER 1 6. COLLECTOR 1	STYLE 17: PIN 1. BASE 1 2. EMITTER 1 3. COLLECTOR 2 4. BASE 2 5. EMITTER 2 6. COLLECTOR 1	STYLE 18: PIN 1. VIN1 2. VCC 3. VOUT2 4. VIN2 5. GND 6. VOUT1
STYLE 19: PIN 1. I OUT 2. GND 3. GND 4. V CC 5. V EN 6. V REF	STYLE 20: PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER 5. COLLECTOR 6. COLLECTOR	STYLE 21: PIN 1. ANODE 1 2. N/C 3. ANODE 2 4. CATHODE 2 5. N/C 6. CATHODE 1	STYLE 22: PIN 1. D1 (i) 2. GND 3. D2 (i) 4. D2 (c) 5. VBUS 6. D1 (c)	STYLE 23: PIN 1. Vn 2. CH1 3. Vp 4. N/C 5. CH2 6. N/C	STYLE 24: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE
STYLE 25: PIN 1. BASE 1 2. CATHODE 3. COLLECTOR 2 4. BASE 2 5. EMITTER 6. COLLECTOR 1	STYLE 26: PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1	STYLE 27: PIN 1. BASE 2 2. BASE 1 3. COLLECTOR 1 4. EMITTER 1 5. EMITTER 2 6. COLLECTOR 2	STYLE 28: PIN 1. DRAIN 2. DRAIN 3. GATE 4. SOURCE 5. DRAIN 6. DRAIN	STYLE 29: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE/ANODE 6. CATHODE	STYLE 30: PIN 1. SOURCE 1 2. DRAIN 2 3. DRAIN 2 4. SOURCE 2 5. GATE 1 6. DRAIN 1

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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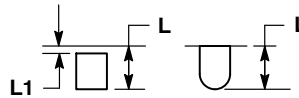
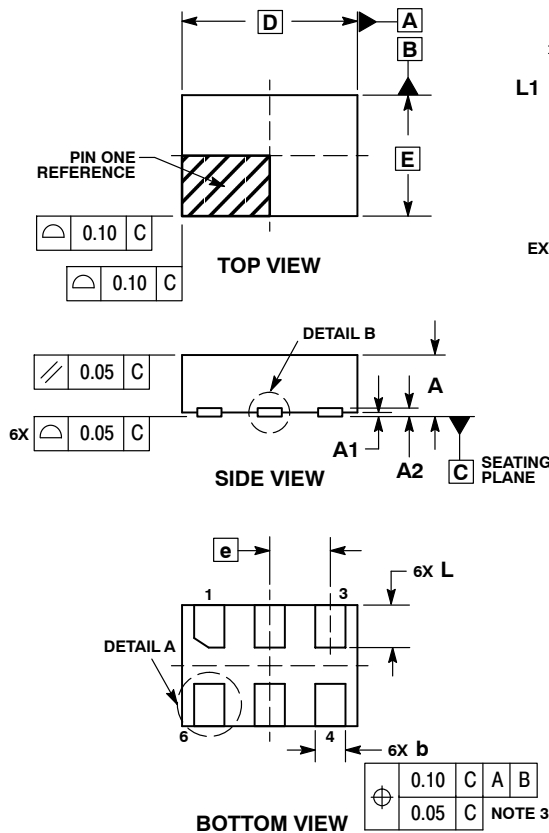
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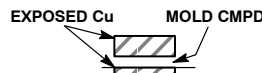
SCALE 4:1

UDFN6, 1.45x1.0, 0.5P  
CASE 517AQ  
ISSUE O

DATE 15 MAY 2008



DETAIL A  
OPTIONAL  
CONSTRUCTIONS



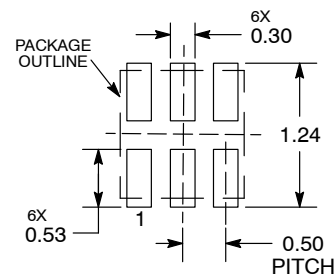
DETAIL B  
OPTIONAL  
CONSTRUCTIONS

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.

MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A2	0.07 REF	
b	0.20	0.30
D	1.45 BSC	
E	1.00 BSC	
e	0.50 BSC	
L	0.30	0.40
L1	---	0.15

MOUNTING FOOTPRINT



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC  
MARKING DIAGRAM\*



X = Specific Device  
Code

M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "▪", may or may not be present.

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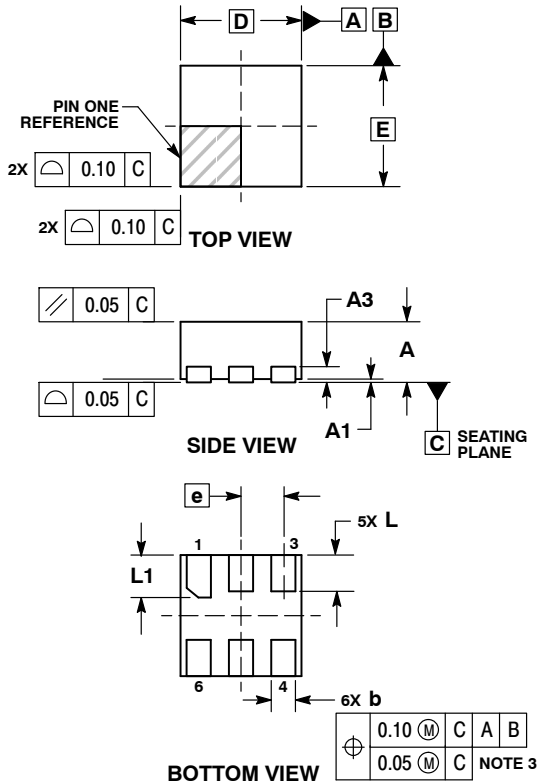
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SCALE 4:1

UDFN6, 1x1, 0.35P  
CASE 517BX  
ISSUE O

DATE 18 MAY 2011

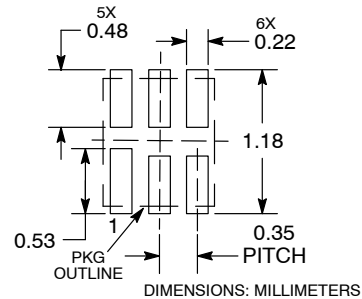


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

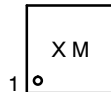
DIM	MILLIMETERS	
	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.13	REF
b	0.12	0.22
D	1.00	BSC
E	1.00	BSC
e	0.35	BSC
L	0.25	0.35
L1	0.30	0.40

RECOMMENDED  
SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC  
MARKING DIAGRAM\*



X = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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