

# Dual 2-Input NOR Gate

## NL27WZ02

The NL27WZ02 is a high performance dual 2-input NOR Gate operating from a 1.65 V to 5.5 V supply.

### Features

- Designed for 1.65 V to 5.5 V  $V_{CC}$  Operation
- 2.5 ns  $t_{PD}$  at  $V_{CC} = 5$  V (typ)
- Inputs/Outputs Overvoltage Tolerant up to 5.5 V
- $I_{OFF}$  Supports Partial Power Down Protection
- Source/Sink 24 mA at 3.0 V
- Available in US8, UDFN8 and UQFN8 Packages
- Chip Complexity < 100 FETs
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

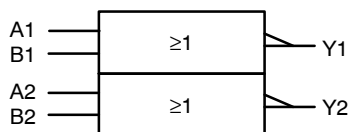

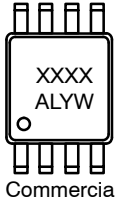

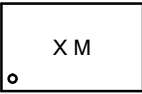

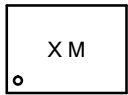

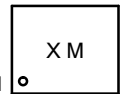

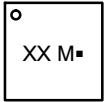




Figure 1. Logic Symbol



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| MARKING DIAGRAMS   |  |   |
|--|--|---|
|    | <b>US8<br/>US SUFFIX<br/>CASE 493</b>                | <br>Commercial |
|    | <b>UDFN8, 1.95x1.0<br/>MU1 SUFFIX<br/>CASE 517CA</b> |                |
|   | <b>UDFN8, 1.6x1.0<br/>MU2 SUFFIX<br/>CASE 517BY</b>  |               |
|  | <b>UDFN8, 1.45x1.0<br/>MU3 SUFFIX<br/>CASE 517BZ</b> |              |
|  | <b>UQFN8, 1.6x1.6<br/>MQ1 SUFFIX<br/>CASE 523AN</b>  |              |
|  | <b>UQFN8, 1.4x1.2<br/>MQ2 SUFFIX<br/>CASE 523AS</b>  |              |

X, XX, XXXX = Specific Device Code  
A = Assembly Location  
L = Lot Code  
Y = Year Code  
W = Week Code  
M = Date Code  
▪ = Pb-Free Package

### ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 6 of this data sheet.

# NL27WZ02

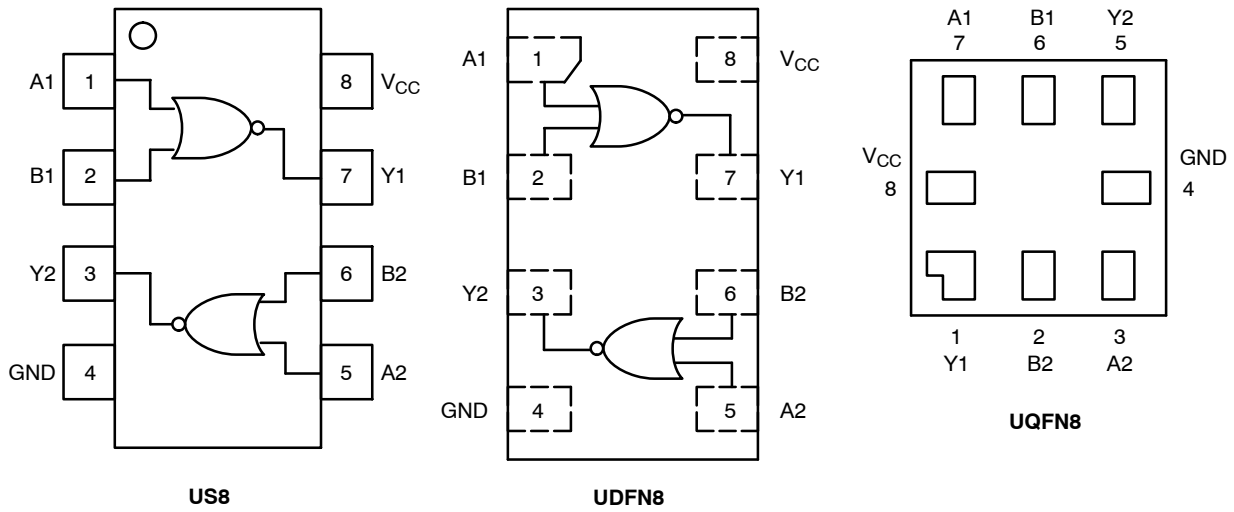


Figure 2. Pinout

### PIN ASSIGNMENT (US8 / UDFN8)

| Pin | Function        |
|-----|-----------------|
| 1   | A1              |
| 2   | B1              |
| 3   | Y2              |
| 4   | GND             |
| 5   | A2              |
| 6   | B2              |
| 7   | Y1              |
| 8   | V <sub>CC</sub> |

### PIN ASSIGNMENT (UQFN8)

| Pin | Function        |
|-----|-----------------|
| 1   | Y1              |
| 2   | B2              |
| 3   | A2              |
| 4   | GND             |
| 5   | Y2              |
| 6   | B1              |
| 7   | A1              |
| 8   | V <sub>CC</sub> |

### FUNCTION TABLE

| Inputs |   | Output |
|--------|---|--------|
| A      | B | Y      |
| L      | L | H      |
| L      | H | L      |
| H      | L | L      |
| H      | H | L      |

H = HIGH Logic Level  
L = LOW Logic Level

# NL27WZ02

## MAXIMUM RATINGS

| Symbol                              | Characteristics                                | Value   | Unit  |      |
|-------------------------------------|--|---|---|------|
| V <sub>CC</sub>                     | DC Supply Voltage                              | -0.5 to +6.5  | V   |      |
| V <sub>IN</sub>                     | DC Input Voltage                               | -0.5 to +6.5  | V   |      |
|                                     | DC Output Voltage                              | Active-Mode (High or Low State)<br>Tri-State Mode (Note 1)<br>Power-Down Mode (V <sub>CC</sub> = 0 V) | -0.5 to V <sub>CC</sub> + 0.5<br>-0.5 to +6.5<br>-0.5 to +6.5 | V    |
| I <sub>IK</sub>                     | DC Input Diode Current                         | V <sub>IN</sub> < GND   | -50   | mA   |
| I <sub>OK</sub>                     | DC Output Diode Current                        | V <sub>OUT</sub> < GND  | -50   | mA   |
| I <sub>OUT</sub>                    | DC Output Source/Sink Current                  |   | ±50   | mA   |
| I <sub>CC</sub> or I <sub>GND</sub> | DC Supply Current per Supply Pin or Ground Pin |   | ±100  | mA   |
| T <sub>STG</sub>                    | Storage Temperature Range                      |   | -65 to +150   | °C   |
| T <sub>L</sub>                      | Lead Temperature, 1 mm from Case for 10 secs   |   | 260   | °C   |
| T <sub>J</sub>                      | Junction Temperature Under Bias                |   | +150  | °C   |
| θ <sub>JA</sub>                     | Thermal Resistance (Note 2)                    | US8   | 250   | °C/W |
|                                     |  | UQFN8   | 210   |      |
|                                     |  | UDFN8   | 231   |      |
| P <sub>D</sub>                      | Power Dissipation in Still Air                 | US8   | 500   | mW   |
|                                     |  | UQFN8   | 595   |      |
|                                     |  | UDFN8   | 541   |      |
| MSL                                 | Moisture Sensitivity                           |   | Level 1   | -    |
| F <sub>R</sub>                      | Flammability Rating                            | Oxygen Index: 28 to 34  | UL 94 V-0 @ 0.125 in  | -    |
| V <sub>ESD</sub>                    | ESD Withstand Voltage (Note 3)                 | Human Body Model  | 2000  | V    |
|                                     |  | Charged Device Model  | 1000  |      |
| I <sub>Latchup</sub>                | Latchup Performance (Note 4)                   |   | ± 100   | mA   |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Applicable to devices with outputs that may be tri-stated.
2. Measured with minimum pad spacing on an FR4 board, using 10mm-by-1inch, 2 ounce copper trace no air flow per JESD51-7.
3. HBM tested to ANSI/ESDA/JEDEC JS-001-2017. CDM tested to EIA/JESD22-C101-F. JEDEC recommends that ESD qualification to EIA/JESD22-A115-A (Machine Model) be discontinued per JEDEC/JEP172A.
4. Tested to EIA/JESD78 Class II.

## RECOMMENDED OPERATING CONDITIONS

| Symbol                          | Characteristics             | Min                                     | Max             | Unit |      |
|---------------------------------|-----------------------------|---|-----------------|------|------|
| V <sub>CC</sub>                 | Positive DC Supply Voltage  | 1.65                                    | 5.5             | V    |      |
| V <sub>IN</sub>                 | DC Input Voltage            | 0                                       | 5.5             | V    |      |
| V <sub>OUT</sub>                | DC Output Voltage           | Active-Mode (High or Low State)         | V <sub>CC</sub> |      |      |
|                                 |                             | Tri-State Mode (Note 1)                 | 5.5             |      |      |
|                                 |                             | Power-Down Mode (V <sub>CC</sub> = 0 V) | 5.5             |      |      |
| T <sub>A</sub>                  | Operating Temperature Range | -55                                     | +125            | °C   |      |
| t <sub>r</sub> , t <sub>f</sub> | Input Rise and Fall Time    | V <sub>CC</sub> = 1.65 V to 1.95 V      | 0               | 20   | ns/V |
|                                 |                             | V <sub>CC</sub> = 2.3 V to 2.7 V        | 0               | 20   |      |
|                                 |                             | V <sub>CC</sub> = 3.0 V to 3.6 V        | 0               | 10   |      |
|                                 |                             | V <sub>CC</sub> = 4.5 V to 5.5 V        | 0               | 5    |      |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

# NL27WZ02

## DC ELECTRICAL CHARACTERISTICS

| Symbol           | Parameter                 | Condition   | V <sub>CC</sub> (V) | T <sub>A</sub> = 25°C |                 |                      | -55°C ≤ T <sub>A</sub> ≤ 125°C |                      | Units |
|------------------|---------------------------|---|---------------------|-----------------------|-----------------|----------------------|--------------------------------|----------------------|-------|
|                  |                           |   |                     | Min                   | Typ             | Max                  | Min                            | Max                  |       |
| V <sub>IH</sub>  | High-Level Input Voltage  |   | 1.65 to 1.95        | 0.65 V <sub>CC</sub>  | -               | -                    | 0.65 V <sub>CC</sub>           | -                    | V     |
|                  |                           |   | 2.3 to 5.5          | 0.70 V <sub>CC</sub>  | -               | -                    | 0.70 V <sub>CC</sub>           | -                    |       |
| V <sub>IL</sub>  | Low-Level Input Voltage   |   | 1.65 to 1.95        | -                     | -               | 0.35 V <sub>CC</sub> | -                              | 0.35 V <sub>CC</sub> | V     |
|                  |                           |   | 2.3 to 5.5          | -                     | -               | 0.30 V <sub>CC</sub> | -                              | 0.30 V <sub>CC</sub> |       |
| V <sub>OH</sub>  | High-Level Output Voltage | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OH</sub> = -100 μA<br>I <sub>OH</sub> = -4 mA<br>I <sub>OH</sub> = -8 mA<br>I <sub>OH</sub> = -12 mA<br>I <sub>OH</sub> = -16 mA<br>I <sub>OH</sub> = -24 mA<br>I <sub>OH</sub> = -32 mA | 1.65 to 5.5         | V <sub>CC</sub> - 0.1 | V <sub>CC</sub> | -                    | V <sub>CC</sub> - 0.1          | -                    | V     |
|                  |                           |   | 1.65                | 1.29                  | 1.4             | -                    | 1.29                           | -                    |       |
|                  |                           |   | 2.3                 | 1.9                   | 2.1             | -                    | 1.9                            | -                    |       |
|                  |                           |   | 2.7                 | 2.2                   | 2.4             | -                    | 2.2                            | -                    |       |
|                  |                           |   | 3.0                 | 2.4                   | 2.7             | -                    | 2.4                            | -                    |       |
|                  |                           |   | 3.0                 | 2.3                   | 2.5             | -                    | 2.3                            | -                    |       |
|                  |                           |   | 4.5                 | 3.8                   | 4.0             | -                    | 3.8                            | -                    |       |
| V <sub>OL</sub>  | Low-Level Output Voltage  | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OL</sub> = 100 μA<br>I <sub>OL</sub> = 4 mA<br>I <sub>OL</sub> = 8 mA<br>I <sub>OL</sub> = 12 mA<br>I <sub>OL</sub> = 16 mA<br>I <sub>OL</sub> = 24 mA<br>I <sub>OL</sub> = 32 mA        | 1.65 to 5.5         | -                     | -               | 0.1                  | -                              | 0.1                  | V     |
|                  |                           |   | 1.65                | -                     | 0.08            | 0.24                 | -                              | 0.24                 |       |
|                  |                           |   | 2.3                 | -                     | 0.2             | 0.3                  | -                              | 0.3                  |       |
|                  |                           |   | 2.7                 | -                     | 0.22            | 0.4                  | -                              | 0.4                  |       |
|                  |                           |   | 3.0                 | -                     | 0.28            | 0.4                  | -                              | 0.4                  |       |
|                  |                           |   | 3.0                 | -                     | 0.38            | 0.55                 | -                              | 0.55                 |       |
|                  |                           |   | 4.5                 | -                     | 0.42            | 0.55                 | -                              | 0.55                 |       |
| I <sub>IN</sub>  | Input Leakage Current     | V <sub>IN</sub> = 5.5 V or GND  | 1.65 to 5.5         | -                     | -               | ±0.1                 | -                              | ±1.0                 | μA    |
| I <sub>OFF</sub> | Power Off Leakage Current | V <sub>IN</sub> = 5.5 V or V <sub>OUT</sub> = 5.5 V   | 0                   | -                     | -               | 1.0                  | -                              | 10                   | μA    |
| I <sub>CC</sub>  | Quiescent Supply Current  | V <sub>IN</sub> = V <sub>CC</sub> or GND  | 5.5                 | -                     | -               | 1.0                  | -                              | 10                   | μA    |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## AC ELECTRICAL CHARACTERISTICS

| Symbol                                 | Parameter                           | V <sub>CC</sub> (V) | Test Conditions  | T <sub>A</sub> = 25°C  |     |     | T <sub>A</sub> = -55 to 125°C |     | Units |
|--|-------------------------------------|---------------------|--|--|-----|-----|-------------------------------|-----|-------|
|  |                                     |                     |  | Min  | Typ | Max | Min                           | Max |       |
| t <sub>PLH</sub> ,<br>t <sub>PHL</sub> | Propagation Delay,<br>(A or B) to Y | 1.65 to 1.95        | C <sub>L</sub> = 15 pF<br>R <sub>L</sub> = 1 MΩ<br>R <sub>1</sub> = Open | -  | 7.4 | 9.5 | -                             | 9.7 | ns    |
|  |                                     | 2.3 to 2.7          |  | -  | 3.3 | 5.4 | -                             | 5.8 |       |
|  |                                     | 3.0 to 3.6          |  | -  | 2.6 | 3.9 | -                             | 4.3 |       |
|  |                                     | 4.5 to 5.5          |  | -  | 1.9 | 3.1 | -                             | 3.3 |       |
|  |                                     | 3.0 to 3.6          |  | C <sub>L</sub> = 50 pF,<br>R <sub>L</sub> = 500 Ω, R <sub>1</sub> = Open | -   | 3.2 | 4.8                           | -   |       |
|  |                                     | 4.5 to 5.5          | -  |  | 2.5 | 3.7 | -                             | 4.0 |       |

## CAPACITIVE CHARACTERISTICS

| Symbol           | Parameter                                 | Condition  | Typical | Units |
|------------------|---|--|---------|-------|
| C <sub>IN</sub>  | Input Capacitance                         | V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub>  | 2.5     | pF    |
| C <sub>OUT</sub> | Output Capacitance                        | V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub>  | 2.5     | pF    |
| C <sub>PD</sub>  | Power Dissipation Capacitance<br>(Note 5) | 10 MHz, V <sub>CC</sub> = 3.3 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub><br>10 MHz, V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub> | 9<br>11 | pF    |

5. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no-load dynamic power consumption; P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.

# NL27WZ02

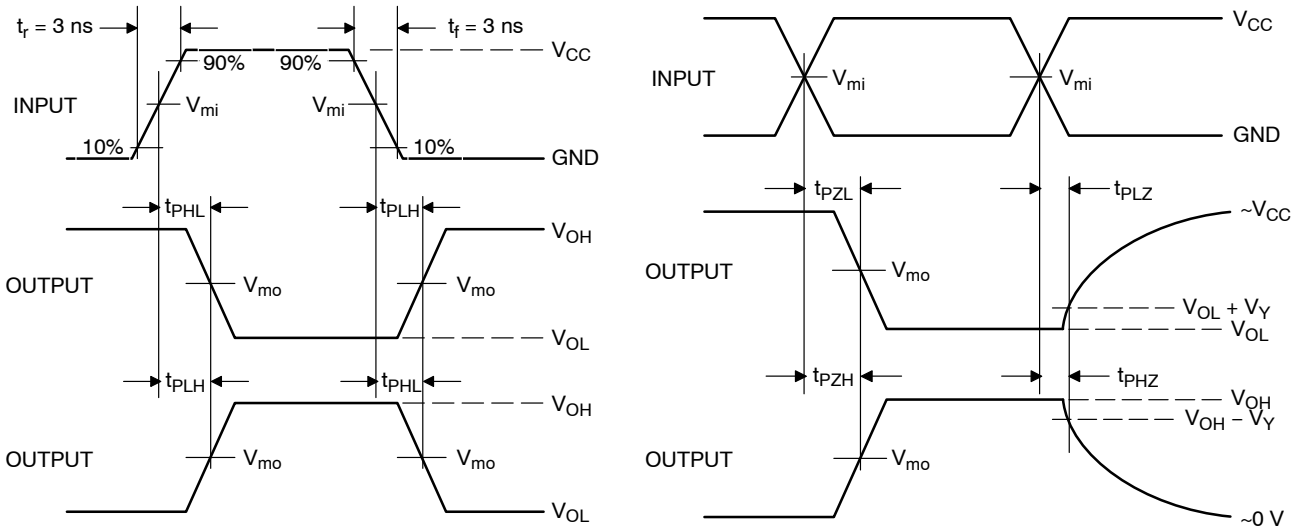


$C_L$  includes probe and jig capacitance  
 $R_T$  is  $Z_{OUT}$  of pulse generator (typically 50  $\Omega$ )  
 $f = 1$  MHz

**Figure 3. Test Circuit**

| Test                | Switch Position   | $C_L$ , pF                   | $R_L$ , $\Omega$ | $R_1$ , $\Omega$ |
|---------------------|-------------------|------------------------------|------------------|------------------|
| $t_{PLH} / t_{PHL}$ | Open              | See AC Characteristics Table |                  |                  |
| $t_{PLZ} / t_{PZL}$ | $2 \times V_{CC}$ | 50                           | 500              | 500              |
| $t_{PHZ} / t_{PZH}$ | GND               | 50                           | 500              | 500              |

X = Don't Care



**Figure 4. Switching Waveforms**

| $V_{CC}$ , V | $V_{mi}$ , V | $V_{mo}$ , V          |   | $V_Y$ , V |
|--------------|--------------|-----------------------|---|-----------|
|              |              | $t_{PLH}$ , $t_{PHL}$ | $t_{PZL}$ , $t_{PLZ}$ , $t_{PZH}$ , $t_{PHZ}$ |           |
| 1.65 to 1.95 | $V_{CC}/2$   | $V_{CC}/2$            | $V_{CC}/2$                                    | 0.15      |
| 2.3 to 2.7   | $V_{CC}/2$   | $V_{CC}/2$            | $V_{CC}/2$                                    | 0.15      |
| 3.0 to 3.6   | $V_{CC}/2$   | $V_{CC}/2$            | $V_{CC}/2$                                    | 0.3       |
| 4.5 to 5.5   | $V_{CC}/2$   | $V_{CC}/2$            | $V_{CC}/2$                                    | 0.3       |

# NL27WZ02

## DEVICE ORDERING INFORMATION

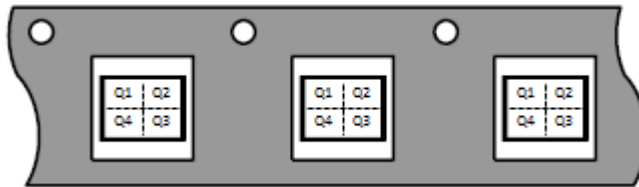
| Device                             | Packages                 | Specific Device Code | Pin 1 Orientation<br>(See below) | Shipping†          |
|------------------------------------|--------------------------|----------------------|----------------------------------|--------------------|
| NL27WZ02USG                        | US8                      | L3                   | Q4                               | 3000 / Tape & Reel |
| NL27WZ02MQ1TCG<br>(In Development) | UQFN8, 1.6 x 1.6, 0.5P   | TBD                  | TBD                              | 3000 / Tape & Reel |
| NL27WZ02MU1TCG                     | UDFN8, 1.95 x 1.0, 0.5P  | AL                   | Q4                               | 3000 / Tape & Reel |
| NL27WZ02MU1TWG                     | UDFN8, 1.95 x 1.0, 0.5P  | AM                   | Q1                               | 3000 / Tape & Reel |
| NL27WZ02MU3TCG                     | UDFN8, 1.45 x 1.0, 0.35P | J                    | Q4                               | 3000 / Tape & Reel |
| NL27WZ02MU2TCG                     | UDFN8, 1.6 x 1.0, 0.4P   | AH                   | Q4                               | 3000 / Tape & Reel |
| NL27WZ02MQ2TCG<br>(In Development) | UQFN8, 1.4 x 1.2, 0.4P   | TBD                  | TBD                              | 3000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

### Pin 1 Orientation in Tape and Reel

Direction of Feed



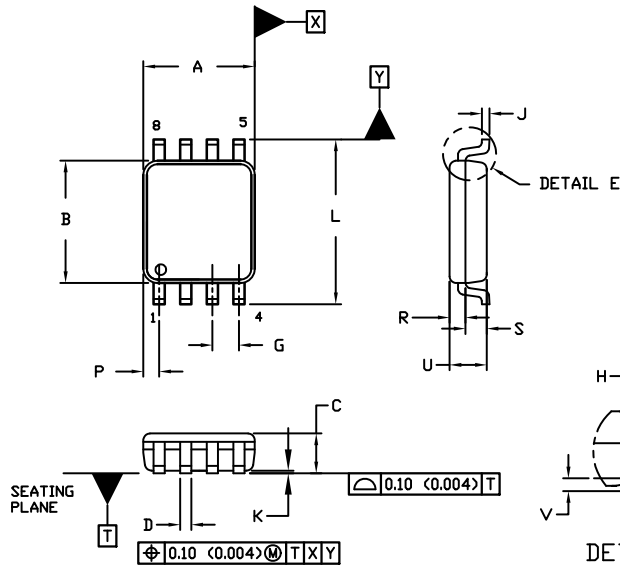
# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



SCALE 4:1

**US8**  
CASE 493  
ISSUE F

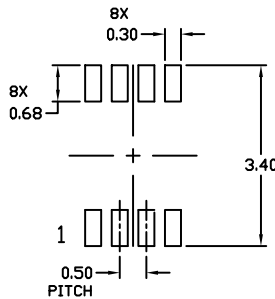
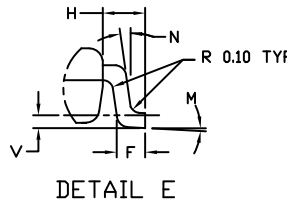
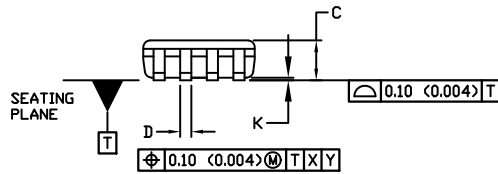
DATE 01 SEP 2021



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSION, OR GATE BURR. MOLD FLASH, PROTRUSION, OR GATE BURR SHALL NOT EXCEED 0.14 (0.0055") PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH AND PROTRUSION SHALL NOT EXCEED 0.14 (0.0055") PER SIDE.
5. LEAD FINISH IS SOLDER PLATING WITH THICKNESS OF 0.0076-0.0203 MM (0.003-0.008").
6. ALL TOLERANCE UNLESS OTHERWISE SPECIFIED ±0.0508 MM (0.002").

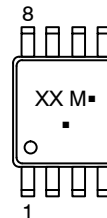
| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN.        | MAX. | MIN.      | MAX.  |
| A   | 1.90        | 2.10 | 0.075     | 0.083 |
| B   | 2.20        | 2.40 | 0.087     | 0.094 |
| C   | 0.60        | 0.90 | 0.024     | 0.035 |
| D   | 0.17        | 0.25 | 0.007     | 0.010 |
| F   | 0.20        | 0.35 | 0.008     | 0.014 |
| G   | 0.50 BSC    |      | 0.020 BSC |       |
| H   | 0.40 REF    |      | 0.016 REF |       |
| J   | 0.10        | 0.18 | 0.004     | 0.007 |
| K   | 0.00        | 0.10 | 0.000     | 0.004 |
| L   | 3.00        | 3.25 | 0.118     | 0.128 |
| M   | 0°          | 6°   | 0°        | 6°    |
| N   | 0°          | 10°  | 0°        | 10°   |
| P   | 0.23        | 0.34 | 0.010     | 0.013 |
| R   | 0.23        | 0.33 | 0.009     | 0.013 |
| S   | 0.37        | 0.47 | 0.015     | 0.019 |
| U   | 0.60        | 0.80 | 0.024     | 0.031 |
| V   | 0.12 BSC    |      | 0.005 BSC |       |



**RECOMMENDED \*  
MOUNTING FOOTPRINT**

\* For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

**GENERIC  
MARKING DIAGRAM\***



- XX = Specific Device Code
- M = Date Code
- = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

|                         |                    |  |
|-------------------------|--------------------|--|
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| <b>DESCRIPTION:</b>     | <b>US8</b>         | <b>PAGE 1 OF 1</b>   |

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# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

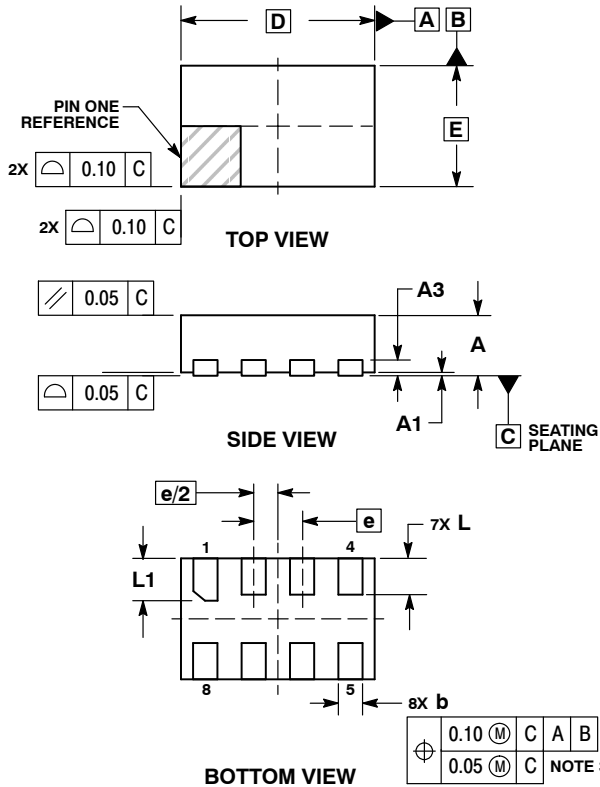
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**UDFN8, 1.6x1, 0.4P**  
CASE 517BY-01  
ISSUE O

SCALE 4:1

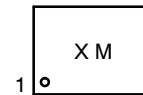
DATE 18 MAY 2011



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
  4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

| MILLIMETERS |      |      |
|-------------|------|------|
| DIM         | MIN  | MAX  |
| A           | 0.45 | 0.55 |
| A1          | 0.00 | 0.05 |
| A3          | 0.13 | REF  |
| b           | 0.15 | 0.25 |
| D           | 1.60 | BSC  |
| E           | 1.00 | BSC  |
| e           | 0.40 | BSC  |
| L           | 0.25 | 0.35 |
| L1          | 0.30 | 0.40 |

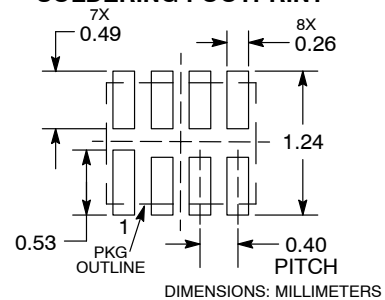
### GENERIC MARKING DIAGRAM\*



X = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "▪", may or may not be present.

### RECOMMENDED SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

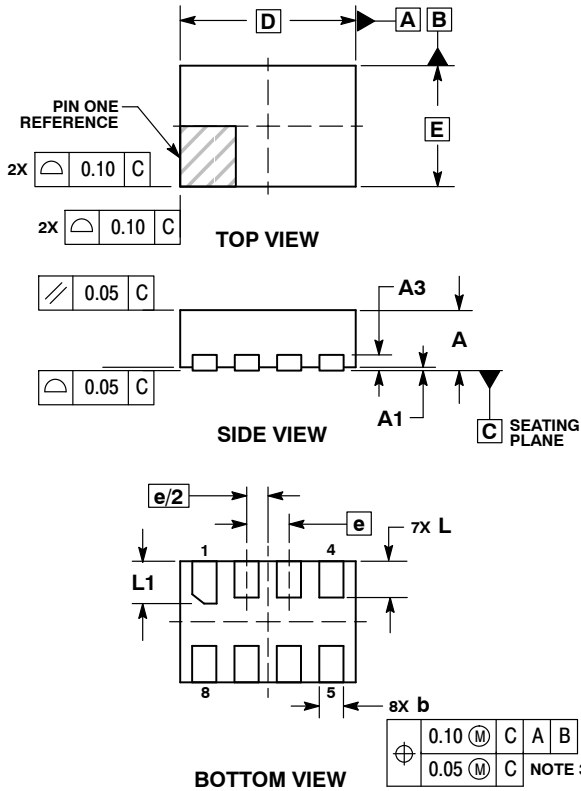
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UDFN8, 1.45x1, 0.35P  
CASE 517BZ-01  
ISSUE O

SCALE 4:1

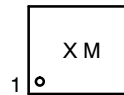
DATE 18 MAY 2011



- NOTES:
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  - CONTROLLING DIMENSION: MILLIMETERS.
  - DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
  - PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

| MILLIMETERS |      |      |
|-------------|------|------|
| DIM         | MIN  | MAX  |
| A           | 0.45 | 0.55 |
| A1          | 0.00 | 0.05 |
| A3          | 0.13 | REF  |
| b           | 0.15 | 0.25 |
| D           | 1.45 | BSC  |
| E           | 1.00 | BSC  |
| e           | 0.35 | BSC  |
| L           | 0.25 | 0.35 |
| L1          | 0.30 | 0.40 |

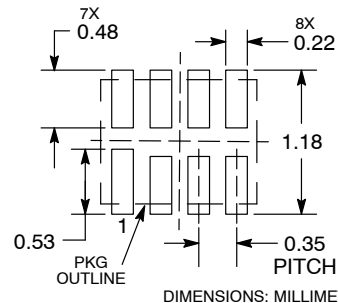
### GENERIC MARKING DIAGRAM\*



X = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "▪", may or may not be present.

### RECOMMENDED SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

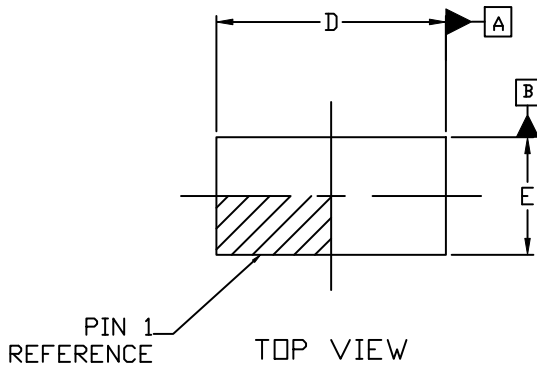
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SCALE 4:1

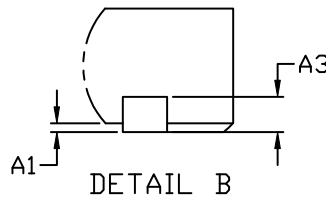
**UDFN8, 1.95x1.0, 0.5P**  
**CASE 517CA**  
**ISSUE A**

DATE 22 SEP 2020

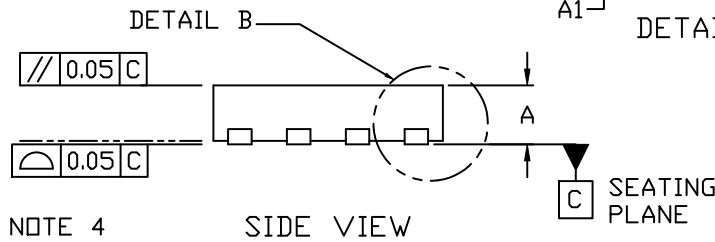


**NOTES:**

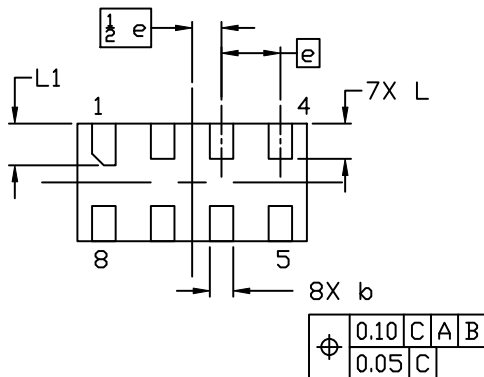
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION *b* APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO ALL THE TERMINALS.
5. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.



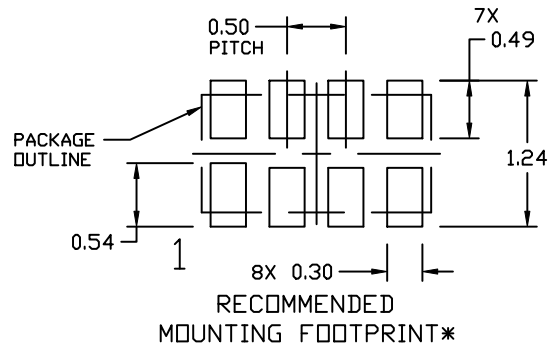
| DIM      | MILLIMETERS |      |      |
|----------|-------------|------|------|
|          | MIN.        | NOM. | MAX. |
| A        | 0.45        | 0.50 | 0.55 |
| A1       | 0.00        | ---  | 0.05 |
| A3       | 0.13 REF    |      |      |
| <i>b</i> | 0.15        | 0.20 | 0.25 |
| D        | 1.85        | 1.95 | 2.05 |
| E        | 0.90        | 1.00 | 1.10 |
| <i>e</i> | 0.50 BSC    |      |      |
| L        | 0.25        | 0.30 | 0.35 |
| L1       | 0.30        | 0.35 | 0.40 |



NOTE 4

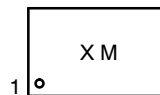


BOTTOM VIEW NOTE 3



\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

**GENERIC MARKING DIAGRAM\***



X = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

|                         |                              |  |
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