

# IGBT NGTB45N60SWG

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss. The IGBT is well suited for half bridge resonant applications. Incorporated into the device is a soft and fast co–packaged free wheeling diode with a low forward voltage.

#### **Features**

- Low Saturation Voltage using Trench with Fieldstop Technology
- Low Switching Loss Reduces System Power Dissipation
- Low Gate Charge
- Soft, Fast Free Wheeling Diode
- These are Pb-Free Devices

## **Typical Applications**

- Inductive Heating
- Soft Switching

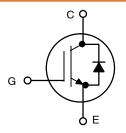
#### **ABSOLUTE MAXIMUM RATINGS**

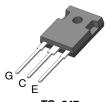
Rating	Symbol	Value	Unit
Collector-emitter Voltage	V <sub>CES</sub>	600	V
Collector Current @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	I <sub>C</sub>	90 45	Α
Pulsed Collector Current, T <sub>pulse</sub> Limited by T <sub>Jmax</sub>	I <sub>CM</sub>	180	Α
Diode Forward Current @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	I <sub>F</sub>	90 45	Α
Diode Pulsed Current, T <sub>pulse</sub> Limited by T <sub>Jmax</sub>	I <sub>FM</sub>	180	Α
Gate-emitter Voltage	$V_{GE}$	±20	٧
Power Dissipation @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	P <sub>D</sub>	250 50	W
Operating Junction Temperature Range	TJ	–55 to +150	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T <sub>SLD</sub>	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1

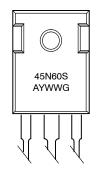
45 A, 600 V V<sub>CEsat</sub> = 2.2 V E<sub>off</sub> = 0.55 mJ





TO-247 CASE 340AM

#### **MARKING DIAGRAM**



45N60S = Specific Device Code A = Assembly Location

Y = Year
WW = Work Week
G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
NGTB45N60SWG	TO-247 (Pb-Free)	30 Units / Rail

## THERMAL CHARACTERISTICS

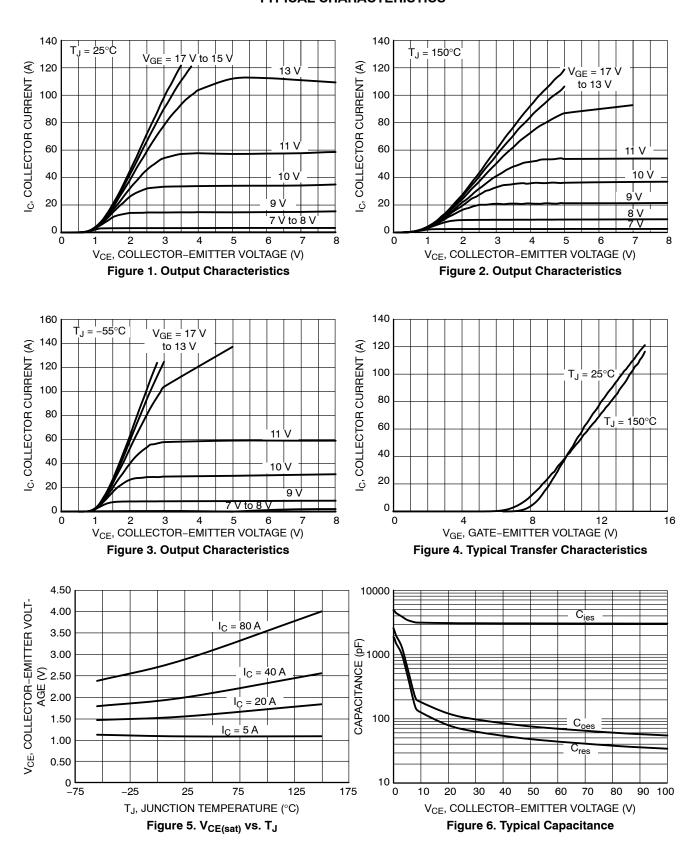
Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ heta JC}$	0.87	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ heta JC}$	1.47	°C/W
Thermal resistance junction-to-ambient	$R_{ heta JA}$	40	°C/W

## **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC	•	•				
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE}$ = 0 V, $I_C$ = 500 $\mu A$	V <sub>(BR)CES</sub>	600	-	-	V
Collector-emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 45 A V <sub>GE</sub> = 15 V, I <sub>C</sub> = 45 A, T <sub>J</sub> = 150°C	V <sub>CEsat</sub>	-	2.2 2.6	2.4	V
Gate-emitter threshold voltage	V <sub>GE</sub> = V <sub>CE</sub> , I <sub>C</sub> = 150 μA	V <sub>GE(th)</sub>	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 600 V V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 600 V, T <sub>J</sub> = 150°C	I <sub>CES</sub>	-	_ _	0.2 2	mA
Gate leakage current, collector-emitter short-circuited	V <sub>GE</sub> = 20 V , V <sub>CE</sub> = 0 V	I <sub>GES</sub>	_	-	100	nA
DYNAMIC CHARACTERISTIC	•	•				
Input capacitance		C <sub>ies</sub>	-	3100	-	pF
Output capacitance	V <sub>CE</sub> = 20 V, V <sub>GE</sub> = 0 V, f = 1 MHz	C <sub>oes</sub>	-	120	-	1
Reverse transfer capacitance	7	C <sub>res</sub>	_	80	-	
Gate charge total		$Q_g$		134		nC
Gate to emitter charge	V <sub>CE</sub> = 480 V, I <sub>C</sub> = 45 A, V <sub>GE</sub> = 15 V	Q <sub>ge</sub>		27		
Gate to collector charge	7	Q <sub>gc</sub>		67		
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-on delay time		t <sub>d(on)</sub>		70		ns
Rise time	T <sub>J</sub> = 25°C	t <sub>r</sub>		34		1
Turn-off delay time	$V_{CC} = 400 \text{ V, } I_{C} = 45 \text{ A}$ $R_{g} = 10 \Omega$	t <sub>d(off)</sub>		144		1
Fall time	$V_{GE} = 0 \text{ V} / 15 \text{ V}$	t <sub>f</sub>		68		
Turn-off switching loss	7	E <sub>off</sub>		0.55		mJ
Turn-on delay time		t <sub>d(on)</sub>		70		ns
Rise time	T <sub>J</sub> = 150°C	t <sub>r</sub>		39		
Turn-off delay time	$V_{CC} = 400 \text{ V, } I_{C} = 45 \text{ A}$ $R_{g} = 10 \Omega$	t <sub>d(off)</sub>		151		
Fall time	$V_{GE} = 0 \text{ V} / 15 \text{ V}$	t <sub>f</sub>		88		1
Turn-off switching loss	7	E <sub>off</sub>		0.89		mJ
DIODE CHARACTERISTIC	•					
Forward voltage	V <sub>GE</sub> = 0 V, I <sub>F</sub> = 20 A V <sub>GE</sub> = 0 V, I <sub>F</sub> = 20 A, T <sub>J</sub> = 150°C	V <sub>F</sub>		1.1 1.03	1.4	V
Reverse recovery time	T <sub>J</sub> = 25°C	t <sub>rr</sub>		376		ns
Reverse recovery charge	$I_F = 25 \text{ Å}, V_R = 200 \text{ V}$ $di_F/dt = 200 \text{ A}/\mu \text{s}$	Q <sub>rr</sub>		4145		nc
Reverse recovery current	7	I <sub>rrm</sub>		22		Α

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **TYPICAL CHARACTERISTICS**



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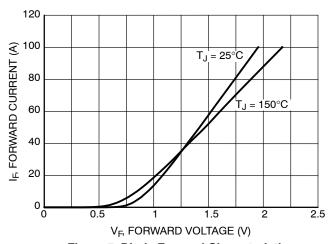


Figure 7. Diode Forward Characteristics

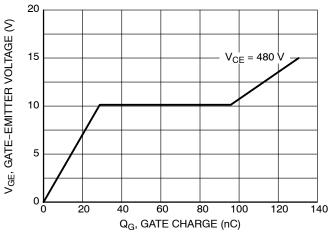


Figure 8. Typical Gate Charge

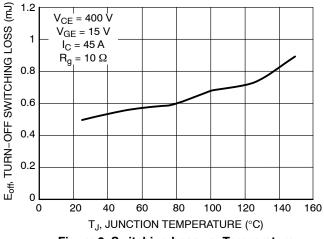


Figure 9. Switching Loss vs. Temperature

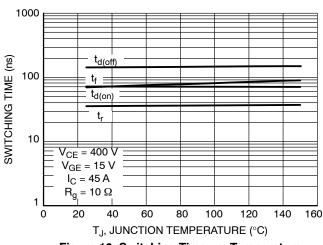


Figure 10. Switching Time vs. Temperature

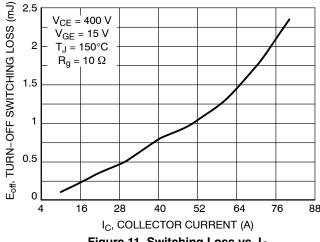


Figure 11. Switching Loss vs. I<sub>C</sub>

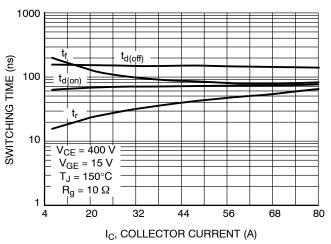


Figure 12. Switching Time vs. Current

#### **TYPICAL CHARACTERISTICS**

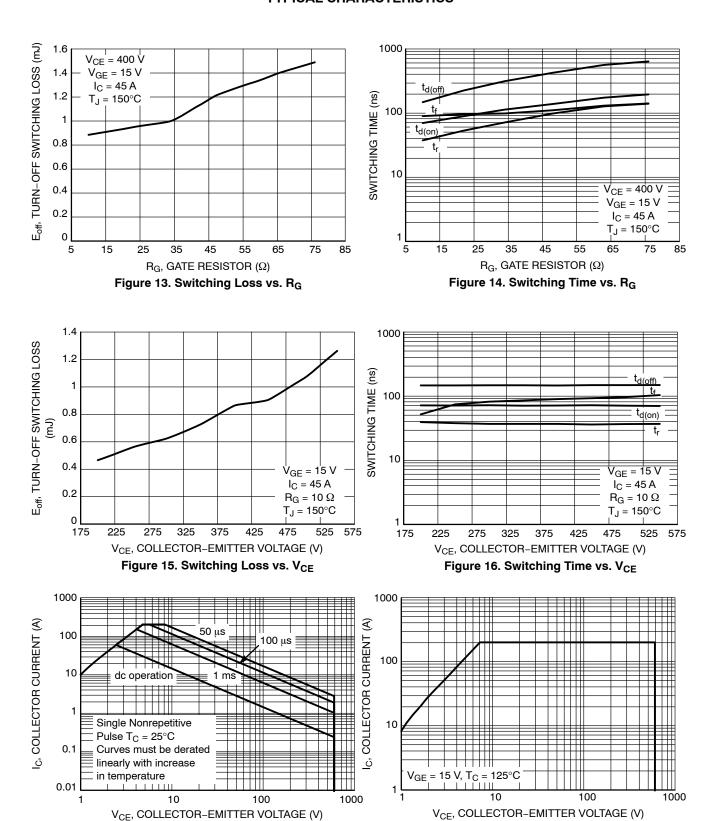


Figure 17. Safe Operating Area

Figure 18. Reverse Bias Safe Operating Area

#### **TYPICAL CHARACTERISTICS**

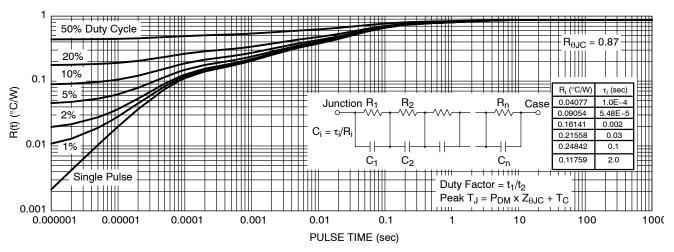


Figure 19. IGBT Transient Thermal Impedance

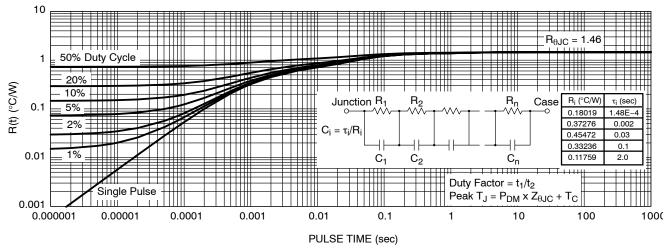


Figure 20. Diode Transient Thermal Impedance

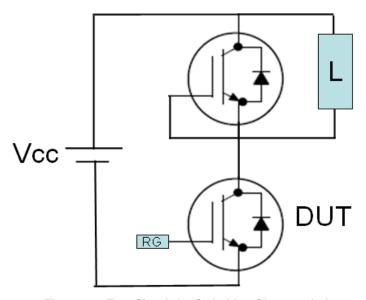


Figure 21. Test Circuit for Switching Characteristics

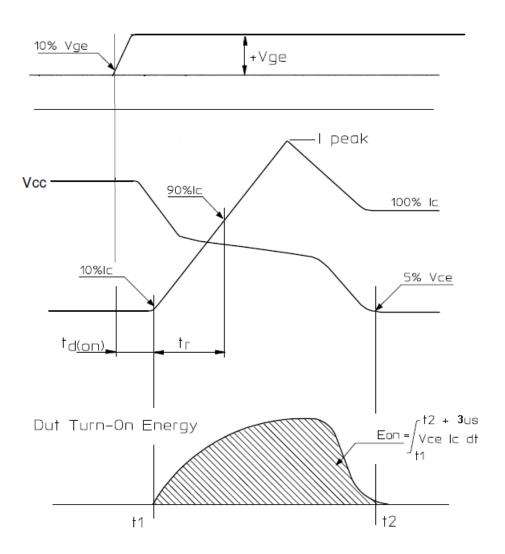


Figure 22. Definition of Turn On Waveform

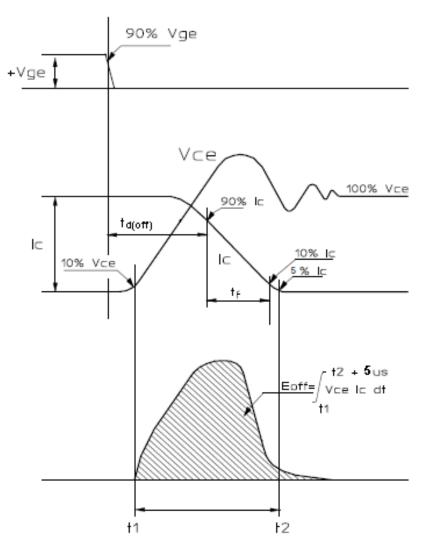
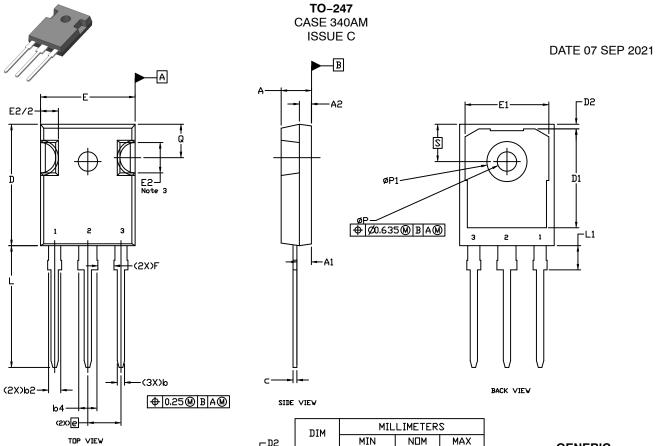
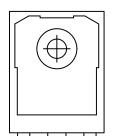
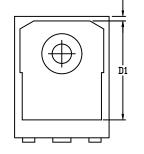


Figure 23. Definition of Turn Off Waveform









NOTE 4 HEATSINK SHAPES

#### NOTES:

- 1. DIMENSIONING AND TOLERANCE AS PER ASME Y14.5M, 2009.
- 2. ALL DIMENSION ARE IN MILLIMETERS.
- 3. SLOT REQUIRED, NOTCH MAY BE ROUNDED.
- 4. OPTIONAL BACK SIDE HEATSINK SHAPE.
- 5. DIMENSIONS ARE EXCLUSIVE OF BURRS AND MOLD FLASH.
  DIMENSIONS D AND E ARE MEASURED AT THE OUTERMOST EXTREME
  OF THE PLASTIC BODY.
- 6. DIMENSIONS AT TO BE MEASURED IN THE REGION DEFINED BY L1.
- 7. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

	MTM	NUM	MAX	
Α	4.70	5.00	5.30	
A1	2,20	2.40	2.60	
A2	1.80	2.00	2.20	
b	1.07	1.20	1.33	
b2	1.65	2.12	2.35	
b4	2.60	3.12	3.40	
c	0.45	0.60	0.75	
D	20.80	21.00	21.34	
D1	16.30			
D2	0.75			
Ε	15.50	16.00	16.25	
E1	13.80	-		
E2	4.32	4.90	5.49	
е	5.45 BSC			
F	2.655			
L	19.80	20.00	20.80	
L1	3.81	4.20	4.35	
Р	3.55	3.60	3.65	
P1	6.60			
Q	5.40	6.00	6.20	

6.15 BSC

# GENERIC MARKING DIAGRAMS\*





XXXX = Specific Device Code

A = Assembly Location

Y = Year

WW = Work Week

G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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