ON Semiconductor

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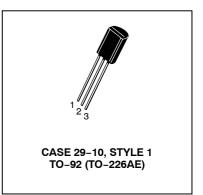
One Watt Darlington Transistors PNP Silicon

• These devices are available in Pb-free package(s). Specifications herein apply to both standard and Pb-free devices. Please see our website at www.onsemi.com for specific Pb-free orderable part numbers, or contact your local ON Semiconductor sales office or representative.

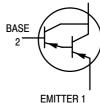
MAXIMUM RATINGS

-30 -30	Vdc Vdc
-30	Vdo
	vuc
-10	Vdc
-500	mAdc
1.0 8.0	Watt mW/°C
2.5 20	Watts mW/°C
-55 to +150	°C
_	









THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	R_{\thetaJA}	125	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	50	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage ($I_C = -100 \ \mu Adc, V_{BE} = 0$)	V _{(BR)CES}	-30	_	Vdc
Collector Cutoff Current ($V_{CB} = -30$ Vdc, $I_E = 0$)	I _{CBO}	_	-100	nAdc
Emitter Cutoff Current ($V_{EB} = -10$ Vdc, $I_C = 0$)	I _{EBO}	—	-100	nAdc

Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

MPSW63 MPSW64

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

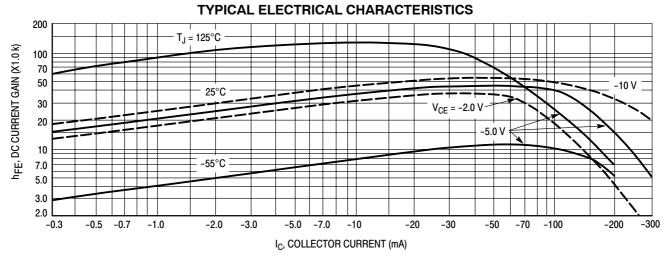
Characteristic		Min	Max	Unit
MPSW63 MPSW64	h _{FE}	5,000 10,000		—
MPSW63 MPSW64		10,000 20,000		
	V _{CE(sat)}	_	-1.5	Vdc
	V _{BE(on)}	—	-2.0	Vdc
	MPSW64 MPSW63	MPSW63 MPSW64 MPSW63 MPSW64 V _{CE(sat)}	MPSW63 MPSW64 MPSW64 MPSW64 MPSW64 VCE(sat) MPSW64 MPSW64 MPSW64 MPSW64 MPSW64 MPSW64 MPSW64 MPSW64 MPSW64 MPSW63 MPSW64 MPSW63 MPSW63 MPSW64 MPSW63 MPSW64 MPSW63 MPSW64	MPSW63 MPSW64 hFE 5,000 10,000 — MPSW63 MPSW64 10,000 20,000 — VCE(sat) — -1.5

SMALL-SIGNAL CHARACTERISTICS

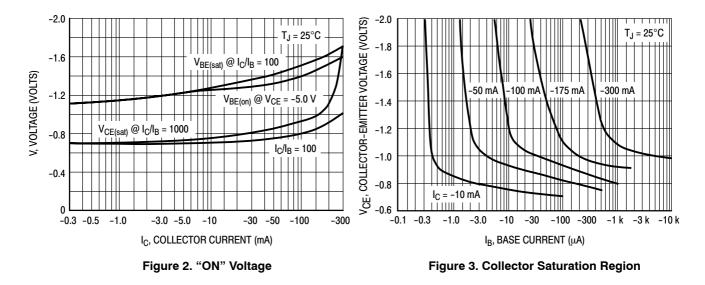
Current–Gain — Bandwidth Product ⁽²⁾	f _T	125	—	MHz
$(I_{C} = -10 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc}, f = 100 \text{ MHz})$				

1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

2. $f_T = |h_{fe}| \cdot f_{test}$.







MPSW63 MPSW64

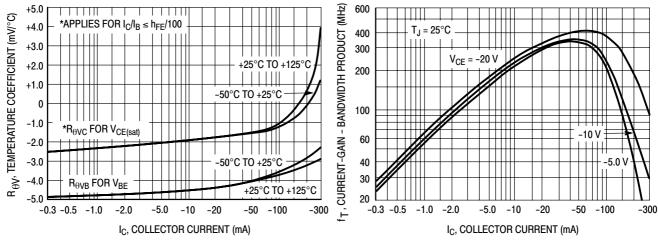


Figure 4. Temperature Coefficients

Figure 5. Current-Gain — Bandwidth Product

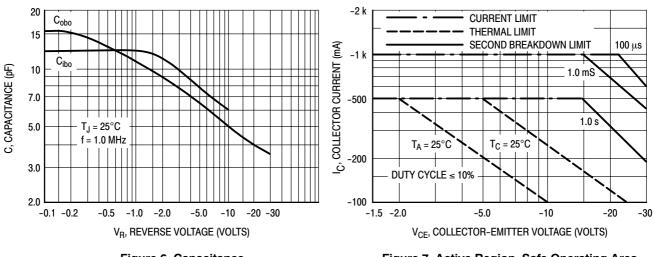
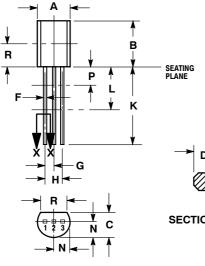


Figure 6. Capacitance

Figure 7. Active Region, Safe Operating Area

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-10 ISSUE AL





SECTION X-X

YLE 1: PIN 1. EMITTER 2. BASE 3. COLLECTOR

NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI

- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.
- 3. CONTOUR OF PACKAGE BEYOND DIMENSION R
- IS UNCONTROLLED. 4. DIMENSION F APPLIES BETWEEN P AND L.
- DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
в	0.290	0.310	7.37	7.87
c	0.125	0.165	3.18	4.19
D	0.018	0.021	0.457	0.533
F	0.016	0.019	0.407	0.482
G	0.045	0.055	1.15	1.39
Η	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.135		3.43	

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