ON Semiconductor

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NPN - MPS8099; PNP - MPS8599

Preferred Device

Amplifier Transistors

Voltage and Current are Negative for PNP Transistors

Features

• Pb–Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	80	Vdc
Collector-Base Voltage	V _{CBO}	80	Vdc
Emitter-Base Voltage	V _{EBO}	4.0	Vdc
Collector Current – Continuous	Ι _C	500	mAdc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	625 5.0	mW mW/°C
Total Device Dissipation @ $T_C = 25^{\circ}C$ Derate above $25^{\circ}C$	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 1)	R_{\thetaJA}	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

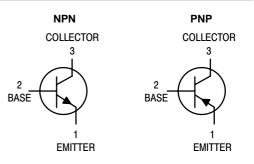
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

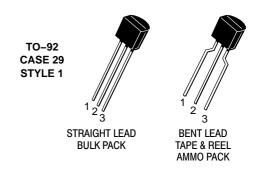
1. $R_{\theta,IA}$ is measured with the device soldered into a typical printed circuit board.



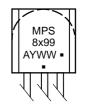
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MARKING DIAGRAM



x= 0 or 5A= Assembly LocationY= YearWW= Work Week•= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

miconductor Soldering and Mounting Techniques DERRM/D. Preferred devices are recommended choices for future use and best overall value.

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ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	·			
Collector – Emitter Breakdown Voltage (Note 2) $(I_C = 10 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	80	_	Vdc
Collector – Base Breakdown Voltage $(I_C = 100 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	80	_	Vdc
Emitter – Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}	5.0	_	Vdc
Collector Cutoff Current ($V_{CE} = 60 \text{ Vdc}, I_B = 0$)	I _{CES}	_	0.1	μAdc
Collector Cutoff Current ($V_{CB} = 80 \text{ Vdc}, I_E = 0$)	I _{CBO}	_	0.1	μAdc
Emitter Cutoff Current ($V_{EB} = 4.0 \text{ Vdc}, I_C = 0$)	I _{EBO}	_	0.1	μAdc
ON CHARACTERISTICS (Note 2)				
DC Current Gain ($I_C = 1.0 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$) ($I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$) ($I_C = 100 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$)	h _{FE}	100 100 75	300 - -	-
Collector – Emitter Saturation Voltage ($I_C = 100 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$) ($I_C = 100 \text{ mAdc}, I_B = 10 \text{ mAdc}$)	V _{CE(sat)}		0.4 0.3	Vdc
Base–Emitter On Voltage ($I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$)	V _{BE(on)}	0.6	0.8	Vdc
SMALL-SIGNAL CHARACTERISTICS			•	
Current-Gain – Bandwidth Product ($I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz}$)	fT	150	_	MHz
Output Capacitance $(V_{CB} = 5.0 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{obo}	-	8.0	pF
Input Capacitance $(V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz})$	C _{ibo}	-	30	pF

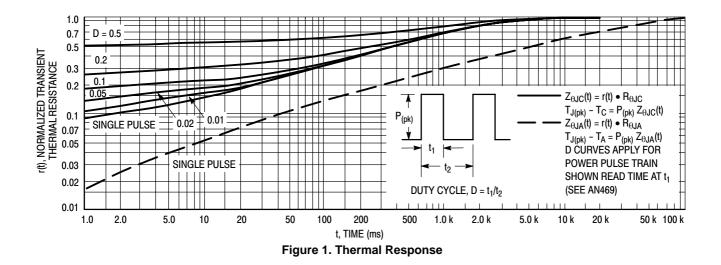
2. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle = 2.0%.

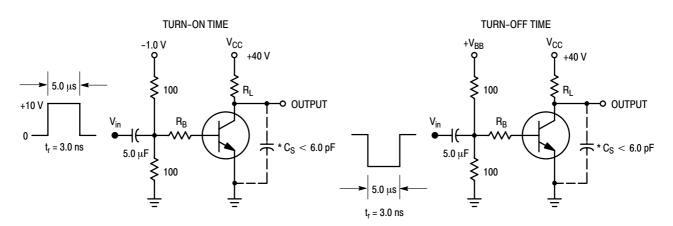
NPN - MPS8099; PNP - MPS8599

ORDERING INFORMATION

Device	Package	Shipping [†]
MPS8099	TO-92	5000 Units / Bulk
MPS8099G	TO–92 (Pb–Free)	5000 Units / Bulk
MPS8099RLRA	TO-92	2000 / Tape & Reel
MPS8099RLRAG	TO–92 (Pb–Free)	2000 / Tape & Reel
MPS8099RLRP	TO-92	2000 / Ammo Pack
MPS8099RLRPG	TO–92 (Pb–Free)	2000 / Ammo Pack
MPS8599RLRA	TO-92	2000 / Tape & Reel
MPS8599RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPS8599RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack

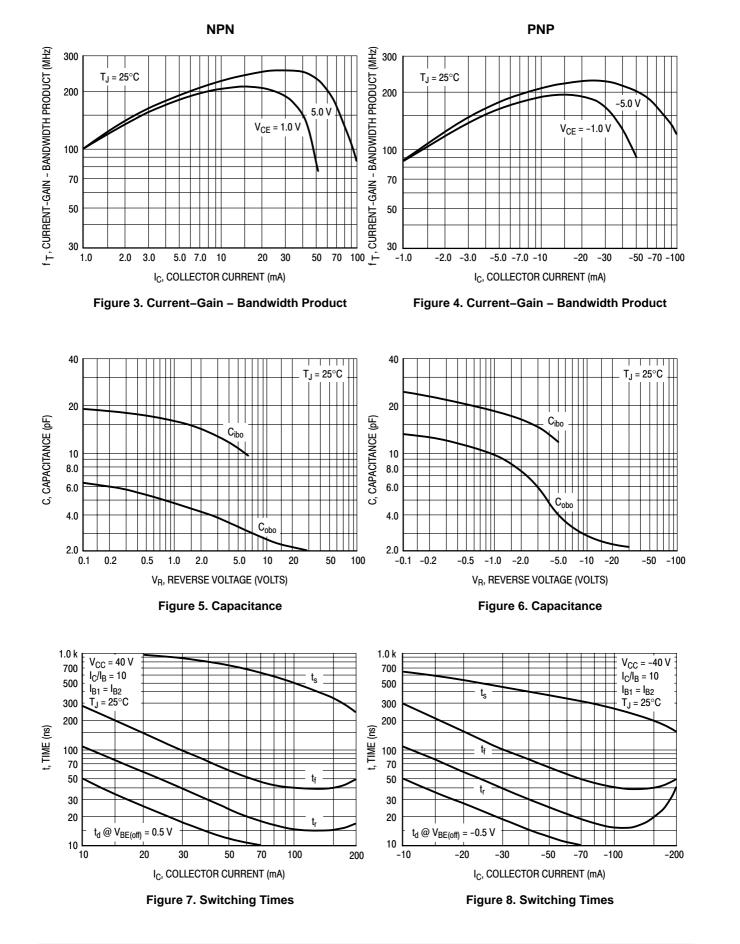
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.





*Total Shunt Capacitance of Test Jig and Connectors For PNP Test Circuits, Reverse All Voltage Polarities

Figure 2. Switching Time Test Circuits





PNP

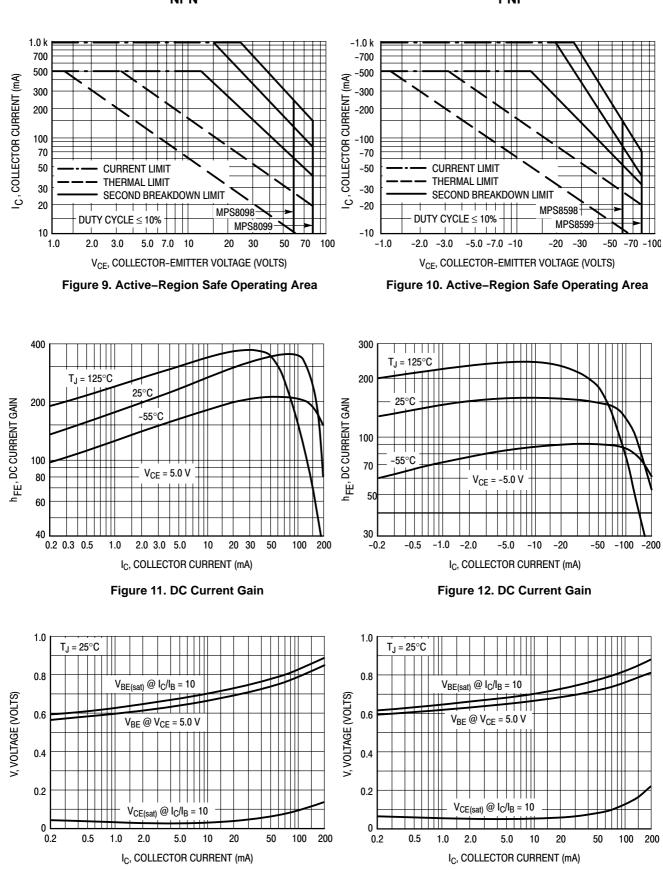


Figure 13. "ON" Voltages

Figure 14. "ON" Voltages

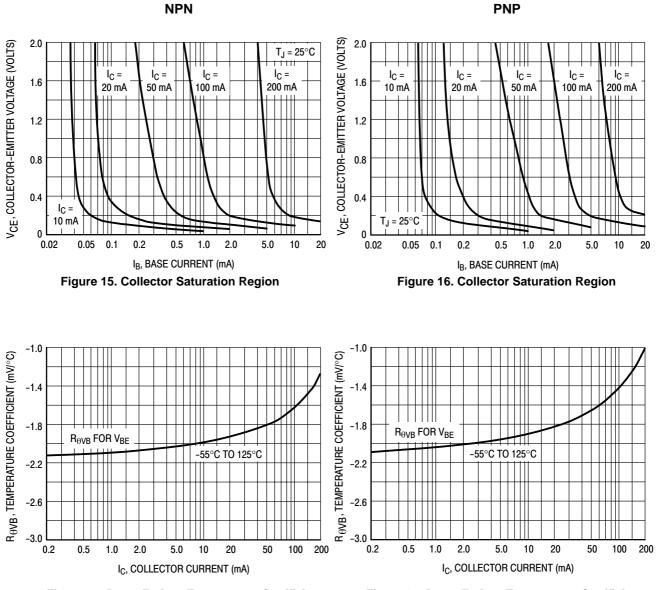
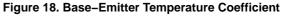
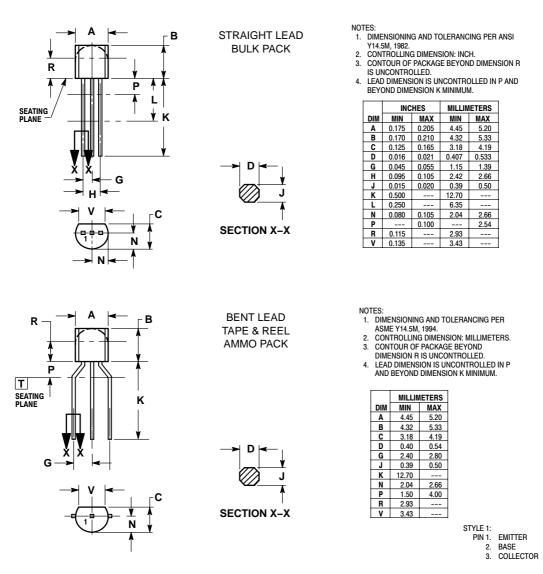


Figure 17. Base–Emitter Temperature Coefficient



PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29–11 ISSUE AM



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