

General Purpose Transistors

PNP Silicon

MMBT3906TT1

This transistor is designed for general purpose amplifier applications. It is housed in the SOT-416/SC-75 package which is designed for low power surface mount applications.

Features

- NSVM Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	-40	Vdc
Collector-Base Voltage	V _{CBO}	-40	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current - Continuous	I _C	-200	mAdc

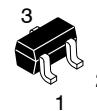
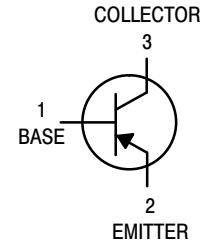
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) @T _A = 25°C Derated above 25°C	P _D	200 1.6	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	R _{θJA}	600	°C/W
Total Device Dissipation, FR-4 Board (Note 2) @T _A = 25°C Derated above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	R _{θJA}	400	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

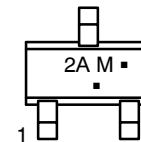
1. FR-4 @ Minimum Pad
2. FR-4 @ 1.0 × 1.0 Inch Pad

GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT



CASE 463
SOT-416/SC-75
STYLE 1

MARKING DIAGRAM



2A = Device Code
M = Date Code*
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping†
MMBT3906TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel
NSVMMBT3906TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MMBT3906TT1

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

Collector – Emitter Breakdown Voltage (Note 3) (I _C = -1.0 mA _{dc} , I _B = 0)	V _{(BR)CEO}	-40	-	V _{dc}
Collector – Base Breakdown Voltage (I _C = -10 μA _{dc} , I _E = 0)	V _{(BR)CBO}	-40	-	V _{dc}
Emitter – Base Breakdown Voltage (I _E = -10 μA _{dc} , I _C = 0)	V _{(BR)EBO}	-5.0	-	V _{dc}
Base Cutoff Current (V _{CE} = -30 V _{dc} , V _{EB} = -3.0 V _{dc})	I _{BL}	-	-50	nA _{dc}
Collector Cutoff Current (V _{CE} = -30 V _{dc} , V _{EB} = -3.0 V _{dc})	I _{CEX}	-	-50	nA _{dc}

ON CHARACTERISTICS (Note 3)

DC Current Gain (I _C = -0.1 mA _{dc} , V _{CE} = -1.0 V _{dc}) (I _C = -1.0 mA _{dc} , V _{CE} = -1.0 V _{dc}) (I _C = -10 mA _{dc} , V _{CE} = -1.0 V _{dc}) (I _C = -50 mA _{dc} , V _{CE} = -1.0 V _{dc}) (I _C = -100 mA _{dc} , V _{CE} = -1.0 V _{dc})	h _{FE}	60 80 100 60 30	- - 300 - -	-
Collector – Emitter Saturation Voltage (I _C = -10 mA _{dc} , I _B = -1.0 mA _{dc}) (I _C = -50 mA _{dc} , I _B = -5.0 mA _{dc})	V _{CE(sat)}	- -	-0.25 -0.4	V _{dc}
Base – Emitter Saturation Voltage (I _C = -10 mA _{dc} , I _B = -1.0 mA _{dc}) (I _C = -50 mA _{dc} , I _B = -5.0 mA _{dc})	V _{BE(sat)}	-0.65 -	-0.85 -0.95	V _{dc}

SMALL-SIGNAL CHARACTERISTICS

Current – Gain – Bandwidth Product (I _C = -10 mA _{dc} , V _{CE} = -20 V _{dc} , f = 100 MHz)	f _T	250	-	MHz
Output Capacitance (V _{CB} = -5.0 V _{dc} , I _E = 0, f = 1.0 MHz)	C _{obo}	-	4.5	pF
Input Capacitance ¹ (V _{EB} = -0.5 V _{dc} , I _C = 0, f = 1.0 MHz)	C _{ibo}	-	10.0	pF
Input Impedance (V _{CE} = -10 V _{dc} , I _C = -1.0 mA _{dc} , f = 1.0 kHz)	h _{ie}	2.0	12	k Ω
Voltage Feedback Ratio (V _{CE} = -10 V _{dc} , I _C = -1.0 mA _{dc} , f = 1.0 kHz)	h _{re}	0.1	10	X 10 ⁻⁴
Small – Signal Current Gain (V _{CE} = -10 V _{dc} , I _C = -1.0 mA _{dc} , f = 1.0 kHz)	h _{fe}	100	400	-
Output Admittance (V _{CE} = -10 V _{dc} , I _C = -1.0 mA _{dc} , f = 1.0 kHz)	h _{oe}	3.0	60	μmhos
Noise Figure (V _{CE} = -5.0 V _{dc} , I _C = -100 μA _{dc} , R _S = 1.0 k Ω, f = 1.0 kHz)	NF	-	4.0	dB

SWITCHING CHARACTERISTICS

Delay Time	(V _{CC} = -3.0 V _{dc} , V _{BE} = 0.5 V _{dc})	t _d	-	35	ns
Rise Time	(I _C = -10 mA _{dc} , I _{B1} = -1.0 mA _{dc})	t _r	-	35	
Storage Time	(V _{CC} = -3.0 V _{dc} , I _C = -10 mA _{dc})	t _s	-	225	ns
Fall Time	(I _{B1} = I _{B2} = -1.0 mA _{dc})	t _f	-	75	

3. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

MMBT3906TT1

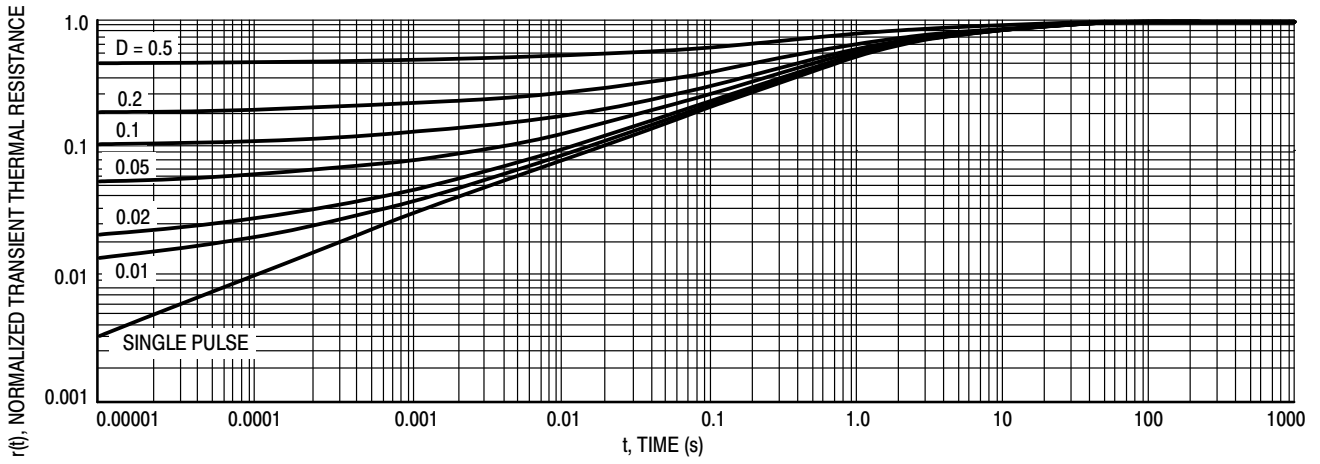


Figure 1. Normalized Thermal Response

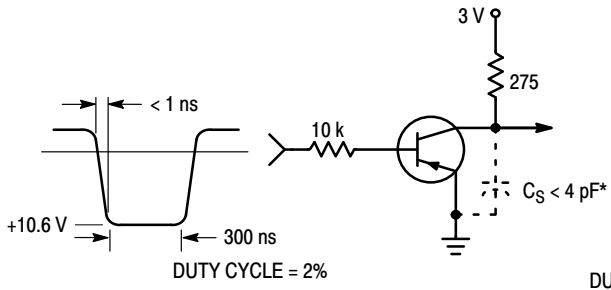


Figure 2. Delay and Rise Time Equivalent Test Circuit

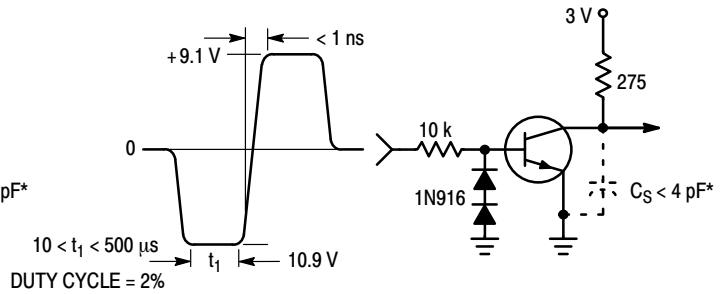


Figure 3. Storage and Fall Time Equivalent Test Circuit

* Total shunt capacitance of test jig and connectors

MMBT3906TT1

TYPICAL TRANSIENT CHARACTERISTICS

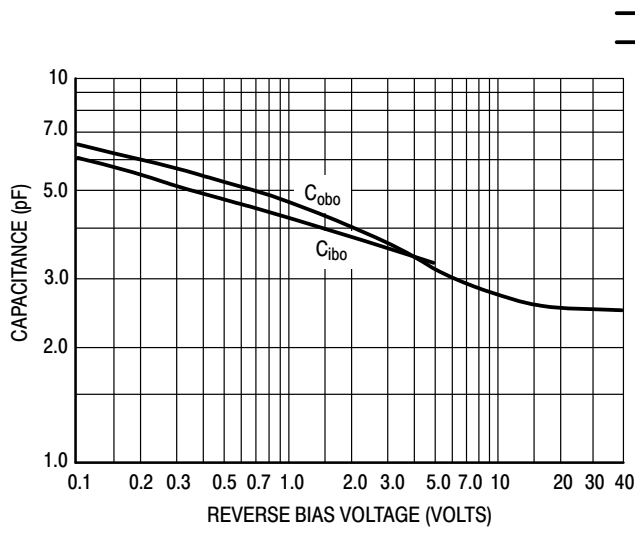


Figure 4. Capacitance

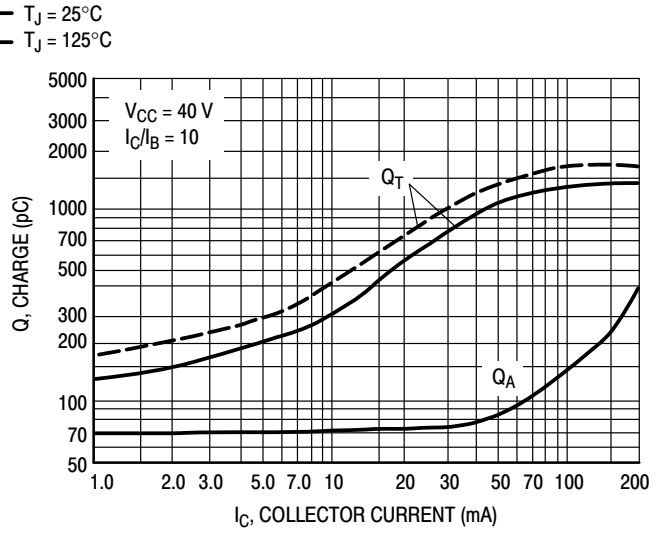


Figure 5. Charge Data

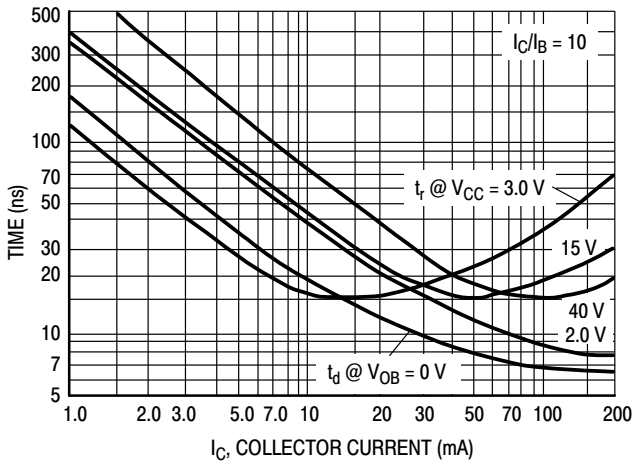


Figure 6. Turn-On Time

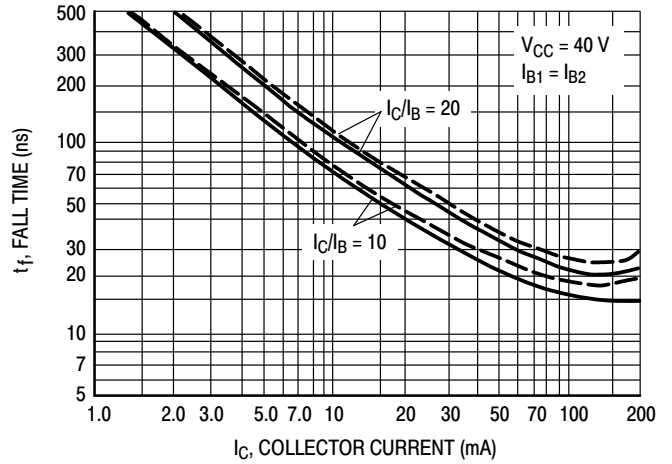


Figure 7. Fall Time

MMBT3906TT1

TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

($V_{CE} = -5.0$ Vdc, $T_A = 25^\circ\text{C}$, Bandwidth = 1.0 Hz)

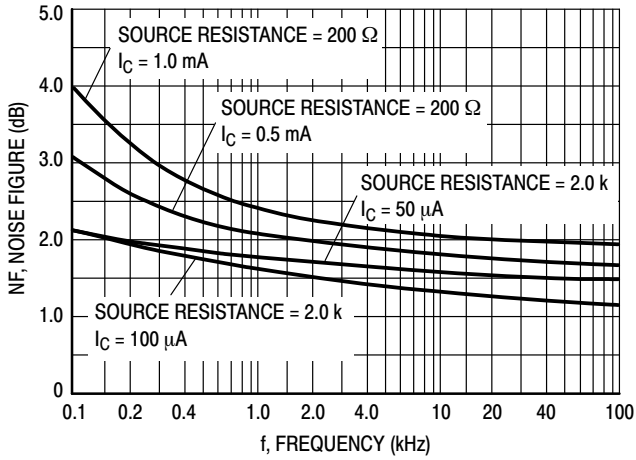


Figure 8.

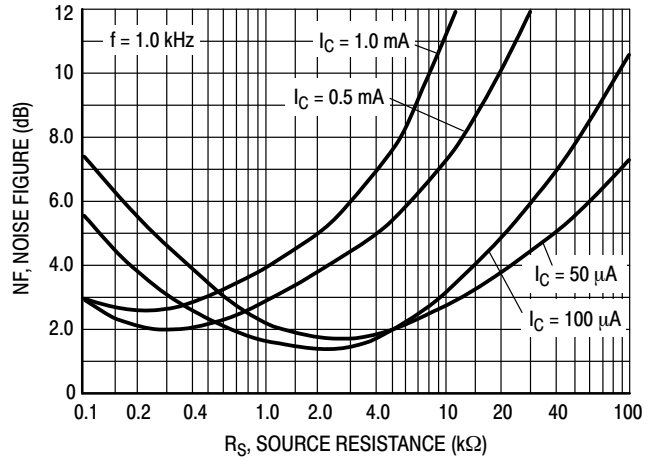


Figure 9.

h PARAMETERS

($V_{CE} = -10$ Vdc, $f = 1.0$ kHz, $T_A = 25^\circ\text{C}$)

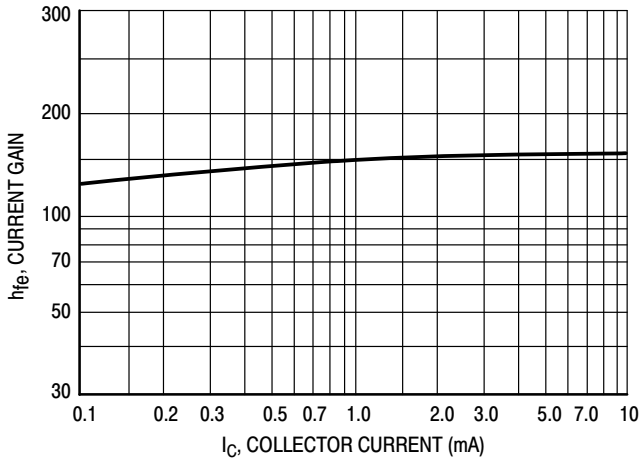


Figure 10. Current Gain

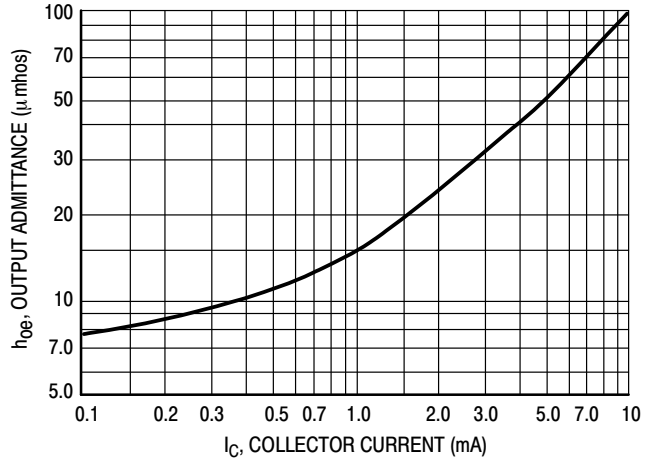


Figure 11. Output Admittance

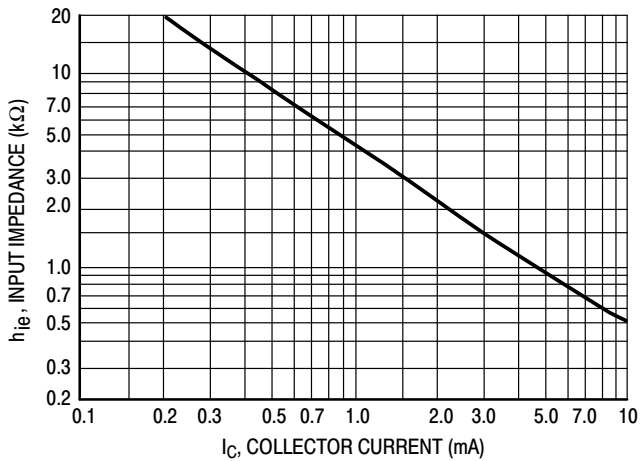


Figure 12. Input Impedance

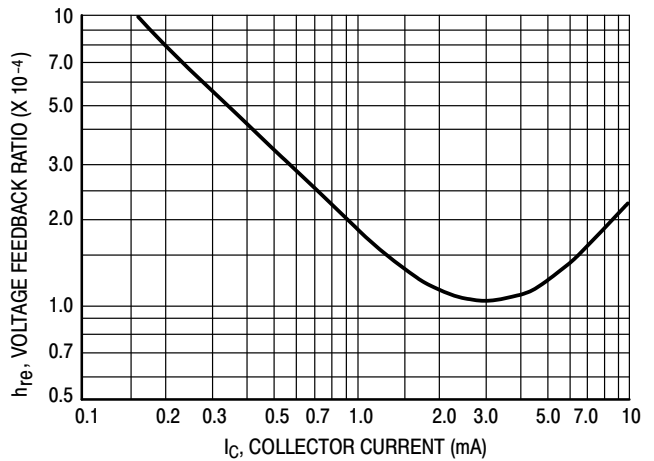


Figure 13. Voltage Feedback Ratio

MMBT3906TT1

STATIC CHARACTERISTICS

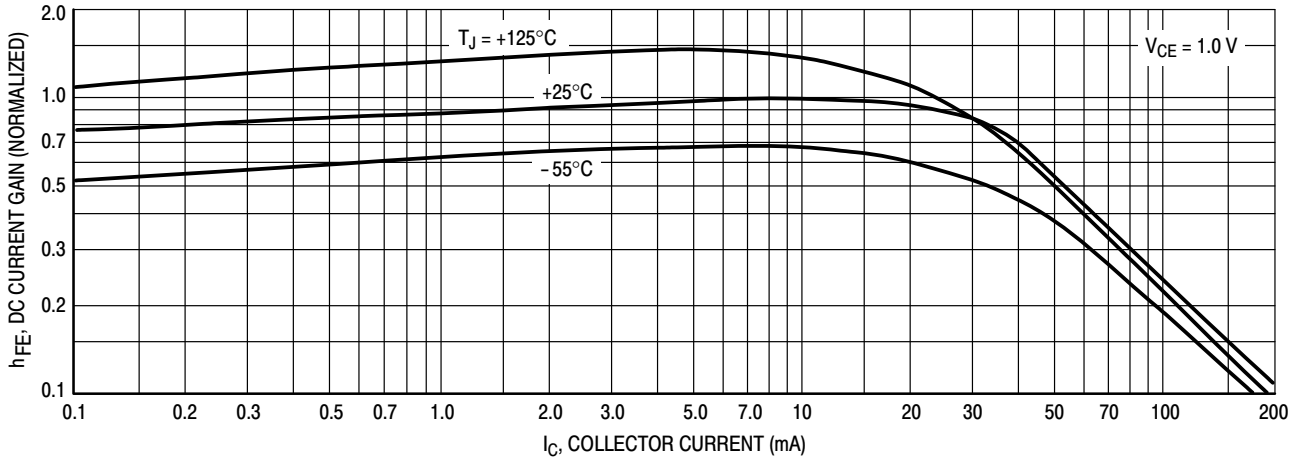


Figure 14. DC Current Gain

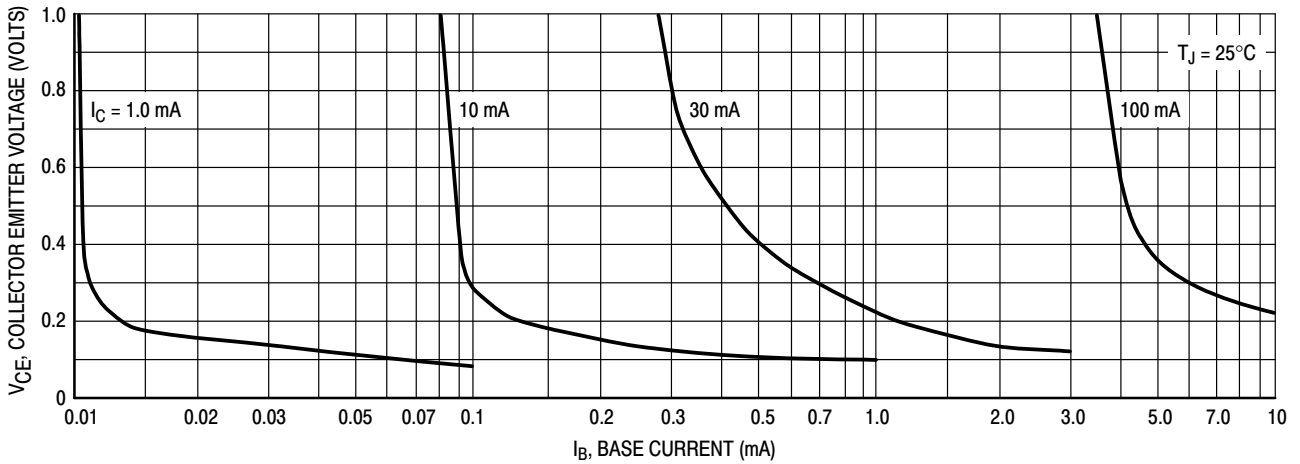


Figure 15. Collector Saturation Region

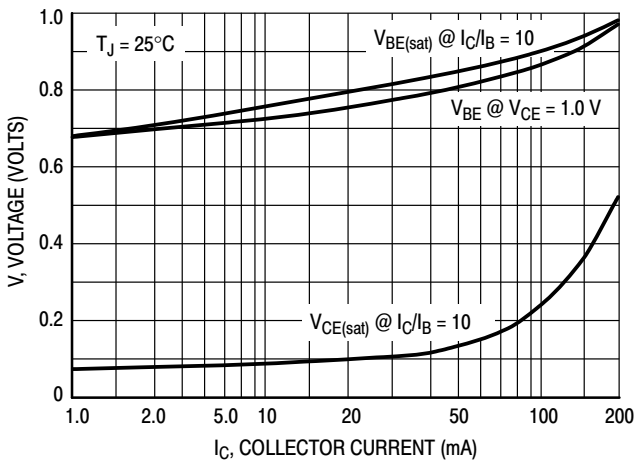


Figure 16. "ON" Voltages

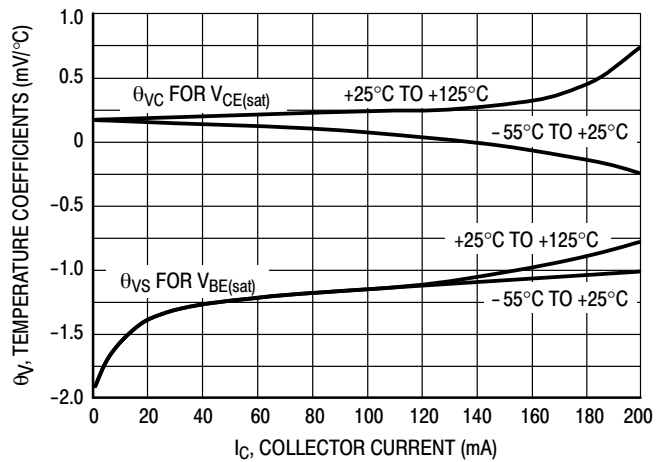
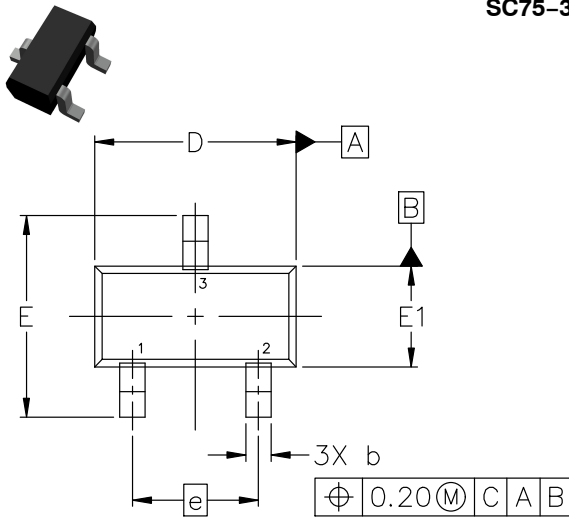


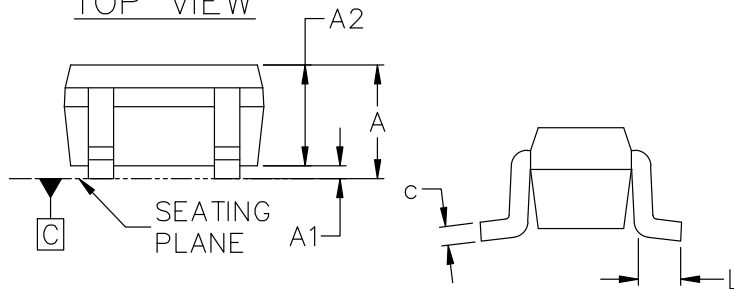
Figure 17. Temperature Coefficients

SC75-3 1.60x0.80x0.80, 1.00P
CASE 463
ISSUE H

DATE 01 FEB 2024



TOP VIEW

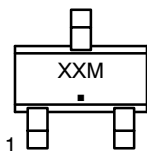


SIDE VIEW

END VIEW

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.80	0.90
A1	0.00	0.05	0.10
A2	0.80 REF.		
b	0.15	0.20	0.30
c	0.10	0.15	0.25
D	1.55	1.60	1.65
E	1.50	1.60	1.70
E1	0.70	0.80	0.90
e	1.00 BSC		
L	0.10	0.15	0.20

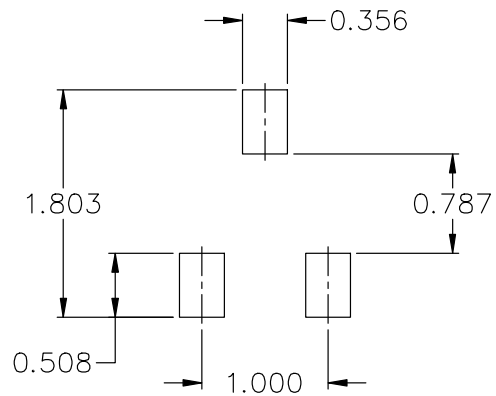
GENERIC MARKING DIAGRAM*



- XX = Specific Device Code
- M = Date Code
- = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

- STYLE 1:
PIN 1. BASE
2. EMITTER
3. COLLECTOR
- STYLE 2:
PIN 1. ANODE
2. N/C
3. CATHODE
- STYLE 3:
PIN 1. ANODE
2. ANODE
3. CATHODE
- STYLE 4:
PIN 1. CATHODE
2. CATHODE
3. ANODE
- STYLE 5:
PIN 1. GATE
2. SOURCE
3. DRAIN



RECOMMENDED MOUNTING FOOTPRINT*

* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

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DESCRIPTION:	SC75-3 1.60x0.80x0.80, 1.00P	PAGE 1 OF 1

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