

# General Purpose Transistor

## PNP Silicon

### MMBT3906L, SMMBT3906L

#### Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### MAXIMUM RATINGS

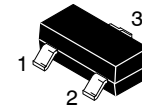
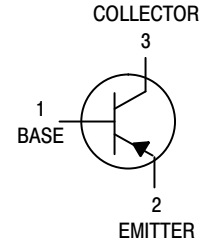
Rating	Symbol	Value	Unit
Collector - Emitter Voltage	$V_{CEO}$	-40	Vdc
Collector - Base Voltage	$V_{CBO}$	-40	Vdc
Emitter - Base Voltage	$V_{EBO}$	-5.0	Vdc
Collector Current - Continuous	$I_C$	-200	mAdc
Collector Current - Peak (Note 3)	$I_{CM}$	-800	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	225 1.8	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate, (Note 2) @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	300 2.4	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	$T_J, T_{stg}$	-65 to +150	$^\circ\text{C}$

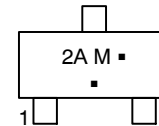
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
2. Alumina =  $0.4 \times 0.3 \times 0.024$  in. 99.5% alumina.
3. Reference SOA curve.



SOT-23 (TO-236)  
CASE 318  
STYLE 6

#### MARKING DIAGRAM



- 2A = Specific Device Code
- M = Date Code\*
- = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
MMBT3906LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
MMBT3906LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel
SMMBT3906LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SMMBT3906LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MMBT3906L, SMMBT3906L

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector – Emitter Breakdown Voltage ( $I_C = -1.0\text{ mAdc}$ , $I_B = 0$ )	$V_{(BR)CEO}$	-40	-	Vdc
Collector – Base Breakdown Voltage ( $I_C = -10\ \mu\text{Adc}$ , $I_E = 0$ )	$V_{(BR)CBO}$	-40	-	Vdc
Emitter – Base Breakdown Voltage ( $I_E = -10\ \mu\text{Adc}$ , $I_C = 0$ )	$V_{(BR)EBO}$	-5.0	-	Vdc
Base Cutoff Current ( $V_{CE} = -30\text{ Vdc}$ , $V_{EB} = -3.0\text{ Vdc}$ )	$I_{BL}$	-	-50	nAdc
Collector Cutoff Current ( $V_{CE} = -30\text{ Vdc}$ , $V_{EB} = -3.0\text{ Vdc}$ )	$I_{CEX}$	-	-50	nAdc

## ON CHARACTERISTICS (Note 4)

DC Current Gain ( $I_C = -0.1\text{ mAdc}$ , $V_{CE} = -1.0\text{ Vdc}$ ) ( $I_C = -1.0\text{ mAdc}$ , $V_{CE} = -1.0\text{ Vdc}$ ) ( $I_C = -10\text{ mAdc}$ , $V_{CE} = -1.0\text{ Vdc}$ ) ( $I_C = -50\text{ mAdc}$ , $V_{CE} = -1.0\text{ Vdc}$ ) ( $I_C = -100\text{ mAdc}$ , $V_{CE} = -1.0\text{ Vdc}$ )	$H_{FE}$	60 80 100 60 30	- - 300 - -	-
Collector – Emitter Saturation Voltage ( $I_C = -10\text{ mAdc}$ , $I_B = -1.0\text{ mAdc}$ ) ( $I_C = -50\text{ mAdc}$ , $I_B = -5.0\text{ mAdc}$ )	$V_{CE(sat)}$	- -	-0.25 -0.4	Vdc
Base – Emitter Saturation Voltage ( $I_C = -10\text{ mAdc}$ , $I_B = -1.0\text{ mAdc}$ ) ( $I_C = -50\text{ mAdc}$ , $I_B = -5.0\text{ mAdc}$ )	$V_{BE(sat)}$	-0.65 -	-0.85 -0.95	Vdc

## SMALL-SIGNAL CHARACTERISTICS

Current – Gain – Bandwidth Product ( $I_C = -10\text{ mAdc}$ , $V_{CE} = -20\text{ Vdc}$ , $f = 100\text{ MHz}$ )	$f_T$	250	-	MHz
Output Capacitance ( $V_{CB} = -5.0\text{ Vdc}$ , $I_E = 0$ , $f = 1.0\text{ MHz}$ )	$C_{obo}$	-	4.5	pF
Input Capacitance ( $V_{EB} = -0.5\text{ Vdc}$ , $I_C = 0$ , $f = 1.0\text{ MHz}$ )	$C_{ibo}$	-	10	pF
Input Impedance ( $I_C = -1.0\text{ mAdc}$ , $V_{CE} = -10\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )	$h_{ie}$	2.0	12	k $\Omega$
Voltage Feedback Ratio ( $I_C = -1.0\text{ mAdc}$ , $V_{CE} = -10\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )	$h_{re}$	0.1	10	$\times 10^{-4}$
Small – Signal Current Gain ( $I_C = -1.0\text{ mAdc}$ , $V_{CE} = -10\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )	$h_{fe}$	100	400	-
Output Admittance ( $I_C = -1.0\text{ mAdc}$ , $V_{CE} = -10\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )	$h_{oe}$	3.0	60	$\mu\text{mhos}$
Noise Figure ( $I_C = -100\ \mu\text{Adc}$ , $V_{CE} = -5.0\text{ Vdc}$ , $R_S = 1.0\text{ k}\Omega$ , $f = 1.0\text{ kHz}$ )	NF	-	4.0	dB

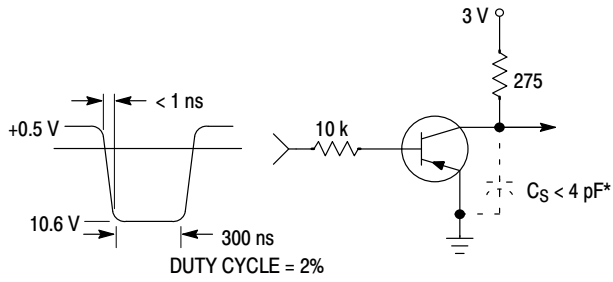
## SWITCHING CHARACTERISTICS

Delay Time	$(V_{CC} = -3.0\text{ Vdc}$ , $V_{BE} = 0.5\text{ Vdc}$ , $I_C = -10\text{ mAdc}$ , $I_{B1} = -1.0\text{ mAdc}$ )	$t_d$	-	35	ns
Rise Time		$t_r$	-	35	
Storage Time	$(V_{CC} = -3.0\text{ Vdc}$ , $I_C = -10\text{ mAdc}$ , $I_{B1} = I_{B2} = -1.0\text{ mAdc}$ )	$t_s$	-	225	ns
Fall Time		$t_f$	-	75	

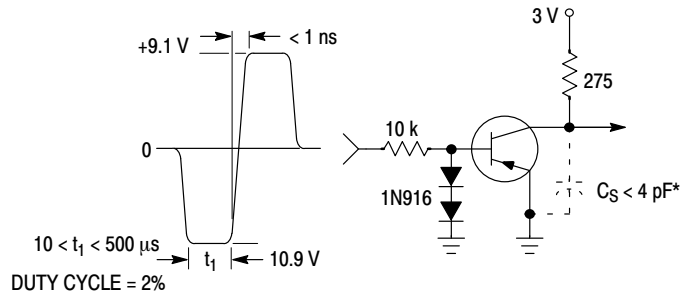
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

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**Figure 1. Delay and Rise Time Equivalent Test Circuit**

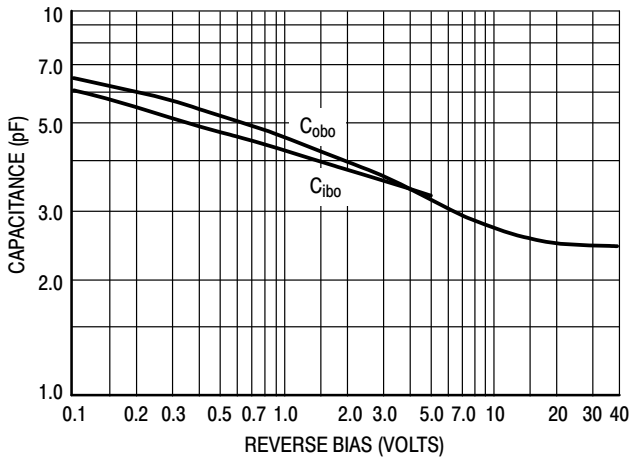


**Figure 2. Storage and Fall Time Equivalent Test Circuit**

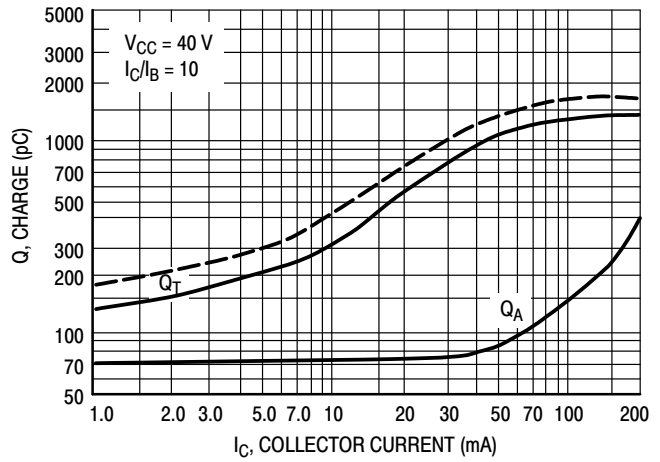
\* Total shunt capacitance of test jig and connectors

## TYPICAL TRANSIENT CHARACTERISTICS

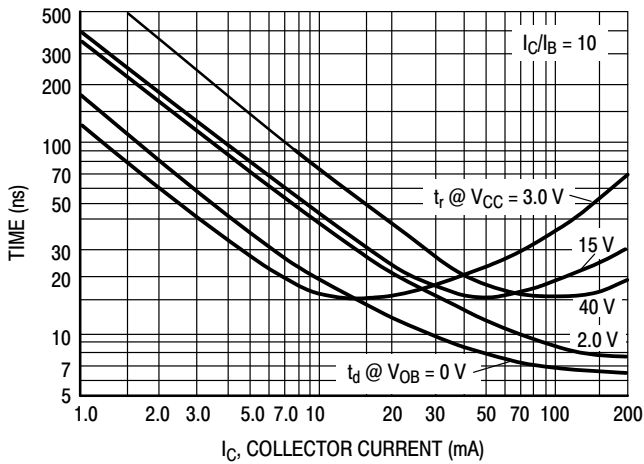
—  $T_J = 25^\circ\text{C}$   
 - - -  $T_J = 125^\circ\text{C}$



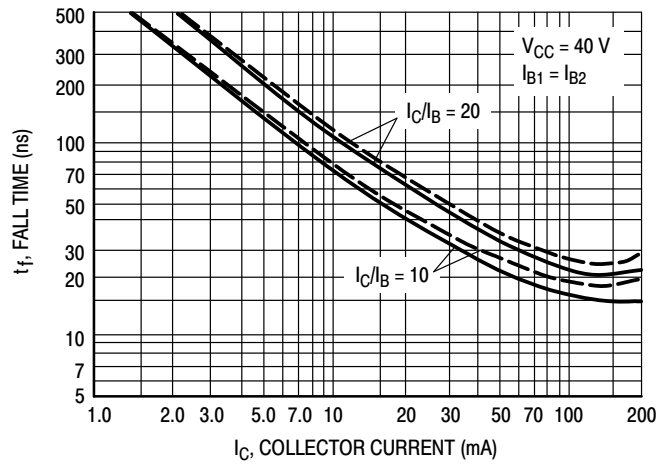
**Figure 3. Capacitance**



**Figure 4. Charge Data**



**Figure 5. Turn-On Time**



**Figure 6. Fall Time**

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## TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

( $V_{CE} = -5.0$  Vdc,  $T_A = 25^\circ\text{C}$ , Bandwidth = 1.0 Hz)

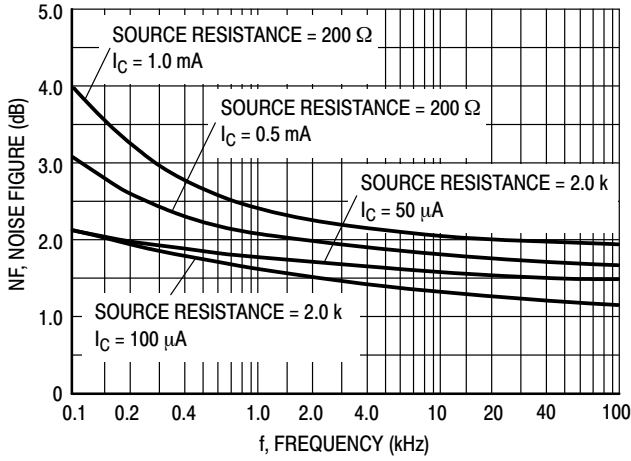


Figure 7.

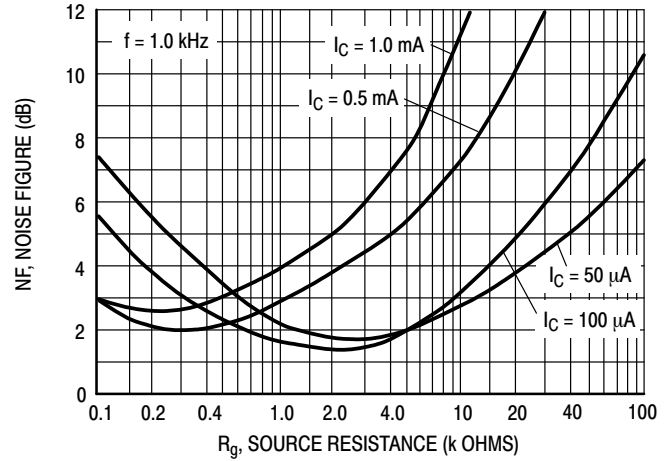


Figure 8.

## h PARAMETERS

( $V_{CE} = -10$  Vdc,  $f = 1.0$  kHz,  $T_A = 25^\circ\text{C}$ )

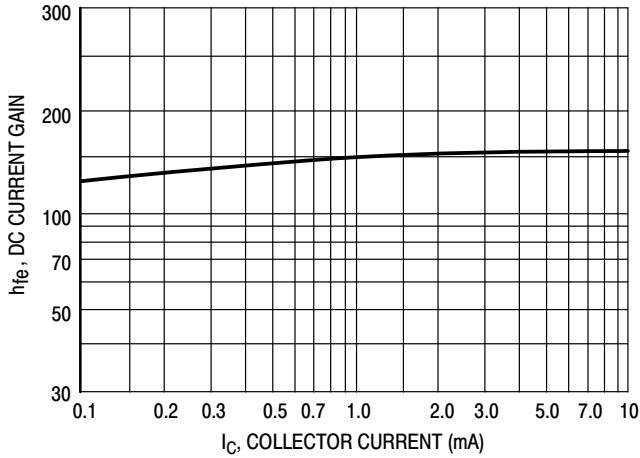


Figure 9. Current Gain

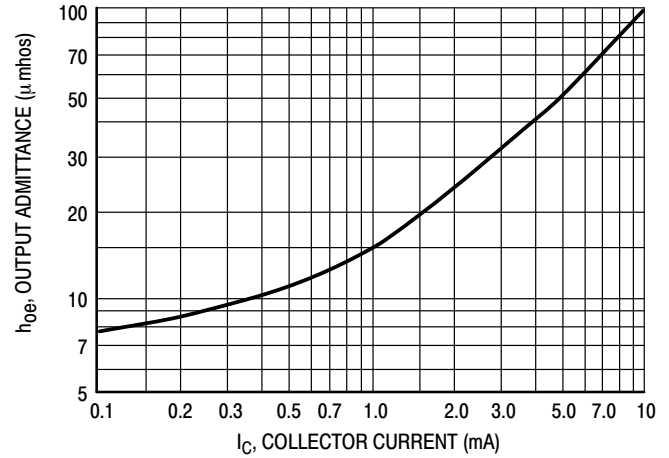


Figure 10. Output Admittance

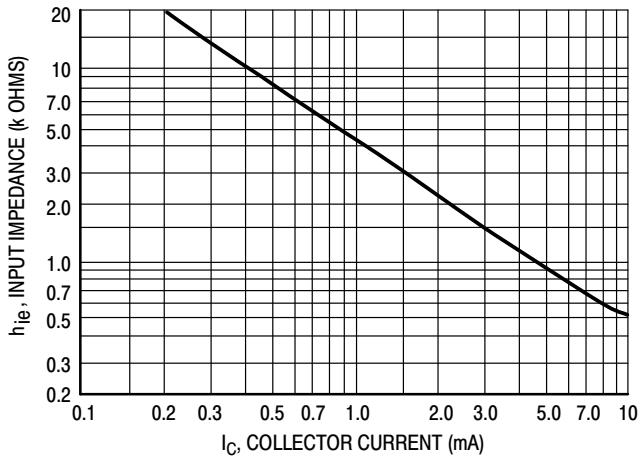


Figure 11. Input Impedance

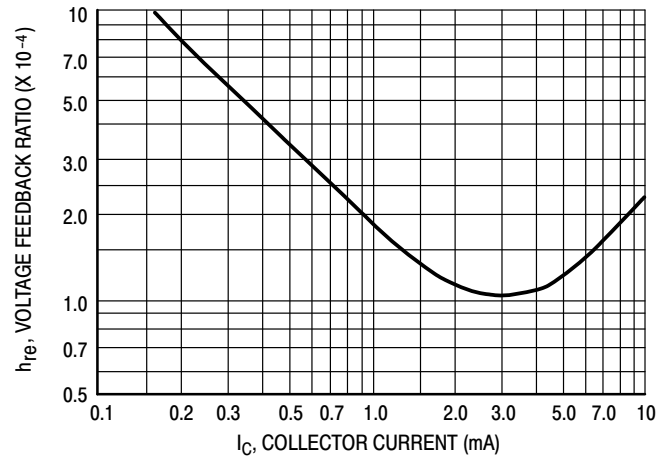


Figure 12. Voltage Feedback Ratio

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## TYPICAL STATIC CHARACTERISTICS

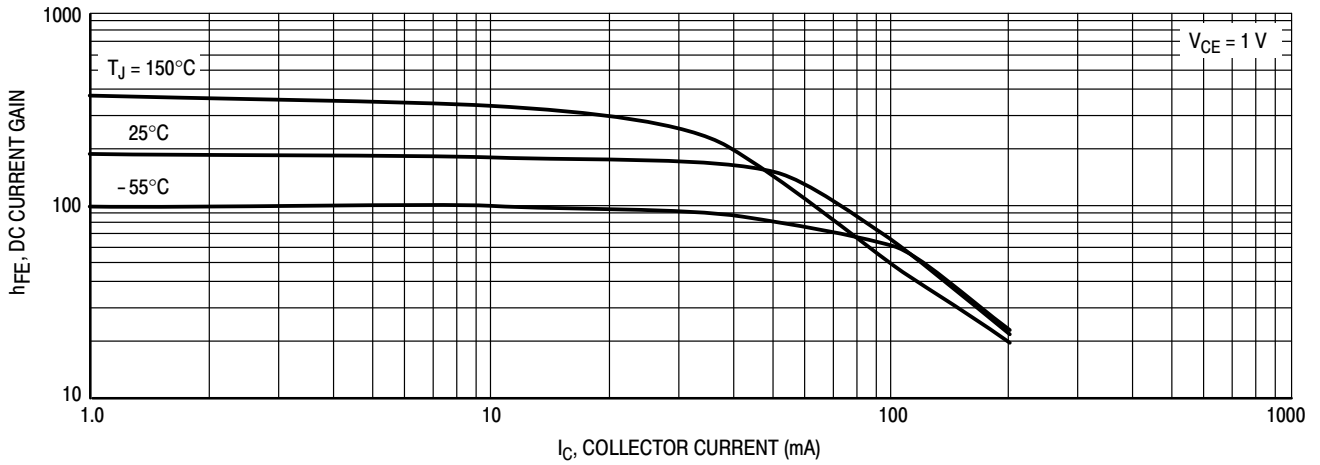


Figure 13. DC Current Gain

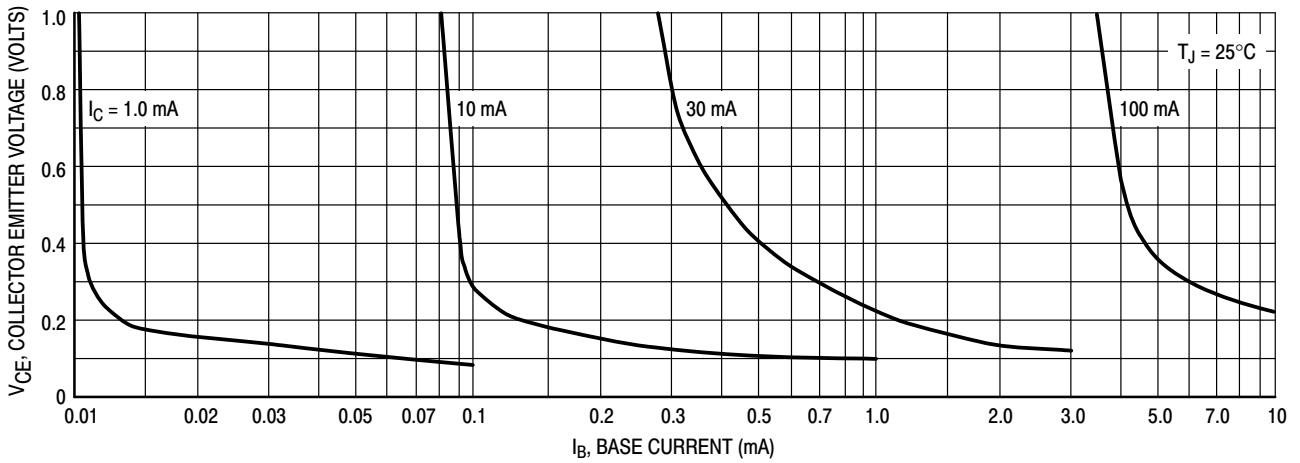
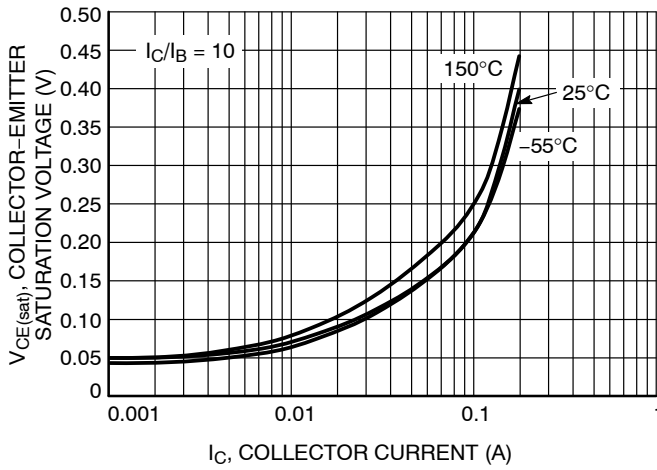
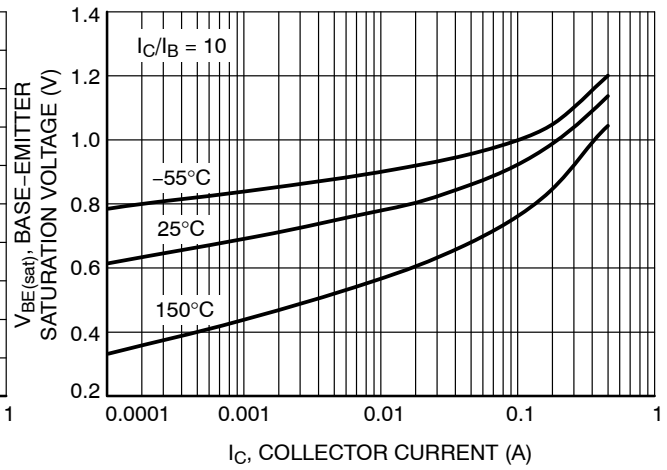


Figure 14. Collector Saturation Region

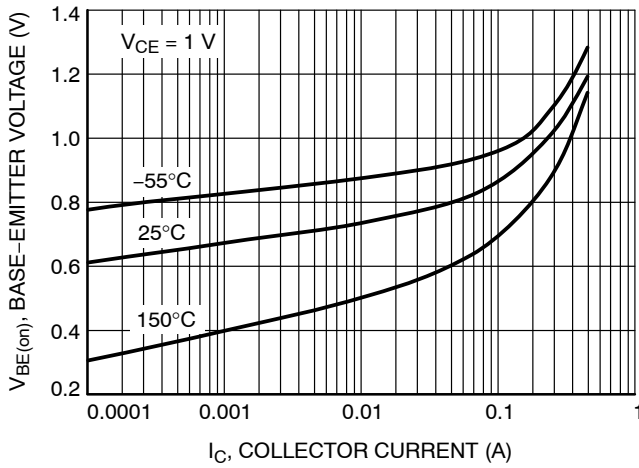
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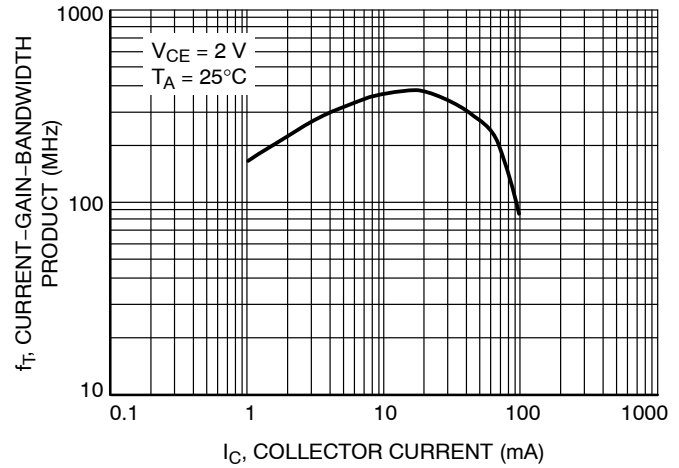
**Figure 15. Collector Emitter Saturation Voltage vs. Collector Current**



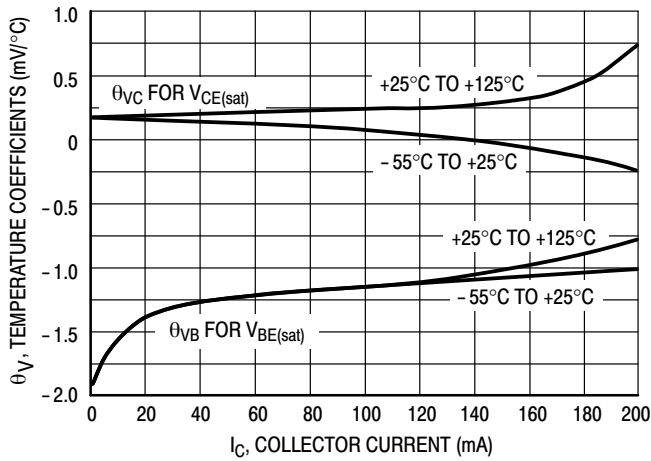
**Figure 16. Base Emitter Saturation Voltage vs. Collector Current**



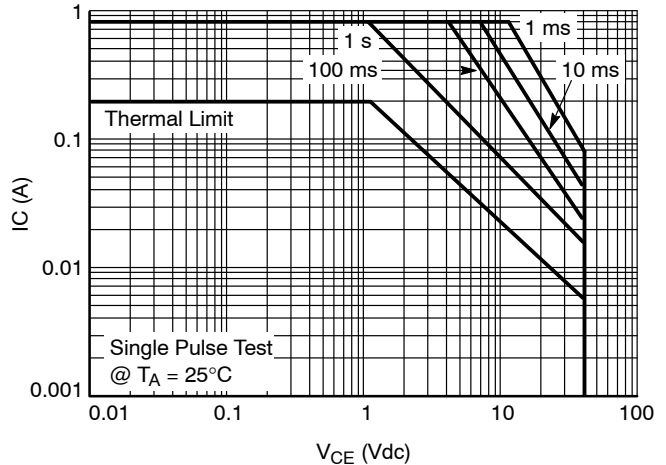
**Figure 17. Base Emitter Voltage vs. Collector Current**



**Figure 18. Current Gain Bandwidth vs. Collector Current**



**Figure 19. Temperature Coefficients**



**Figure 20. Safe Operating Area**



SCALE 4:1

**SOT-23 (TO-236) 2.90x1.30x1.00 1.90P**  
**CASE 318**  
**ISSUE AU**

DATE 14 AUG 2024



MILLIMETERS			
DIM	MIN	NOM	MAX
A	0.89	1.00	1.11
A1	0.01	0.06	0.10
b	0.37	0.44	0.50
c	0.08	0.14	0.20
D	2.80	2.90	3.04
E	1.20	1.30	1.40
e	1.78	1.90	2.04
L	0.30	0.43	0.55
L1	0.35	0.54	0.69
HE	2.10	2.40	2.64
T	0°	---	10°

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSIONS: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

**GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



\* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

**STYLES ON PAGE 2**

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**SOT-23 (TO-236) 2.90x1.30x1.00 1.90P**  
**CASE 318**  
**ISSUE AU**

DATE 14 AUG 2024

STYLE 1 THRU 5:  
CANCELLED

STYLE 6:  
PIN 1. BASE  
2. EMITTER  
3. COLLECTOR

STYLE 7:  
PIN 1. EMITTER  
2. BASE  
3. COLLECTOR

STYLE 8:  
PIN 1. ANODE  
2. NO CONNECTION  
3. CATHODE

STYLE 9:  
PIN 1. ANODE  
2. ANODE  
3. CATHODE

STYLE 10:  
PIN 1. DRAIN  
2. SOURCE  
3. GATE

STYLE 11:  
PIN 1. ANODE  
2. CATHODE  
3. CATHODE-ANODE

STYLE 12:  
PIN 1. CATHODE  
2. CATHODE  
3. ANODE

STYLE 13:  
PIN 1. SOURCE  
2. DRAIN  
3. GATE

STYLE 14:  
PIN 1. CATHODE  
2. GATE  
3. ANODE

STYLE 15:  
PIN 1. GATE  
2. CATHODE  
3. ANODE

STYLE 16:  
PIN 1. ANODE  
2. CATHODE  
3. CATHODE

STYLE 17:  
PIN 1. NO CONNECTION  
2. ANODE  
3. CATHODE

STYLE 18:  
PIN 1. NO CONNECTION  
2. CATHODE  
3. ANODE

STYLE 19:  
PIN 1. CATHODE  
2. ANODE  
3. CATHODE-ANODE

STYLE 20:  
PIN 1. CATHODE  
2. ANODE  
3. GATE

STYLE 21:  
PIN 1. GATE  
2. SOURCE  
3. DRAIN

STYLE 22:  
PIN 1. RETURN  
2. OUTPUT  
3. INPUT

STYLE 23:  
PIN 1. ANODE  
2. ANODE  
3. CATHODE

STYLE 24:  
PIN 1. GATE  
2. DRAIN  
3. SOURCE

STYLE 25:  
PIN 1. ANODE  
2. CATHODE  
3. GATE

STYLE 26:  
PIN 1. CATHODE  
2. ANODE  
3. NO CONNECTION

STYLE 27:  
PIN 1. CATHODE  
2. CATHODE  
3. CATHODE

STYLE 28:  
PIN 1. ANODE  
2. ANODE  
3. ANODE

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