Voltage Regulator -
Adjustable Output,
Negative
1.5 A

**LM337**

The LM337 is an adjustable 3–terminal negative voltage regulator capable of supplying in excess of 1.5 A over an output voltage range of −1.2 V to −37 V. This voltage regulator is exceptionally easy to use and requires only two external resistors to set the output voltage. Further, it employs internal current limiting, thermal shutdown and safe area compensation, making it essentially blow–out proof.

The LM337 serves a wide variety of applications including local, on card regulation. This device can also be used to make a programmable output regulator, or by connecting a fixed resistor between the adjustment and output, the LM337 can be used as a precision current regulator.

**Features**
- Output Current in Excess of 1.5 A
- Output Adjustable between −1.2 V and −37 V
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting Constant with Temperature
- Output Transistor Safe–Area Compensation
- Floating Operation for High Voltage Applications
- Eliminates Stocking many Fixed Voltages
- Available in Surface Mount D²PAK and Standard 3–Lead Transistor Package
- These Devices are Pb–Free and are RoHS Compliant

![Diagrams](https://www.onsemi.com/pub_link/Assets/Document/0022/2388.webp)

**MARKING DIAGRAMS**

Heatsink surface (shown as terminal 4 in case outline drawing) is connected to Pin 2.

1. Adjust
2. Vin
3. Vout

**ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 8 of this data sheet.

**Figure 1. Standard Application**

\[
V_{out} = -1.25V \left(1 + \frac{R_2}{R_1}\right)
\]
**MAXIMUM RATINGS** (Ta = +25°C, unless otherwise noted)

<table>
<thead>
<tr>
<th>Characteristic</th>
<th>Symbol</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input–Output Voltage Differential</td>
<td>V1–V0</td>
<td>40</td>
<td>Vdc</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>PD</td>
<td>Internally Limited</td>
<td>W</td>
</tr>
<tr>
<td>Tj = +25°C</td>
<td>θJA</td>
<td>65</td>
<td>°C/W</td>
</tr>
<tr>
<td>Thermal Resistance, Junction–to–Ambient</td>
<td>θJC</td>
<td>5.0</td>
<td>°C/W</td>
</tr>
<tr>
<td>Case 221A</td>
<td>Pd</td>
<td>Internally Limited</td>
<td>W</td>
</tr>
<tr>
<td>Tj = +25°C</td>
<td>θJA</td>
<td>70</td>
<td>°C/W</td>
</tr>
<tr>
<td>Thermal Resistance, Junction–to–Case</td>
<td>θJC</td>
<td>5.0</td>
<td>°C/W</td>
</tr>
<tr>
<td>Case 936 (D²PAK)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Tj = +25°C</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Operating Junction Temperature Range</td>
<td>Tj</td>
<td>−40 to +125</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>Tstg</td>
<td>−65 to +150</td>
<td>°C</td>
</tr>
</tbody>
</table>

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

**ELECTRICAL CHARACTERISTICS** ([V1–V0] = 5.0 V; IO = 0.5 A for T package; Tj = Tlow to Thigh [Note 1]; Imax and Pmax [Note 2].)

<table>
<thead>
<tr>
<th>Characteristics</th>
<th>Figure</th>
<th>Symbol</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line Regulation (Note 3), Tja = +25°C, 3.0 V ≤</td>
<td>V1–V0</td>
<td>≤ 40 V</td>
<td>1</td>
<td>Regline</td>
<td>–</td>
<td>0.01</td>
</tr>
<tr>
<td>Load Regulation (Note 3), Tja = +25°C, 10 mA ≤</td>
<td>I0</td>
<td>≤ Imax</td>
<td>2</td>
<td>Regload</td>
<td>–</td>
<td>15</td>
</tr>
<tr>
<td>Thermal Regulation, Tja = +25°C (Note 5), 10 ms Pulse</td>
<td></td>
<td></td>
<td></td>
<td>Regtherm</td>
<td>–</td>
<td>0.003</td>
</tr>
<tr>
<td>Adjustment Pin Current Change, 2.5 V ≤</td>
<td></td>
<td>≤ 40 V, 10 mA ≤</td>
<td>IL</td>
<td>≤ Imax</td>
<td>1, 2</td>
<td>ΔIAL</td>
</tr>
<tr>
<td>Reference Voltage, Tja = +25°C, 3.0 V ≤</td>
<td></td>
<td>≤ 40 V, 10 mA ≤</td>
<td>IO</td>
<td>≤ Imax, PD ≤</td>
<td>Pmax</td>
<td>JA</td>
</tr>
<tr>
<td>Line Regulation (Note 3), 3.0 V ≤</td>
<td></td>
<td>≤ 40 V</td>
<td>1</td>
<td>Regline</td>
<td>–</td>
<td>0.02</td>
</tr>
<tr>
<td>Load Regulation (Note 3), 10 mA ≤</td>
<td></td>
<td>≤ Imax</td>
<td>2</td>
<td>Regload</td>
<td>–</td>
<td>20</td>
</tr>
<tr>
<td>Temperature Stability (Tlow ≤ Tj ≤ Thigh)</td>
<td></td>
<td></td>
<td></td>
<td>Ts</td>
<td>–</td>
<td>0.6</td>
</tr>
<tr>
<td>Minimum Load Current to Maintain Regulation</td>
<td></td>
<td></td>
<td></td>
<td>Imin</td>
<td>–</td>
<td>1.5</td>
</tr>
<tr>
<td>Maximum Output Current</td>
<td></td>
<td></td>
<td></td>
<td>Imax</td>
<td>–</td>
<td>1.5</td>
</tr>
<tr>
<td>RMS Noise, % of VO, Tja = +25°C, 10 Hz ≤ f ≤ 10 kHz</td>
<td></td>
<td></td>
<td></td>
<td>–</td>
<td>N</td>
<td>0.003</td>
</tr>
<tr>
<td>Ripple Rejection, VO = −10 V, f = 120 Hz (Note 4)</td>
<td></td>
<td></td>
<td></td>
<td>RR</td>
<td>–</td>
<td>60</td>
</tr>
<tr>
<td>Without CAdj</td>
<td></td>
<td></td>
<td></td>
<td>66</td>
<td>C Adj = 10 μF</td>
<td>77</td>
</tr>
<tr>
<td>Long–Term Stability, Tja = Thigh (Note 6), Tja = +25°C for Endpoint Measurements</td>
<td>3</td>
<td>S</td>
<td>–</td>
<td>0.3</td>
<td>1.0</td>
<td>%/1.0 k Hrs.</td>
</tr>
<tr>
<td>Thermal Regulation, Junction–to–Case, T Package</td>
<td>RJ (JC)</td>
<td>–</td>
<td>4.0</td>
<td>–</td>
<td>°C/W</td>
<td></td>
</tr>
</tbody>
</table>

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Tlow to Thigh = 0°C to +125°C, for LM337T, D2T. Tlow to Thigh = −40°C to +125°C, for LM337BT, BD2T.
2. Imax = 1.5 A, Pmax = 20 W
3. Load and line regulation are specified at constant junction temperature. Change in VO because of heating effects is covered under the Thermal Regulation specification. Pulse testing with a low duty cycle is used.
4. C Adj, when used, is connected between the adjustment pin and ground.
5. Power dissipation within an IC voltage regulator produces a temperature gradient on the die, affecting individual IC components on the die. These effects can be minimized by proper integrated circuit design and layout techniques. Thermal Regulation is the effect of these temperature gradients on the output voltage and is expressed in percentage of output change per watt of power change in a specified time.
6. Since Long Term Stability cannot be measured on each device before shipment, this specification is an engineering estimate of average stability from lot to lot.

www.onsemi.com
This device contains 39 active transistors.

Figure 1. Line Regulation and $\Delta I_{\text{Adj}}$/Line Test Circuit

Line Regulation (%/V) = \frac{|V_{\text{OL}} - V_{\text{OH}}|}{V_{\text{OH}}} \times 100
To Calculate $R_2$: $R_2 = \left( \frac{V_O}{V_{ref}} - 1 \right) R_1$

This assumes $I_{Adj}$ is negligible.

* Pulse testing required.
1% Duty Cycle is suggested.

Figure 3. Standard Test Circuit

To Calculate $R_2$: $R_2 = \left( \frac{V_O}{V_{ref}} - 1 \right) R_1$

This assumes $I_{Adj}$ is negligible.

* Pulse testing required.
1% Duty Cycle is suggested.

Figure 4. Ripple Rejection Test Circuit

* $D_1$ Discharges $C_{Adj}$ if output is shorted to Ground.
Figure 11. Ripple Rejection versus Output Voltage

Figure 12. Ripple Rejection versus Output Current

Figure 13. Ripple Rejection versus Frequency

Figure 14. Output Impedance

Figure 15. Line Transient Response

Figure 16. Load Transient Response
APPLICATIONS INFORMATION

Basic Circuit Operation

The LM337 is a 3–terminal floating regulator. In operation, the LM337 develops and maintains a nominal −1.25 V reference (V_{ref}) between its output and adjustment terminals. This reference voltage is converted to a programming current (I_{PROG}) by R_1 (see Figure 17), and this constant current flows through R_2 from ground.

The regulated output voltage is given by:

\[ V_{out} = V_{ref} \left(1 + \frac{R_2}{R_1}\right) + I_{Adj} \frac{R_2}{R_2} \]

Since the current into the adjustment terminal (I_{Adj}) represents an error term in the equation, the LM337 was designed to control I_{Adj} to less than 100 \mu A and keep it constant. To do this, all quiescent operating current is returned to the output terminal. This imposes the requirement for a minimum load current. If the load current is less than this minimum, the output voltage will rise.

Since the LM337 is a floating regulator, it is only the voltage differential across the circuit which is important to performance, and operation at high voltages with respect to ground is possible.

External Capacitors

A 1.0 \mu F tantalum input bypass capacitor (C_{in}) is recommended to reduce the sensitivity to input line impedance.

The adjustment terminal may be bypassed to ground to improve ripple rejection. This capacitor (C_{Adj}) prevents ripple from being amplified as the output voltage is increased. A 10 \mu F capacitor should improve ripple rejection about 15 dB at 120 Hz in a 10 V application.

An output capacitance (C_O) in the form of a 1.0 \mu F tantalum or 10 \mu F aluminum electrolytic capacitor is required for stability. Using the classical tantalum or aluminum electrolytic capacitor types with non–reduced ESR (Equivalent Series Resistance) value is necessary. Low–ESR or similar capacitor types with reduced ESR value and ceramic capacitors can cause instability or continuous oscillations in the application.

Protection Diodes

When external capacitors are used with any IC regulator it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low current points into the regulator.

Figure 18 shows the LM337 with the recommended protection diodes for output voltages in excess of −25 V or high capacitance values (C_O > 25 \mu F, C_{Adj} > 10 \mu F). Diode D_1 prevents C_O from discharging thru the IC during an input short circuit. Diode D_2 protects against capacitor C_{Adj} discharging through the IC during an output short circuit. The combination of diodes D_1 and D_2 prevents C_{Adj} from the discharging through the IC during an input short circuit.

Load Regulation

The LM337 is capable of providing extremely good load regulation, but a few precautions are needed to obtain maximum performance. For best performance, the programming resistor (R_1) should be connected as close to the regulator as possible to minimize line drops which effectively appear in series with the reference, thereby degrading regulation. The ground end of R_2 can be returned near the load ground to provide remote ground sensing and improve load regulation.

Figure 17. Basic Circuit Configuration

Figure 18. Voltage Regulator with Protection Diodes
Figure 19. D²PAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

ORDERING INFORMATION

<table>
<thead>
<tr>
<th>Device</th>
<th>Operating Temperature Range</th>
<th>Package</th>
<th>Shipping¹</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM337BD2TR4G</td>
<td>T_J = -40° to +125°C</td>
<td>D²PAK (Pb−Free)</td>
<td>800 / Tape &amp; Reel</td>
</tr>
<tr>
<td>LM337BTG</td>
<td></td>
<td>TO−220AB (Pb−Free)</td>
<td>50 Units / Rail</td>
</tr>
<tr>
<td>LM337D2TR4G</td>
<td>T_J = 0° to +125°C</td>
<td>D²PAK (Pb−Free)</td>
<td>800 / Tape &amp; Reel</td>
</tr>
<tr>
<td>LM337TG</td>
<td></td>
<td>TO−220AB (Pb−Free)</td>
<td>50 Units / Rail</td>
</tr>
</tbody>
</table>

¹For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
TO-220, SINGLE GAUGE
CASE 221AB-01
ISSUE A

DATE 16 NOV 2010

NOTES:
2. CONTROLLING DIMENSION: INCHES.
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.
4. PRODUCT SHIPPED PRIOR TO 2008 HAD DIMENSIONS S = 0.045 - 0.055 INCHES (1.143 - 1.397 MM)

<table>
<thead>
<tr>
<th>DIM</th>
<th>MIN</th>
<th>MAX</th>
<th>MIN</th>
<th>MAX</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>0.570</td>
<td>0.620</td>
<td>14.48</td>
<td>15.75</td>
</tr>
<tr>
<td>B</td>
<td>0.380</td>
<td>0.405</td>
<td>9.66</td>
<td>10.28</td>
</tr>
<tr>
<td>C</td>
<td>0.160</td>
<td>0.190</td>
<td>4.07</td>
<td>4.82</td>
</tr>
<tr>
<td>D</td>
<td>0.025</td>
<td>0.035</td>
<td>0.64</td>
<td>0.68</td>
</tr>
<tr>
<td>E</td>
<td>0.144</td>
<td>0.147</td>
<td>3.61</td>
<td>3.73</td>
</tr>
<tr>
<td>F</td>
<td>0.095</td>
<td>0.105</td>
<td>2.42</td>
<td>2.66</td>
</tr>
<tr>
<td>G</td>
<td>0.110</td>
<td>0.155</td>
<td>2.80</td>
<td>3.90</td>
</tr>
<tr>
<td>H</td>
<td>0.018</td>
<td>0.025</td>
<td>0.46</td>
<td>0.64</td>
</tr>
<tr>
<td>J</td>
<td>0.500</td>
<td>0.560</td>
<td>12.70</td>
<td>14.27</td>
</tr>
<tr>
<td>K</td>
<td>0.045</td>
<td>0.060</td>
<td>1.15</td>
<td>1.52</td>
</tr>
<tr>
<td>L</td>
<td>0.190</td>
<td>0.210</td>
<td>4.85</td>
<td>5.33</td>
</tr>
<tr>
<td>Q</td>
<td>0.100</td>
<td>0.120</td>
<td>2.54</td>
<td>3.04</td>
</tr>
<tr>
<td>R</td>
<td>0.080</td>
<td>0.110</td>
<td>2.04</td>
<td>2.79</td>
</tr>
<tr>
<td>S</td>
<td>0.020</td>
<td>0.024</td>
<td>0.508</td>
<td>0.61</td>
</tr>
<tr>
<td>T</td>
<td>0.355</td>
<td>0.359</td>
<td>9.07</td>
<td>9.13</td>
</tr>
<tr>
<td>U</td>
<td>0.000</td>
<td>0.050</td>
<td>0.00</td>
<td>1.27</td>
</tr>
<tr>
<td>V</td>
<td>0.045</td>
<td>0.045</td>
<td>1.15</td>
<td>1.15</td>
</tr>
<tr>
<td>Z</td>
<td>0.080</td>
<td>0.080</td>
<td>2.04</td>
<td>2.04</td>
</tr>
</tbody>
</table>

**STYLE 1:**
1. BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

**STYLE 2:**
1. BASE
2. EMITTER
3. COLLECTOR
4. EMITTER

**STYLE 3:**
1. CATHODE
2. ANODE
3. GATE
4. ANODE

**STYLE 4:**
1. MAIN TERMINAL 1
2. MAIN TERMINAL 2
3. GATE
4. MAIN TERMINAL 2

**STYLE 5:**
1. GATE
2. DRAIN
3. SOURCE
4. DRAIN

**STYLE 6:**
1. ANODE
2. CATHODE
3. ANODE
4. CATHODE

**STYLE 7:**
1. ANODE
2. GATE
3. CATHODE
4. ANODE

**STYLE 8:**
1. GATE
2. ANODE
3. EXTERNAL TRIP/Delay
4. ANODE

**STYLE 9:**
1. GATE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

**STYLE 10:**
1. SOURCE
2. DRAIN
3. GATE
4. SOURCE

**STYLE 11:**
1. DRAIN
2. SOURCE
3. GATE
4. SOURCE
NOTES:
2. CONTROLLING DIMENSION: INCHES.
3. TAB CONTOUR OPTIONAL WITHIN DIMENSIONS A AND K.
4. DIMENSIONS U AND V ESTABLISH A MINIMUM MOUNTING SURFACE FOR TERMINAL 4.
5. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.
6. SINGLE GAUGE DESIGN WILL BE SHIPPED AFTER FPCN EXPIRATION IN OCTOBER 2011.

**SOLDERING FOOTPRINT**

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.*

**GENERIC MARKING DIAGRAM**

XXXXXX = Specific Device Code
A = Assembly Location
L = Wafer Lot
Y = Year
WW = Work Week
G = Pb–Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot "/C0071", may or may not be present.