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## **ON Semiconductor**®

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### FAIRCHILD

SEMICONDUCTOR®

### KST5401

### High Voltage Transistor



1. Base 2. Emitter 3. Collector

### **PNP Epitaxial Silicon Transistor**

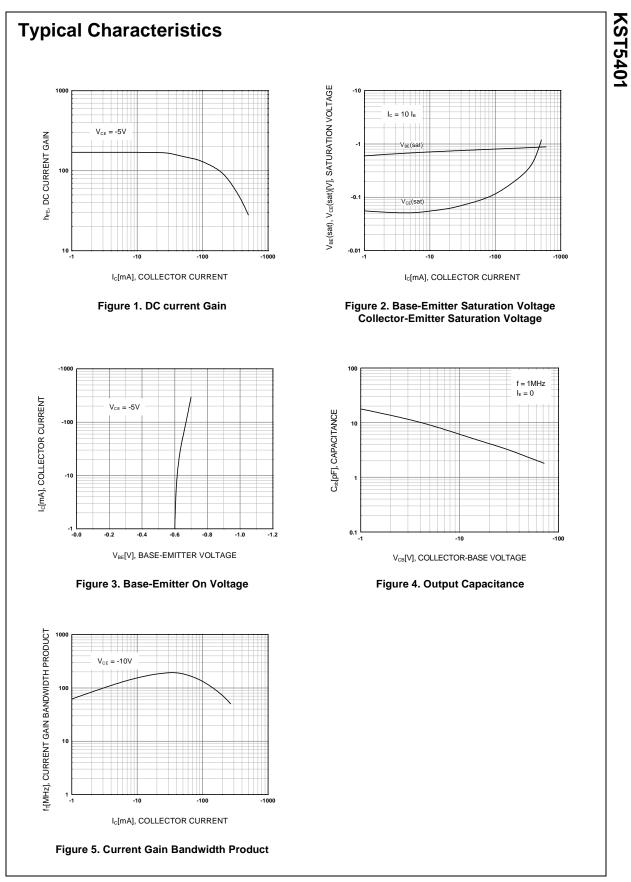
Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	-160	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-150	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
I <sub>C</sub>	Collector Current	-500	mA
P <sub>C</sub>	Collector Power Dissipation	350	mW
T <sub>STG</sub>	Storage Temperature	150	°C

#### Electrical Characteristics Ta=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -100μΑ, I <sub>E</sub> =0	-160		V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1.0mA, I <sub>B</sub> =0	-150		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -10μΑ, I <sub>C</sub> =0	-5		V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> = -100V, I <sub>E</sub> =0		-50	nA
h <sub>FE</sub>	DC Current Gain	$V_{CE}$ = -5V, I <sub>C</sub> = -1.0mA $V_{CE}$ = -5V, I <sub>C</sub> = -10mA $V_{CE}$ = -5V, I <sub>C</sub> = -50mA	50 60 50	240	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -10mA, I <sub>B</sub> = -1.0mA I <sub>C</sub> = -50mA, I <sub>B</sub> = -5mA		-0.2 -0.5	V V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = -10mA, I <sub>B</sub> = -1.0mA I <sub>C</sub> = -50mA, I <sub>B</sub> = -5mA		-1.0 -1.0	V V
f <sub>T</sub>	Current Gain Bandwidth Product	I <sub>C</sub> = -10mA, V <sub>CE</sub> = -10V f=100MHz	100	300	MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = -10V, I <sub>E</sub> =0, f=1.0MHz		6.0	pF
NF	Noise Figure	V <sub>CE</sub> = -5V, I <sub>C</sub> = -200μA R <sub>S</sub> =10KΩ, f=10Hz to 15.7KHz		8.0	dB

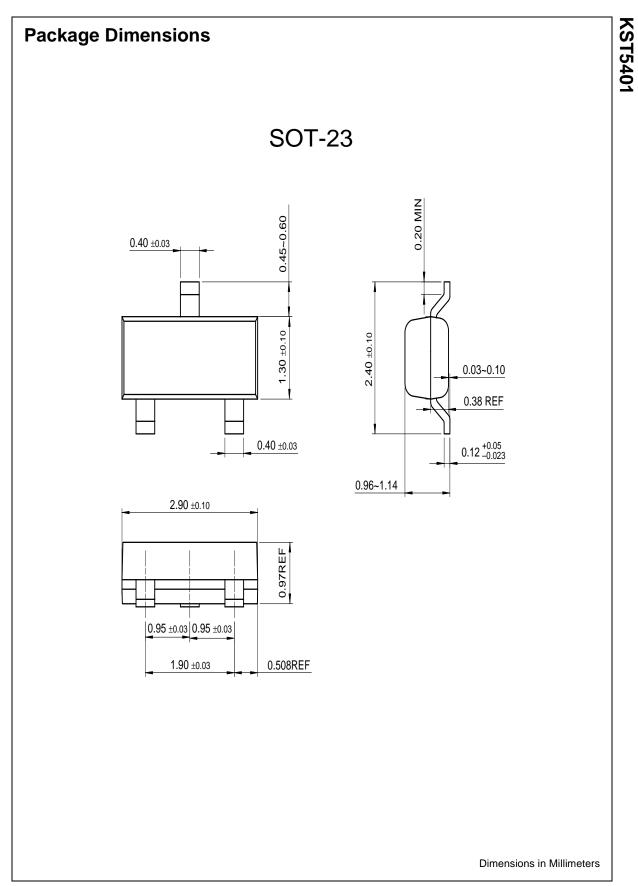






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