

6-Pin DIP High Voltage Photodarlington Optocouplers



ON Semiconductor®

www.onsemi.com

H11G1M, H11G2M

Description

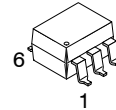
The H11G1M and H11G2M are photodarlington-type optically coupled optocouplers. These devices have a gallium arsenide infrared emitting diode coupled with a silicon darlington connected phototransistor which has an integral base-emitter resistor to optimize elevated temperature characteristics.

Features

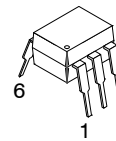
- High BV_{CEO} :
 - ◆ 100 V Minimum for H11G1M
 - ◆ 80 V Minimum for H11G2M
- High Sensitivity to Low Input Current (Minimum 500% CTR at $I_F = 1$ mA)
- Low Leakage Current at Elevated Temperature (Maximum 100 μ A at 80°C)
- Safety and Regulatory Approvals:
 - ◆ UL1577, 4,170 VAC_{RMS} for 1 Minute
 - ◆ DIN-EN/IEC60747-5-5, 850 V Peak Working Insulation Voltage

Application

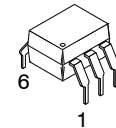
- CMOS Logic Interface
- Telephone Ring Detector
- Low Input TTL Interface
- Power Supply Isolation
- Replace Pulse Transformer



**PDIP6 8.51x6.35, 2.54P
CASE 646BY**

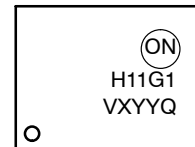


**PDIP6 8.51x6.35, 2.54P
CASE 646BX**



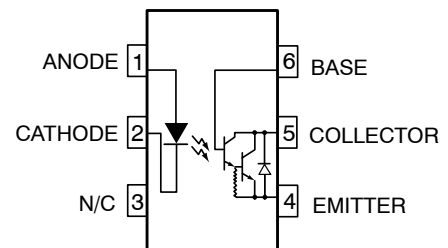
**PDIP6 8.51x6.35, 2.54P
CASE 646BZ**

MARKING DIAGRAM



- H11G1 = Specific Device Code
- V = DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)
- X = One-Digit Year Code
- YY = Digit Work Week
- Q = Assembly Package Code

SCHEMATIC



ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

H11G1M, H11G2M

SAFETY AND INSULATION RATINGS

(As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for “safe electrical insulation” only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.)

Parameter		Characteristics
Installation Classifications per DIN VDE 0110/1.89 Table 1, For Rated Mains Voltage	<150 V _{RMS}	I-IV
	<300 V _{RMS}	I-IV
Climatic Classification		55/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
V _{PR}	Input-to-Output Test Voltage, Method A, V _{IORM} × 1.6 = V _{PR} , Type and Sample Test with t _m = 10 s, Partial Discharge < 5 pC	1360	V _{peak}
	Input-to-Output Test Voltage, Method B, V _{IORM} × 1.875 = V _{PR} , 100% Production Test with t _m = 1 s, Partial Discharge < 5 pC	1594	V _{peak}
V _{IORM}	Maximum Working Insulation Voltage	850	V _{peak}
V _{IOTM}	Highest Allowable Over-Voltage	6000	V _{peak}
	External Creepage	≥7	mm
	External Clearance	≥7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥0.5	mm
T _S	Case Temperature (Note 1)	175	°C
I _{S,INPUT}	Input Current (Note 1)	350	mA
P _{S,OUTPUT}	Output Power (Note 1)	800	mW
R _{IO}	Insulation Resistance at T _S , V _{IO} = 500 V (Note 1)	>10 ⁹	Ω

1. Safety limit values – maximum values allowed in the event of a failure.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Max	Unit
--------	-----------	-----	------

TOTAL DEVICE

T _{STG}	Storage Temperature	-40 to +125	°C
T _{OPR}	Operating Temperature	-40 to +100	°C
T _J	Junction Temperature	-40 to +125	°C
T _{SOL}	Lead Solder Temperature	260 for 10 seconds	°C
P _D	Total Device Power Dissipation @ T _A = 25°C	290	mW
	Derate Above 25°C	3.5	mW/°C

EMITTER

I _F	DC / Average Forward Input Current	60	mA
V _R	Reverse Input Voltage	6.0	V
I _{F(pk)}	Forward Current – Peak (1 μs pulse, 300 pps)	3.0	A
P _D	LED Power Dissipation @ T _A = 25°C	90	mW
	Derate Above 25°C	1.8	mW/°C

DETECTOR

V _{CEO}	Collector Emitter Voltage	H11G1M	100	V
		H11G2M	80	V
P _D	Photodetector Power Dissipation @ T _A = 25°C		200	mW
	Derate Above 25°C		2.67	mW/°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

H11G1M, H11G2M

ELECTRICAL CHARACTERISTICS – INDIVIDUAL COMPONENT CHARACTERISTICS

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
EMITTER						
V_F	Forward Voltage	$I_F = 10\text{ mA}$	–	1.3	1.5	V
$\Delta V_F/\Delta T_A$	Forward Voltage Temperature Coefficient		–	–1.8	–	mV/°C
BV_R	Reverse Breakdown Voltage	$I_R = 10\ \mu\text{A}$	3.0	25	–	V
C_J	Junction Capacitance	$V_F = 0\text{ V}, f = 1\text{ MHz}$	–	50	–	pF
		$V_F = 1\text{ V}, f = 1\text{ MHz}$	–	65	–	pF
I_R	Reverse Leakage Current	$V_R = 3.0\text{ V}$	–	0.001	10	μA

DETECTOR

BV_{CEO}	Breakdown Voltage Collector to Emitter	H11G1M	$I_C = 1.0\text{ mA}, I_F = 0$	100	–	–	V
		H11G2M		80	–	–	V
BV_{CBO}	Collector to Base	H11G1M	$I_C = 100\ \mu\text{A}$	100	–	–	V
		H11G2M		80	–	–	V
BV_{EBO}	Emitter Base			7	10	–	V
I_{CEO}	Leakage Current Collector to Emitter	H11G1M	$V_{CE} = 80\text{ V}, I_F = 0$	–	–	100	nA
		H11G2M	$V_{CE} = 60\text{ V}, I_F = 0$	–	–	100	nA
		H11G1M	$V_{CE} = 80\text{ V}, I_F = 0, T_A = 80^\circ\text{C}$	–	–	100	μA
		H11G2M	$V_{CE} = 60\text{ V}, I_F = 0, T_A = 80^\circ\text{C}$	–	–	100	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ELECTRICAL CHARACTERISTICS – TRANSFER CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
EMITTER						
CTR	Current Transfer Ratio, Collector to Emitter	$I_F = 10\text{ mA}, V_{CE} = 1\text{ V}$	100 (1000)	–	–	mA (%)
		$I_F = 1\text{ mA}, V_{CE} = 5\text{ V}$	5 (500)	–	–	mA (%)
$V_{CE(SAT)}$	Saturation Voltage	$I_F = 16\text{ mA}, I_C = 50\text{ mA}$,	–	0.85	1.0	V
		$I_F = 1\text{ mA}, I_C = 1\text{ mA}$,	–	0.75	1.0	V

SWITCHING TIMES

t_{ON}	Turn on Time	$R_L = 100\ \Omega, I_F = 10\text{ mA}, V_{CE} = 5\text{ V}, f \leq 30\text{ Hz}, \text{Pulse Width} \leq 300\ \mu\text{s}$	–	5	–	μs
t_{OFF}	Turn off Time		–	100	–	μs

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ELECTRICAL CHARACTERISTICS – ISOLATION CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{ISO}	Input–Output Isolation Voltage	$t = 1\text{ Minute}$	4170	–	–	V_{ACRMS}
C_{ISO}	Isolation Capacitance	$V_{I-O} = 0\text{ V}, f = 1\text{ MHz}$	–	0.2	–	pF
R_{ISO}	Isolation Resistance	$V_{I-O} = \pm 500\text{ VDC}, T_A = 25^\circ\text{C}$	10^{11}	–	–	Ω

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

H11G1M, H11G2M

TYPICAL PERFORMANCE CURVES

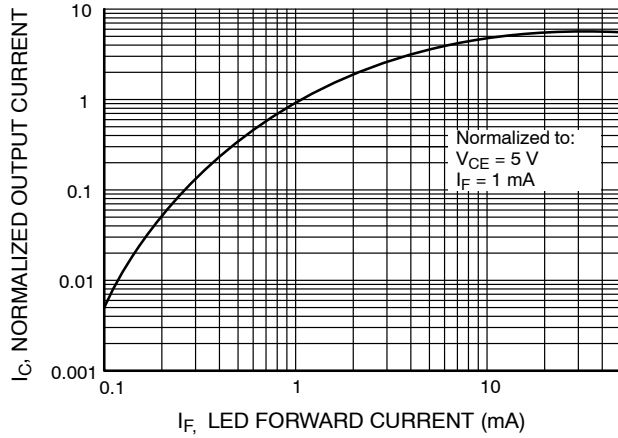


Figure 1. Output Current vs. Input Current

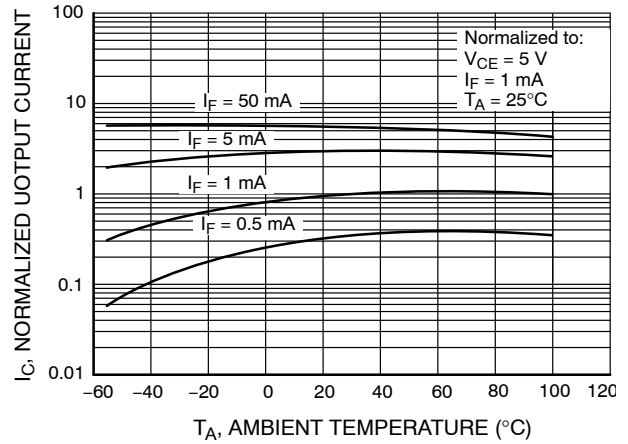


Figure 2. Normalized Output Current vs. Temperature

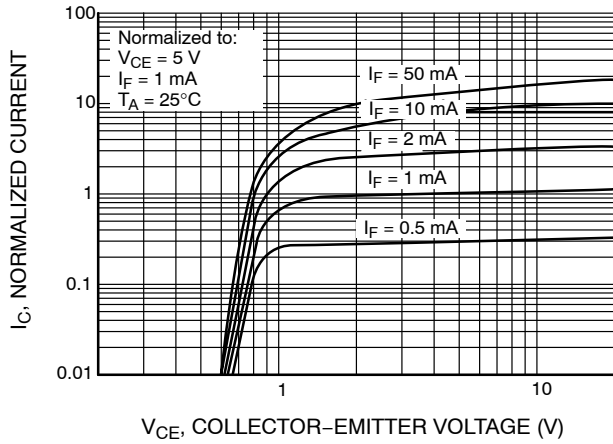


Figure 3. Output Current vs. Collector-Emitter Voltage

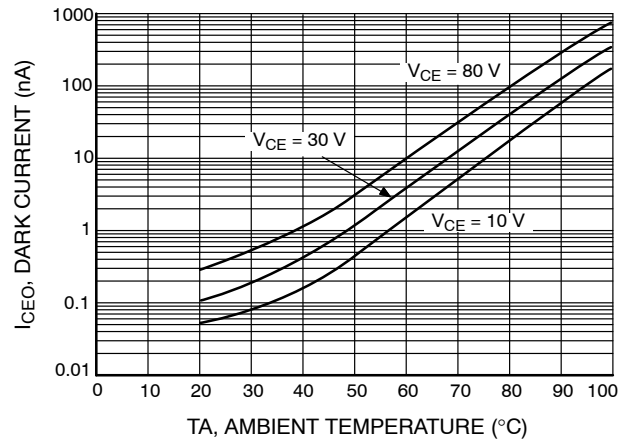


Figure 4. Collector-Emitter Dark Current vs. Ambient Temperature

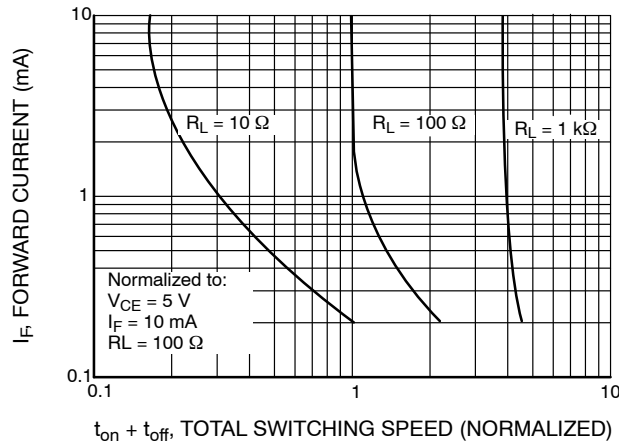
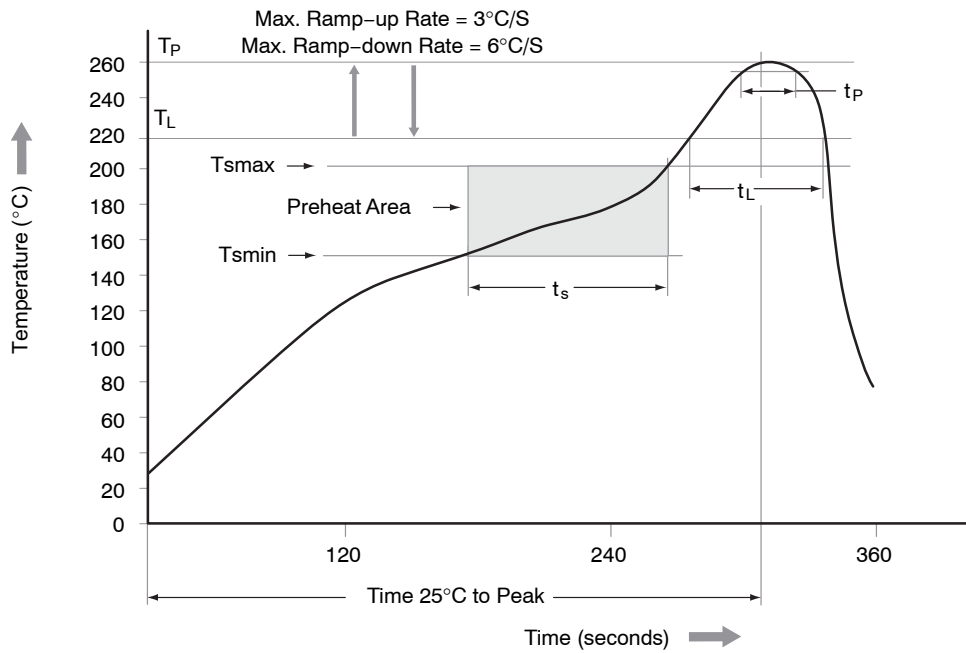


Figure 5. Input Current vs. Total Switching Speed (Typical Values)

H11G1M, H11G2M

REFLOW PROFILE



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T _{smin})	150°C
Temperature Max. (T _{smax})	200°C
Time (t _s) from (T _{smin} to T _{smax})	60–120 seconds
Ramp-up Rate (t _L to t _p)	3°C/second max.
Liquidous Temperature (T _L)	217°C
Time (t _L) Maintained Above (T _L)	60–150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t _p) within 5°C of 260°C	30 seconds
Ramp-down Rate (T _p to T _L)	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Figure 6. Reflow Profile

H11G1M, H11G2M

ORDERING INFORMATION

Part Number	Package	Shipping†
H11G1M	DIP 6-Pin	50 Units / Tube
H11G1SM	SMT 6-Pin (Lead Bend)	50 Units / Tube
H11G1SR2M	SMT 6-Pin (Lead Bend)	1000 / Tape & Reel
H11G1VM	DIP 6-Pin, DIN EN/IEC60747-5-5 Option	50 Units / Tube
H11G1SVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	50 Units / Tube
H11G1SR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	1000 / Tape & Reel
H11G1TVM	DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option	50 Units / Tube

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

ON Semiconductor®



PDIP6 8.51x6.35, 2.54P
CASE 646BX
ISSUE O

DATE 31 JUL 2016



NOTES:

- A) NO STANDARD APPLIES TO THIS PACKAGE.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSION

DOCUMENT NUMBER:	98AON13449G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	PDIP6 8.51X6.35, 2.54P	PAGE 1 OF 1

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

ON Semiconductor®



PDIP6 8.51x6.35, 2.54P

CASE 646BY

ISSUE A

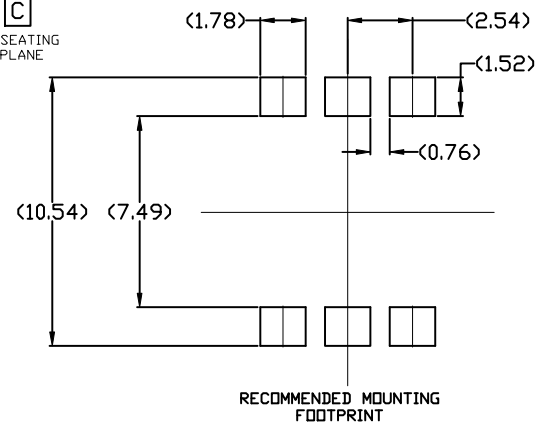
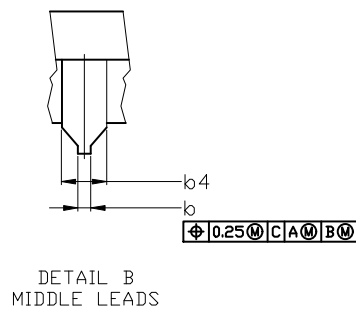
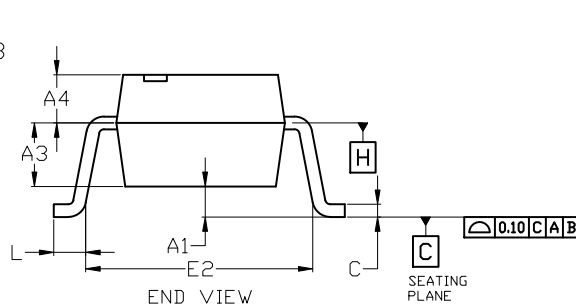
DATE 15 JUL 2019



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS A, A1, AND L ARE MEASURED WITH THE PACKAGE SEATED.
4. DIMENSIONS D, D1, AND E1 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE NOT TO EXCEED 2.54mm.
5. PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE CORNERS).
6. CENTER LINE OF CORNER LEADS ARE LOCATED BY LOCATING THE CENTER OF FEATURE b2 AND b3.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	---	---	4.80
A1	0.38	---	---
A2	3.28	3.40	3.53
A3	2.49 REF		
A4	1.89 REF		
b	0.41	0.46	0.51
b1	0.76	0.92	1.14
b2	0.25	0.28	0.36
b3	1.02	1.40	1.78
b4	1.778 REF		
c	0.20	0.25	0.30
D	8.13	8.51	8.89
D1	0.86 REF		
E	6.10	6.35	6.60
E1	8.43	9.17	9.90
E2	8.13 REF		
e	2.54 BSC		
L	0.16	0.52	0.88



For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

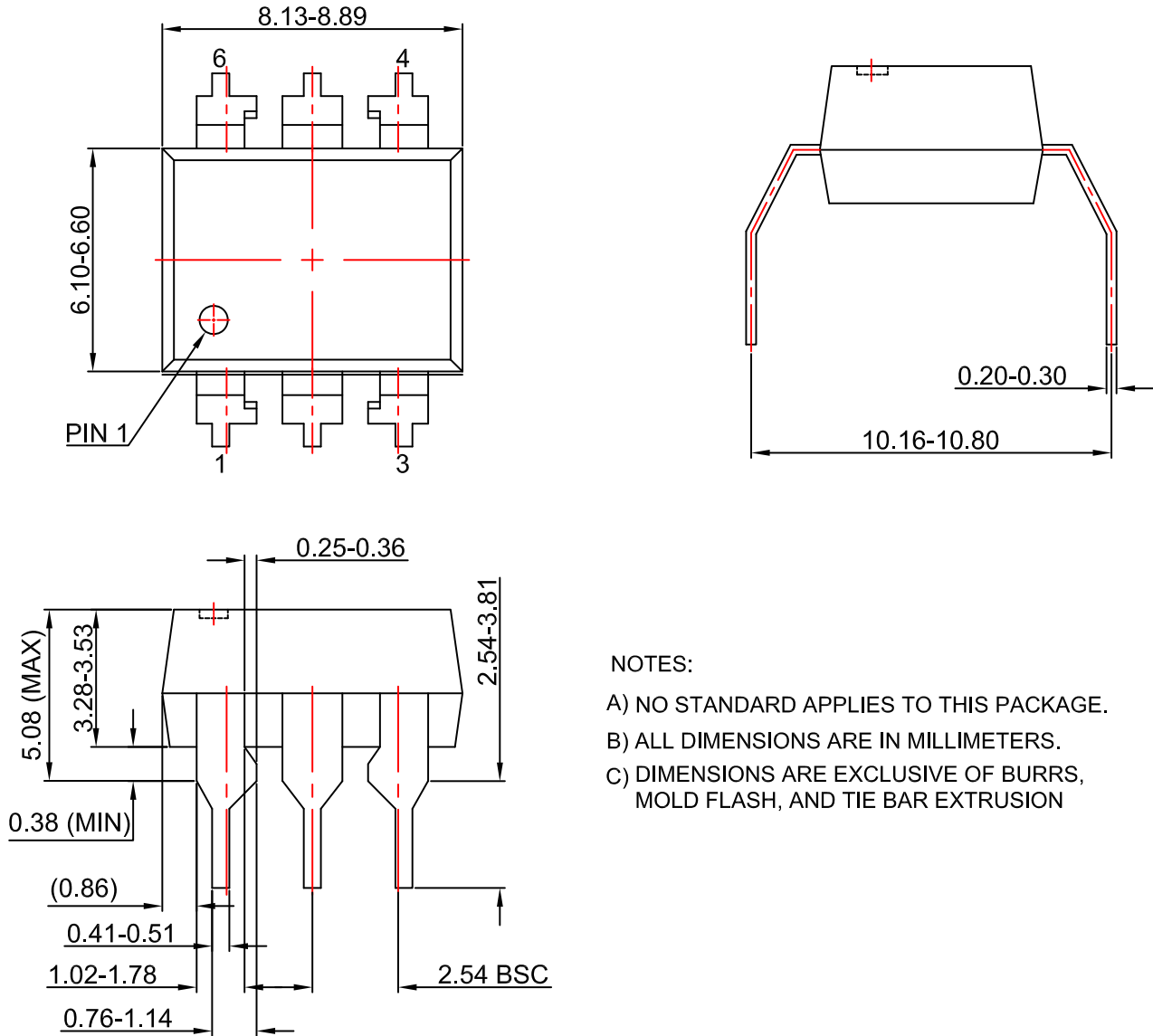
DOCUMENT NUMBER:	98AON13450G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	PDIP6 8.51x6.35, 2.54P	PAGE 1 OF 1

ON Semiconductor and ON are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS

PDIP6 8.51x6.35, 2.54P
CASE 646BZ
ISSUE O

DATE 31 JUL 2016



NOTES:

- A) NO STANDARD APPLIES TO THIS PACKAGE.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSION

DOCUMENT NUMBER:	98AON13451G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	PDIP6 8.51X6.35, 2.54P	PAGE 1 OF 1

ON Semiconductor and **ON** are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Email Requests to: orderlit@onsemi.com

ON Semiconductor Website: www.onsemi.com

TECHNICAL SUPPORT

North American Technical Support:
Voice Mail: 1 800-282-9855 Toll Free USA/Canada
Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative