# onsemi

# FGHL40T120RWD

#### Description

Using the novel field stop 7th generation IGBT technology and the Gen7 Diode in TO247 3–lead package, FGHL40T120RWD offers the optimum performance with low conduction losses and good switching controllability for a high efficiency operation in various applications like motor control, UPS, data center and high–power switch.

#### Features

- Low Conduction Loss and Optimized Switching
- Maximum Junction Temperature  $T_J = 175^{\circ}C$
- Positive Temperature Coefficient for Easy Parallel Operation
- High Current Capability
- 100% of the Parts are Dynamically Tested
- Short Circuit Rated
- RoHS Compliant

#### Applications

- Motor Control
- UPS
- General Application Requiring High Power Switch

#### **MAXIMUM RATINGS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

Parameter				
Collector-to-Emitter Voltage				
	V <sub>GES</sub>	±20		
r Voltage		±30		
T <sub>C</sub> = 25°C (Note 1)	۱ <sub>C</sub>	80	А	
$T_C = 100^{\circ}C$		40		
$T_{C} = 25^{\circ}C$	PD	600	W	
$T_C = 100^{\circ}C$		300		
$\begin{array}{l} T_C = 25^\circ C \text{ (Note 2)},\\ t_p = 10 \ \mu s \end{array}$	I <sub>CM</sub>	120	A	
T <sub>C</sub> = 25°C (Note 1)	١ <sub>F</sub>	80		
$T_C = 100^{\circ}C$		40		
T <sub>C</sub> = 25°C, t <sub>p</sub> = 10 μs (Note 1)	I <sub>FM</sub>	120		
Short Circuit Withstand Time $V_{GE}$ = 15 V, $V_{CC}$ = 600 V, $T_{C}$ = 150°C			μs	
Operating Junction and Storage Temperature Range			°C	
Lead Temperature for Soldering Purposes				
	r Voltage $T_C = 25^{\circ}C \text{ (Note 1)}$ $T_C = 100^{\circ}C$ $T_C = 25^{\circ}C$ $T_C = 25^{\circ}C$ $T_C = 25^{\circ}C \text{ (Note 2)},$ $t_p = 10 \ \mu\text{s}$ $T_C = 25^{\circ}C \text{ (Note 1)}$ $T_C = 25^{\circ}C \text{ (Note 1)}$ $T_C = 25^{\circ}C,$ $t_p = 10 \ \mu\text{s} \text{ (Note 1)}$ me $T_C = 150^{\circ}C$ torage Temperature	J J   age V <sub>CES</sub> VGES VGES   T <sub>C</sub> = 25°C (Note 1) I <sub>C</sub> T <sub>C</sub> = 100°C P <sub>D</sub> T <sub>C</sub> = 100°C P <sub>D</sub> T <sub>C</sub> = 25°C (Note 2), t <sub>p</sub> = 10 µs I <sub>CM</sub> T <sub>C</sub> = 25°C (Note 2), t <sub>p</sub> = 10 µs I <sub>CM</sub> T <sub>C</sub> = 25°C (Note 1) I <sub>F</sub> T <sub>C</sub> = 25°C, t <sub>p</sub> = 10 µs (Note 1) I <sub>F</sub> T <sub>C</sub> = 150°C T <sub>SC</sub> torage Temperature T <sub>J</sub> , T <sub>stg</sub>	$\begin{array}{c c c c c c c } & V_{CES} & 1200 \\ \hline V_{CES} & 1200 \\ \hline V_{GES} & \pm 20 \\ \hline & & & & & & \\ \hline & & & & & \\ \hline T_C = 25^\circ C \ (Note 1) & I_C & 80 \\ \hline T_C = 100^\circ C & & & & \\ \hline & & & & & & \\ \hline T_C = 100^\circ C & & & & \\ \hline & & & & & \\ \hline T_C = 25^\circ C \ (Note 2), & & & \\ t_p = 10 \ \mu s & & & \\ \hline & & & & & \\ \hline T_C = 25^\circ C \ (Note 1) & I_F & & \\ \hline & & & & & \\ \hline & & & & & \\ \hline T_C = 25^\circ C, & & & I_{FM} & & \\ \hline & & & & & \\ \hline & & & & & \\ \hline & & & &$	

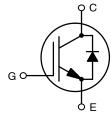
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Value limit by bond wire.

2. Repetitive rating: pulse width limited by max. Junction temperature.

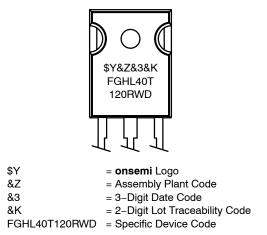
BV <sub>CES</sub>	V <sub>CE(SAT)</sub>	I <sub>C</sub>
1200 V	1.5 V	40 A

**PIN CONNECTIONS** 





#### MARKING DIAGRAM



#### **ORDERING INFORMATION**

Device	Package	Shipping
FGHL40T120RWD	TO-247 (Pb-Free)	30 Units / Tube

#### THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case for IGBT	$R_{\theta JC}$	0.25	°C/W
Thermal Resistance, Junction-to-Case for Diode	$R_{\theta JCD}$	0.42	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	40	

# ELECTRICAL CHARACTERISTICS OF IGBT (T<sub>J</sub> = 25°C unless otherwise noted)

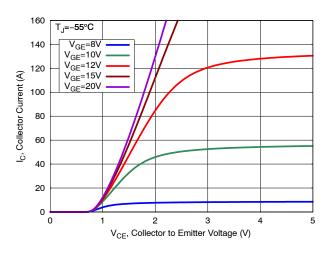
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
OFF CHARACTERISTICS						-
Collector-to-Emitter Breakdown Voltage	BV <sub>CES</sub>		1200			V
Collector-to-Emitter Breakdown Voltage Temperature Coefficient	$\Delta BV_{CES}/\Delta T_{J}$	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 5 mA		1226		mV/°C
Zero Gate Voltage Collector Current	ICES	$V_{GE}$ = 0 V, $V_{CE}$ = $V_{CES}$			40	μA
Gate-to-Emitter Leakage Current	I <sub>GES</sub>	$V_{GE}$ = 20 V, $V_{CE}$ = 0 V			±400	nA
ON CHARACTERISTICS					•	
Gate Threshold Voltage	V <sub>GE(th)</sub>	$V_{GE} = V_{CE}, I_C = 40 \text{ mA}, \\ T_J = 25^{\circ}\text{C}$	4.9	5.94	6.7	V
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 40 \text{ A}, $ $T_{J} = 25^{\circ}\text{C}$	1.2	1.49	1.8	
		$V_{GE}$ = 15 V, I <sub>C</sub> = 40 A, T <sub>J</sub> = 175°C		1.83		
DYNAMIC CHARACTERISTICS						-
Input Capacitance	Cies			4670		pF
Output Capacitance	C <sub>oes</sub>	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 30 V, f = 1 MHz		171		
Reverse Transfer Capacitance	C <sub>res</sub>			16.7		
Total Gate Charge	Qg			174		nC
Gate-to-Emitter Charge	Q <sub>ge</sub>	$V_{CE} = 600 \text{ V}, V_{GE} = 15 \text{ V},$ $I_{C} = 40 \text{ A}$		42.2		
Gate-to-Collector Charge	Q <sub>gc</sub>	IC = 40 A		73		
SWITCHING CHARACTERISTICS						
Turn-on Delay Time	t <sub>d(on)</sub>			37		ns
Turn-off Delay Time	t <sub>d(off)</sub>			269		
Rise Time	t <sub>r</sub>			22		
Fall Time	t <sub>f</sub>	$V_{CE}$ = 600 V, $V_{GE}$ = 0/15 V, I <sub>C</sub> = 20 A R <sub>G</sub> = 4.7 Ω, T <sub>J</sub> = 25°C		136		
Turn-on Switching Loss	E <sub>on</sub>	10 - 20 / 110 - 11/ 11, 1 <u>1</u> - 20 0		1.2		mJ
Turn-off Switching Loss	E <sub>off</sub>			1.4		
Total Switching Loss	E <sub>ts</sub>			2.6		
Turn-on Delay Time	t <sub>d(on)</sub>			38		ns
Turn-off Delay Time	t <sub>d(off)</sub>	$V_{CE}$ = 600 V, $V_{GE}$ = 0/15 V, I <sub>C</sub> = 40 A R <sub>G</sub> = 4.7 Ω, T <sub>J</sub> = 25°C		184		
Rise Time	t <sub>r</sub>			46		1
Fall Time	t <sub>f</sub>			134		1
Turn-on Switching Loss	E <sub>on</sub>			2.9		mJ
Turn-off Switching Loss	E <sub>off</sub>	1		2.1		1
Total Switching Loss	E <sub>ts</sub>	1		5.0		1

# **ELECTRICAL CHARACTERISTICS OF IGBT** ( $T_J$ = 25°C unless otherwise noted) (continued)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS						
Turn-on Delay Time	t <sub>d(on)</sub>			34		ns
Turn-off Delay Time	t <sub>d(off)</sub>			328		
Rise Time	t <sub>r</sub>			24		
Fall Time	t <sub>f</sub>	$V_{GE} = 0/15 \text{ V}, \text{ I}_{C} = 20 \text{ A},$ $V_{CE} = 600 \text{ V}, \text{ R}_{G} = 4.7 \Omega,$		240		
Turn-on Switching Loss	E <sub>on</sub>	$T_J = 175^{\circ}C$		2.2		mJ
Turn-off Switching Loss	E <sub>off</sub>			2.2		
Total Switching Loss	E <sub>ts</sub>			4.4		
Turn-on Delay Time	t <sub>d(on)</sub>			38		ns
Turn–off Delay Time	t <sub>d(off)</sub>			213		
Rise Time	t <sub>r</sub>	V 0/15 V I 20 A		51		
Fall Time	t <sub>f</sub>	$V_{GE}$ = 0/15 V, I <sub>C</sub> = 20 A, $V_{CE}$ = 600 V, R <sub>G</sub> = 4.7 Ω, $T_{J}$ = 175°C		205		
Turn-on Switching Loss	E <sub>on</sub>	T <sub>J</sub> = 175°C		4.5		mJ
Turn-off Switching Loss	E <sub>off</sub>			2.9		
Total Switching Loss	E <sub>ts</sub>			7.4		
DIODE CHARACTERISTICS						
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 40 A, T <sub>J</sub> = 25°C	1.46	1.69	2.08	V
		I <sub>F</sub> = 40 A, T <sub>J</sub> = 175°C		1.63		
DIODE SWITCHING CHARACTERISTIC	S, INDUCTIVE LOAD	)				
Reverse Recovery Time	t <sub>rr</sub>			163		ns
Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>R</sub> = 600 V, I <sub>F</sub> = 20 A,		1462		nC
Reverse Recovery Energy	E <sub>REC</sub>	$dI_F/dt = 500 \text{ A}/\mu \text{s}, T_J = 25^{\circ}\text{C}$		0.5		mJ
Peak Reverse Recovery Current	I <sub>RRM</sub>			17.9		Α
Reverse Recovery Time	t <sub>rr</sub>			248		ns
Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>R</sub> = 600 V, I <sub>F</sub> = 40 A,		2372		nC
Reverse Recovery Energy	E <sub>REC</sub>	$dI_F/dt = 500 \text{ A/}\mu\text{s}, T_J = 25^{\circ}\text{C}$		0.8		mJ
Peak Reverse Recovery Current	I <sub>RRM</sub>			19.2		Α
Reverse Recovery Time	t <sub>rr</sub>			269		ns
Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>R</sub> = 600 V, I <sub>F</sub> = 20 A,		3447		nC
Reverse Recovery Energy	E <sub>REC</sub>	$d_{\rm F}/dt = 500 \text{ A}/\mu \text{s}, T_{\rm J} = 175^{\circ}\text{C}$		1.3		mJ
Peak Reverse Recovery Current	I <sub>RRM</sub>			25.6		А
Reverse Recovery Time	t <sub>rr</sub>		1	422		ns
Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>R</sub> = 600 V, I <sub>F</sub> = 40 A,		5717		nC
Reverse Recovery Energy	E <sub>REC</sub>	$dI_F/dt = 500 \text{ A}/\mu\text{s}, T_J = 175^{\circ}\text{C}$		2.3		mJ
		1				

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

# **TYPICAL CHARACTERISTICS**





160

140

120

100 80

60

40

20

0

0

I<sub>C</sub>, Collector Current (A)

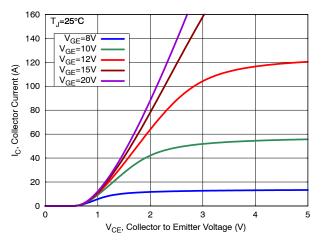
T<sub>J</sub>=175°C

V<sub>GE</sub>=8V V<sub>GE</sub>=10V

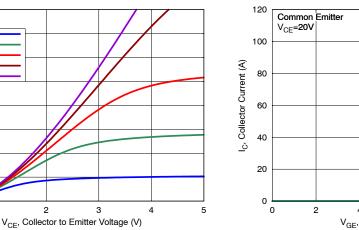
V<sub>GE</sub>=12V

V<sub>GE</sub>=15V

V<sub>GE</sub>=20V







**Figure 3. Output Characteristics** 

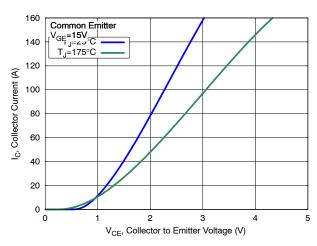


Figure 5. Saturation Characteristics

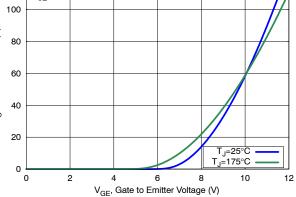
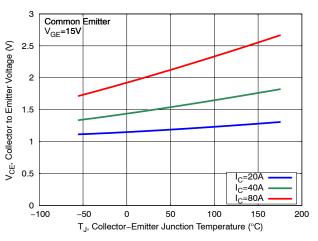
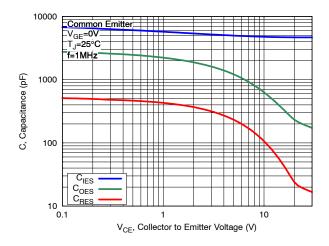


Figure 4. Transfer Characteristics

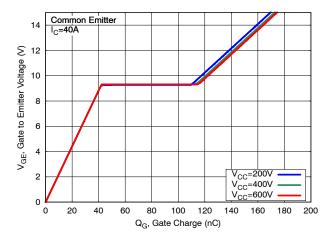




# **TYPICAL CHARACTERISTICS**









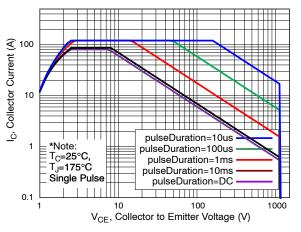


Figure 9. SOA Characteristics

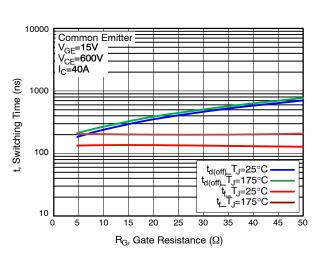


Figure 11. Turn-Off Switching Time vs. Gate Resistance

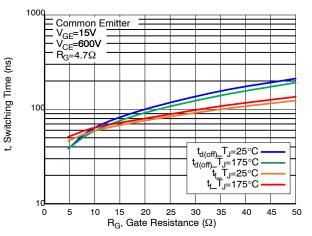


Figure 10. Turn-On Switching Time vs Gate Resistance

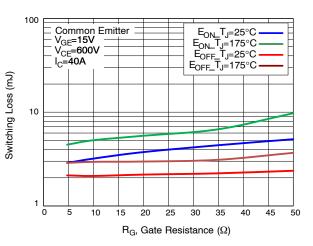
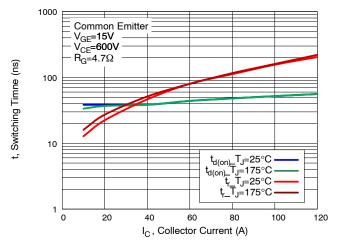


Figure 12. Switching Loss vs. Gate Resistance

#### **TYPICAL CHARACTERISTICS**





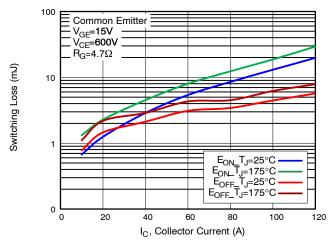


Figure 15. Switching Loss vs. Collector Current

100

80

60

40

20

0

400

600

800

I<sub>RRM</sub>, Reverse Recovery Current (A)

V<sub>R</sub>=600V I<sub>F</sub>=40A

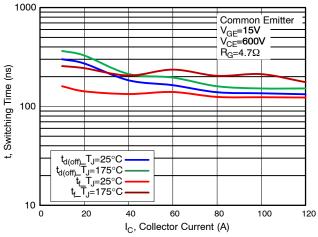
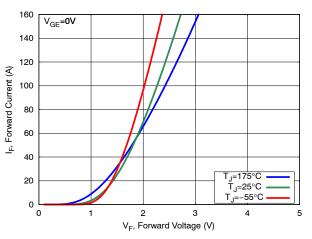
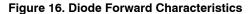
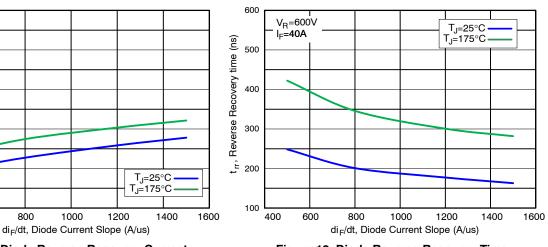


Figure 14. Turn-Off Switching Time vs. Collector Current



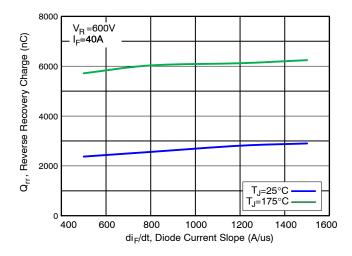




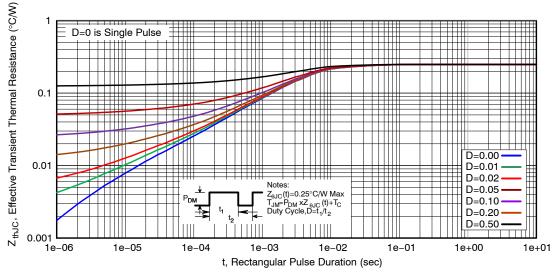




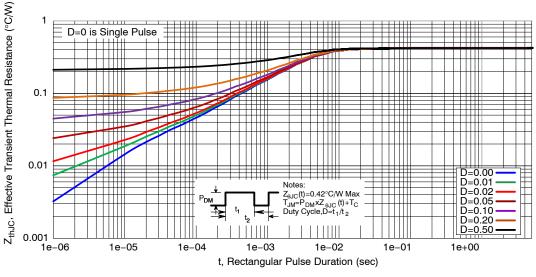
#### **TYPICAL CHARACTERISTICS**





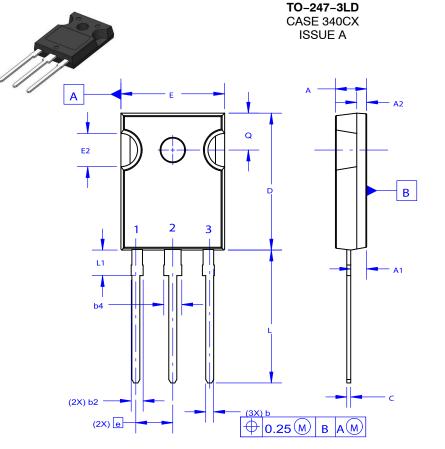












NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

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# GENERIC **MARKING DIAGRAM\*** Х



XXXXX	= Specific Device Code
Α	= Assembly Location

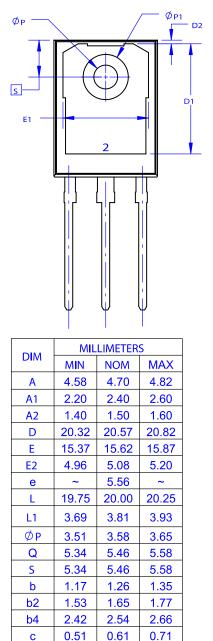
- = Assembly Location
- = Year
- ww = Work Week
- G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ", may or may not be present. Some products may not follow the Generic Marking.

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DATE 06 JUL 2020



D1

D2

E1

ØP1

13.08

0.51

12.81

6.60

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0.93

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6.80

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1.35

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7.00

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