

# Silicon Carbide (SiC) **Schottky Diode** - EliteSiC, 40 A, 650 V, D1, TO-247-2L

# FFSH4065A

#### Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size & cost.

#### **Features**

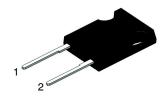
- Max Junction Temperature 175°C
- Avalanche Rated 182 mJ

- No Reverse Recovery/No Forward Recovery
   This Device is Pb–Free, Halogen Free/BFR Free and RoHS
  Compliant

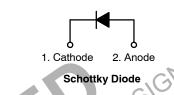
  Applications
   General Purpose
   SMPS, Solar Inverter, UPS
   Power Switching Circuits

  Applications

  ORDFPIN



TO-247-2LD CASE 340CL



# MARKING DIAGRAM



= Assembly Plant Code

= Date Code (Year & Week)

= Lot Traceability Code

= Specific Device Code

### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of

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# ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C unless otherwise noted)

Symbol	Parameter	Value	Unit	
$V_{RRM}$	Peak Repetitive Reverse Voltage	650	V	
E <sub>AS</sub>	Single Pulse Avalanche Energy (Note 1)	182	mJ	
l <sub>F</sub>	Continuous Rectified Forward Current @ T <sub>C</sub> <	40	А	
	Continuous Rectified Forward Current @ T <sub>C</sub> <	48	А	
I <sub>F, Max</sub>	Non-Repetitive Peak Forward Surge Current	T <sub>C</sub> = 25°C, 10 μs	1300	А
		T <sub>C</sub> = 150°C, 10 μs	1200	А
I <sub>F,SM</sub>	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	180	Α
I <sub>F,RM</sub>	Repetitive Forward Surge Current	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	85	Α
Ptot	Power Dissipation	T <sub>C</sub> = 25°C	349	W
		T <sub>C</sub> = 150°C	58	W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	√ °C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

# THERMAL CHARACTERISTICS

Symbol	Parameter			Value	Unit
$R_{ hetaJC}$	Thermal Resistance, Junction to Case, Max		50,	0.43	°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
V <sub>F</sub>	Forward Voltage	$I_F = 40 \text{ A}, T_C = 25^{\circ}\text{C}$	10-11	1.50	1.75	V
		$I_F = 40 \text{ A}, T_C = 125^{\circ}\text{C}$	0R-11	1.60	2.0	
		$I_F = 40 \text{ A}, T_C = 175^{\circ}\text{C}$	5	1.72	2.4	
I <sub>R</sub>	Reverse Current	$V_R = 650 \text{ V}, T_C = 25^{\circ}\text{C}$	ı	-	200	μΑ
	No. CR	V <sub>R</sub> = 650 V, T <sub>C</sub> = 125°C	ı	-	400	
		$V_R = 650 \text{ V}, T_C = 175^{\circ}\text{C}$	ı	-	600	
$Q_{C}$	Total Capacitive Charge	V = 400 V	ı	119	-	nC
С	Total Capacitance	V <sub>R</sub> = 1 V, f = 100 kHz	ı	1989	-	pF
11	S SEY	V <sub>R</sub> = 200 V, f = 100 kHz	_	218	_	
1/1		V <sub>R</sub> = 400 V, f = 100 kHz	=	164	_	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### **ORDERING INFORMATION**

Part Number	Top Marking	Package	Shipping
FFSH4065A	FFSH4065A	TO-247-2LD	30 Units / Tube

<sup>1.</sup>  $E_{AS}$  of 182 mJ is based on starting  $T_J = 25^{\circ}C$ , L = 0.5 mH,  $I_{AS} = 27$  A, V = 50 V.

# **TYPICAL CHARACTERISTICS**

(T<sub>J</sub> = 25°C UNLESS OTHERWISE NOTED)

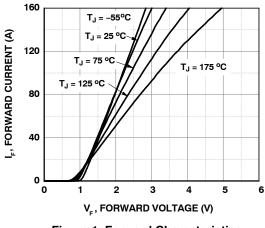


Figure 1. Forward Characteristics

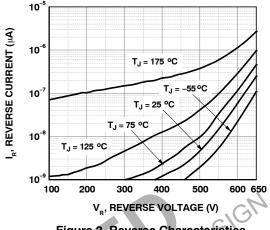


Figure 2. Reverse Characteristics

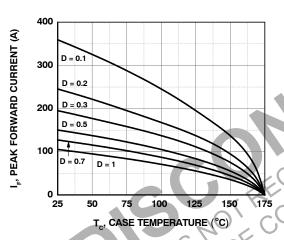


Figure 3. Current Derating

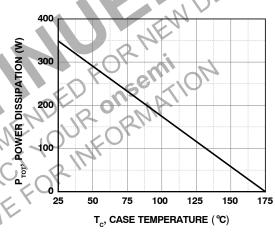


Figure 4. Power Derating

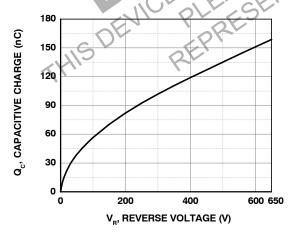


Figure 5. Capacitive Charge vs. Reverse Voltage

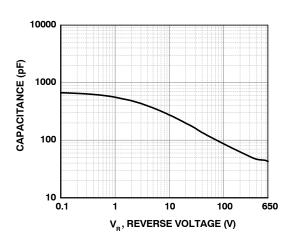


Figure 6. Capacitance vs. Reverse Voltage

# **TYPICAL CHARACTERISTICS**

(T<sub>J</sub> = 25°C UNLESS OTHERWISE NOTED)

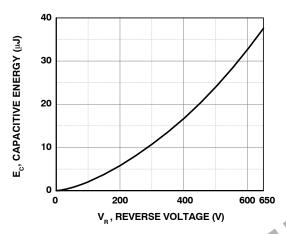
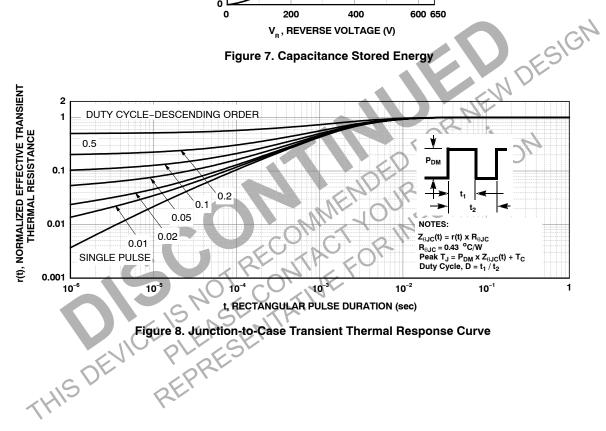
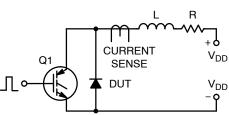


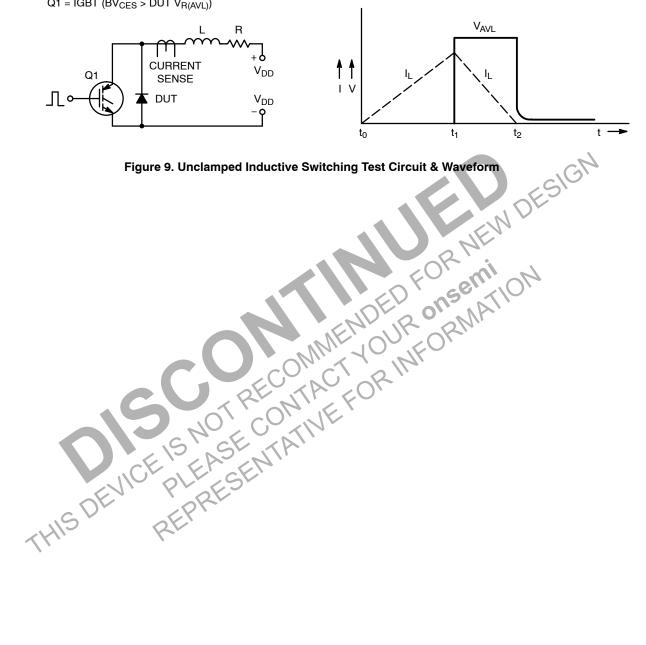
Figure 7. Capacitance Stored Energy



#### **TEST CIRCUIT AND WAVEFORMS**

L = 0.5 mH $R < 0.1 \Omega$  $V_{DD} = 50 \text{ V}$ EAVL = 1/2LI2 [V<sub>R(AVL)</sub> / (V<sub>R(AVL)</sub> - V<sub>DD</sub>)] Q1 = IGBT (BV<sub>CES</sub> > DUT V<sub>R(AVL)</sub>)

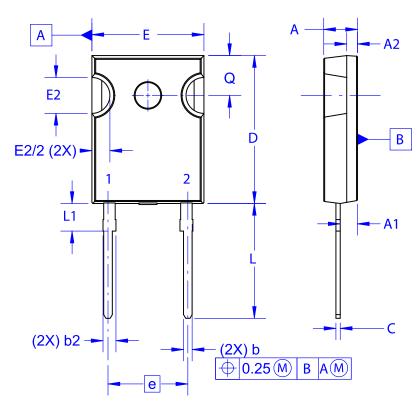




**DATE 03 DEC 2019** 



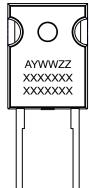






- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
  D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

### **GENERIC MARKING DIAGRAM\***



XXXX = Specific Device Code

= Assembly Location

= Year

= Work Week WW

= Assembly Lot Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

Ø P —		Ø P1 D2
S E1 —		D1
		J

DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.58	4.70	4.82		
A1	2.29	2.40	2.66		
A2	1.30	1.50	1.70		
b	1.17	1.26	1.35		
b2	1.53	1.65	1.77		
С	0.51	0.61	0.71		
D	20.32	20.57	20.82		
D1	16.37	16.57	16.77		
D2	0.51	0.93	1.35		
Е	15.37	15.62	15.87		
E1	12.81	~	~		
E2	4.96	5.08	5.20		
е	~	11.12	~		
L	15.75	16.00	16.25		
L1	3.69	3.81	3.93		
ØΡ	3.51	3.58	3.65		
Ø <b>P</b> 1	6.61	6.73	6.85		
Q	5.34	5.46	5.58		
S	5.34	5.46	5.58		

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DESCRIPTION:	TO-247-2LD		PAGE 1 OF 1		

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