MOSFET - N-Channel, UniFET™, FRFET®

200 V, 18 A, 140 mΩ

FDPF18N20FT, FDP18N20F

Description

UniFET MOSFET is **onsemi**'s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on–state resistance, and to provide better switching performance and higher avalanche energy strength. The body diode's reverse recovery performance of UniFET FRFET MOSFET has been enhanced by lifetime control. Its t_{rr} is less than 100 ns and the reverse dv/dt immunity is 15 V/ns while normal planar MOSFETs have over 200 ns and 4.5 V/ns respectively. Therefore, it can remove additional component and improve system reliability in certain applications in which the performance of MOSFET's body diode is significant. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

Features

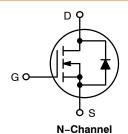
- $R_{DS(on)} = 120 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 9 \text{ A}$
- Low Gate Charge (Typ. 20 nC)
- Low C_{rss} (Typ. 24 pF)
- 100% Avalanche Tested

Applications

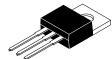
- LCD/LED TV
- Consumer Appliances
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply

V _{DS}	R _{DS(ON)} MAX	I _D MAX	
200 V	140 mΩ @ 10 V	18 A*	

^{*}Drain current limited by maximum junction temperature.







TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT

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MARKING DIAGRAM



FDPF18N20FT = Specific Device Code
A = Assembly Location
YWW = Date Code (Year & Week)
ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FDPF18N20FT	TO-220F	1000 Units / Tube
FDP18N20F	TO-220	1000 Units / Tube

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MOSFET MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter		FDP18N20F FDPF18N20FT		Unit
V _{DSS}	Drain to Source Voltage		2	200	
V _{GSS}	Gate to Source Voltage		±30		V
I _D	Drain Current	– Continuous (T _C = 25°C)	18	18*	Α
		- Continuous (T _C = 100°C)	10.8	10.8*	
I _{DM}	Drain Current	- Pulsed (Note 1)	72	72*	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		324		mJ
I _{AR}	Avalanche Current (Note 1)			18	
E _{AR}	Repetitive Avalanche Energy (Note 1)			10	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4	4.5	
P_{D}	Power Dissipation	(T _C = 25°C)	100	41	W
		– Derate Above 25°C	0.83	0.33	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		–55 t	-55 to +150	
TL	Maximum Lead Temper 1/8" from Case for 5 Se		300		°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality stresses exceeding those listed in the Maximum Hatings table may damage it should not be assumed, damage may occur and reliability may be affected. *Drain current limited by maximum junction temperature.
1. Repetitive rating: pulse–width limited by maximum junction temperature.
2. L = 2 mH, I_{AS} = 18 A, V_{DD} = 50 V, P_{CD} = 25 P_{CD} , starting P_{CD} = 25°C.
3. P_{CD} = 18 A, di/dt P_{CD} = 200 A/ms, P_{CD} = 8V P_{CD} = 8V P_{CD} starting P_{CD} = 25°C.

THERMAL CHARACTERISTICS

Symbol	Parameter	FDP18N20F	FDPF18N20FT	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	1.2	3.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHAR	ACTERISTICS					
BV _{DSS}	Drain to Source Breakdown Voltage	I_D = 250 μ A, V_{GS} = 0 V, T_J = 25 $^{\circ}$ C	200	_	_	V
ΔBV_{DSS}	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, referenced to 25°C	-	0.2	=	V/°C
ΔT_J						
I_{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 200 V, V _{GS} = 0 V	_	_	10	μΑ
		V _{DS} = 160 V, T _C = 125°C	-	-	100	
I_{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±100	nA
ON CHARA	CTERISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, \ I_D = 250 \ \mu A$	3.0	-	5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 9 A	-	0.12	0.14	Ω
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 9 A	-	13.6	_	S
DYNAMIC (CHARACTERISTICS					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	-	885	1180	pF
C _{oss}	Output Capacitance		-	200	270	pF
C _{rss}	Reverse Transfer Capacitance		-	24	35	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 160 V, I _D = 18 A, V _{GS} = 10 V	-	20	26	nC
Q _{gs}	Gate to Source Gate Charge	(Note 4)	-	5	_	nC
Q _{gd}	Gate to Drain "Miller" Charge	1	-	9	_	nC
SWITCHING	G CHARACTERISTICS			•	•	
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 100 \text{ V}, I_D = 18 \text{ A}, V_{GS} = 10 \text{ V},$	_	16	40	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$ (Note 4)	-	50	110	ns
t _{d(off)}	Turn-Off Delay Time	1 ` '	-	50	110	ns
t _f	Turn-Off Fall Time	1	-	40	90	ns
DRAIN-SO	URCE DIODE CHARACTERISTICS					
Is	Maximum Continuous Drain to Source Diode Forward Current		-	_	18	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		_	_	72	Α
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 18 A	-	_	1.5	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V, } I_{SD} = 18 \text{ A, } dI_F/dt = 100 \text{ A}/\mu\text{s}$	_	80	-	ns
Q _{rr}	Reverse Recovery Charge	1	_	240	_	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

ID, Drain Current (A)

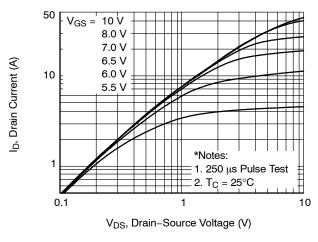


Figure 1. On-Region Characteristics

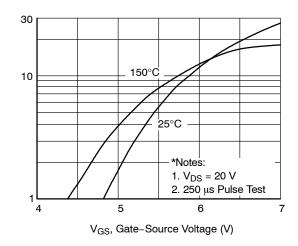


Figure 2. Transfer Characteristics

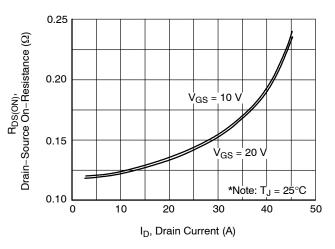


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

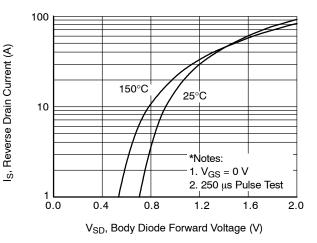


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

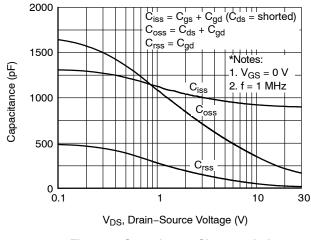


Figure 5. Capacitance Characteristics

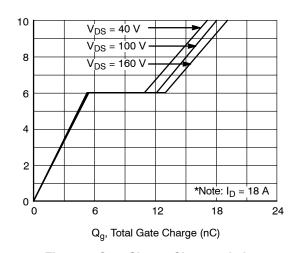


Figure 6. Gate Charge Characteristics

V_{GS}, Gate-Source Voltage (V)

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

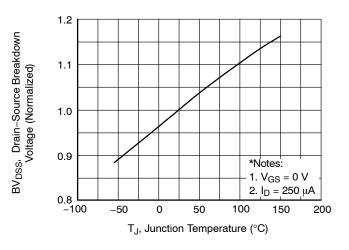


Figure 7. Breakdown Voltage Variation vs. Temperature

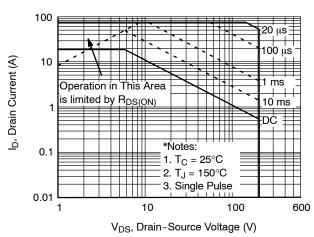


Figure 8. Maximum Safe Operating
Area – FDP18N20F

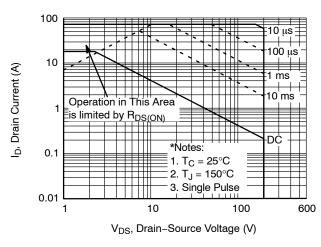


Figure 9. Maximum Safe Operating Area – FDPF18N20FT

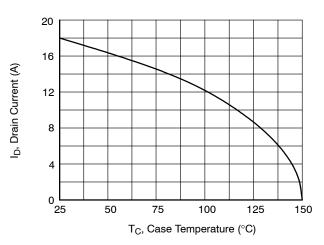


Figure 10. Maximum Drain Current vs. Case Temperature

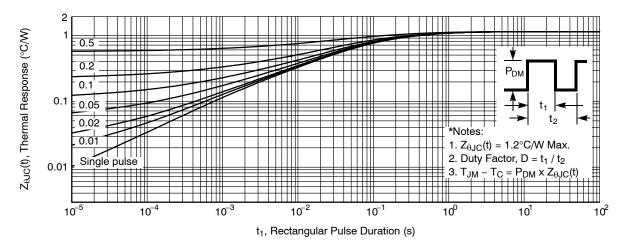


Figure 11. Transient Thermal Response Curve - FDP18N20F

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

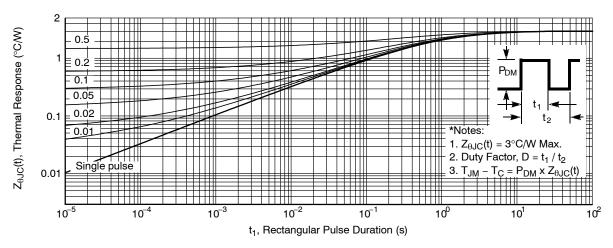


Figure 12. Transient Thermal Response Curve - FDPF18N20FT

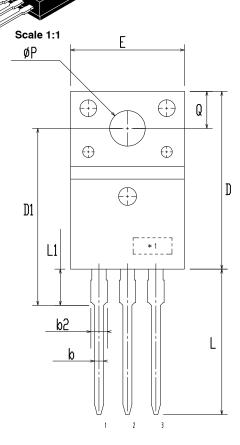
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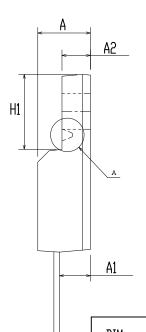
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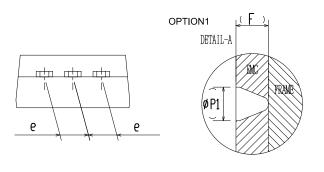


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DATE 19 JAN 2021







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DIM	MIN	NDM	MAX
Α	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	~	~	1.47
С	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9,96	10.16	10.36
е	2.34	2.54	2.74
F	~	0.84	*
H1	6.48	6.68	6.88
L	12.78	12.98	13.18
L1	3.03	3.23	3.43
ØΡ	2.98	3.18	3.38
ø P1	~	1.00	~
Q	3.20	3.30	3.40

MILLIMITERS

NOTES:

- A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCSIONS.

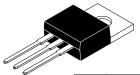
C

C. OPTION 1 - WITH SUPPORT PIN HOLE OPTION 2 - NO SUPPORT PIN HOLE

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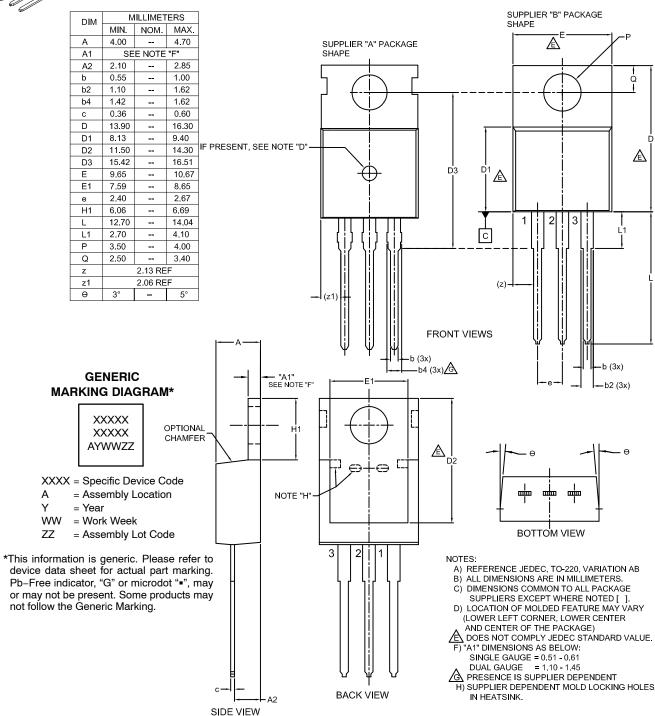
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