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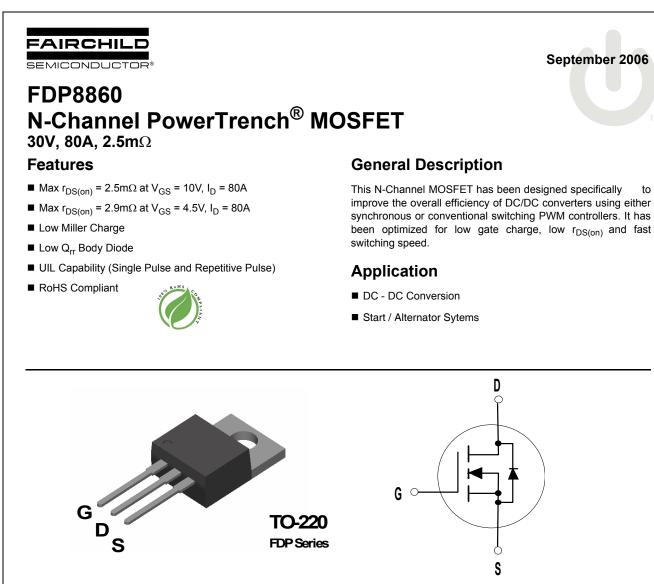


ON Semiconductor®

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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

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MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter			Ratings	Units
V _{DS}	Drain to Source Voltage			30	V
V _{GS}	Gate to Source Voltage			±20	V
	Drain Current -Continuous (Package limited)	T _C = 25°C		80	
I _D	-Continuous (Silicon limited)	T _C = 25°C		219	Α
	-Pulsed		(Note 1)	556	
E _{AS}	Single Pulse Avalanche Energy		(Note 2)	673	mJ
PD	Power Dissipation			254	W
T _J , T _{STG}	Operating and Storage Temperature			-55 to +175	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case TO220	0.59	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient TO220	62	C/VV

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP8860	FDP8860	TO220AB	Tube	N/A	50 units

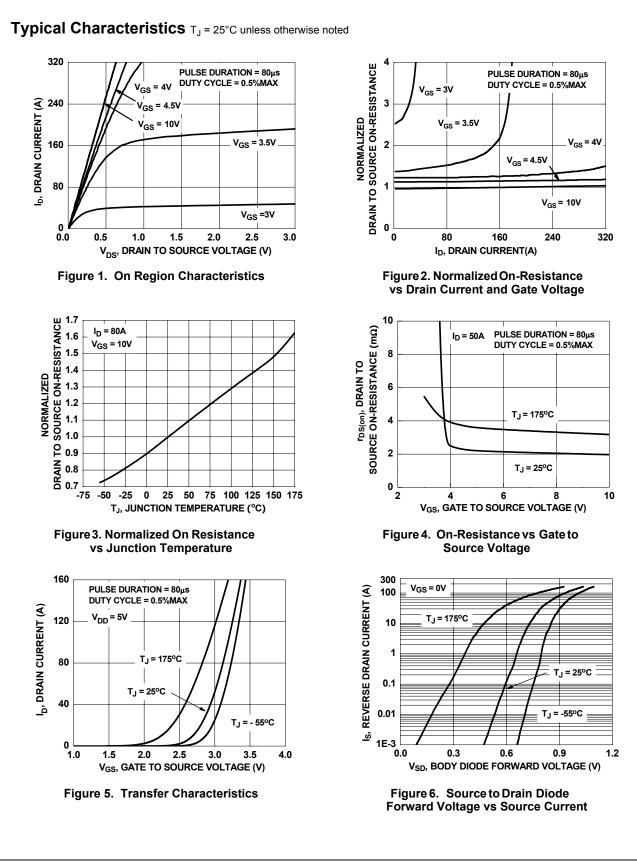
to

FDP8860
N-Channel
PowerTrench [®]
MOSFET

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units	
Off Chara	acteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = 1mA, V _{GS} = 0V	30			V	
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I_D = 1mA, referenced to 25°C		22		mV/°C	
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24V,$ $V_{GS} = 0V$ $T_{J} = 150^{\circ}C$			1 250	μA	
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±20V			±100	nA	
	acteristics			- I	+	ł.	
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	1	1.6	2.5	V	
$\frac{\Delta V_{GS(th)}}{\Delta T_{.l}}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250 \mu A$, referenced to 25°C		-9.6		mV/°C	
		V _{GS} = 10V, I _D = 80A		1.9	2.5		
r	Drain to Source On Registence	V _{GS} = 5V, I _D = 80A		2.0	2.8		
r _{DS(on)} Drain to S	Drain to Source On Resistance	V _{GS} = 4.5V, I _D = 80A		2.1	2.9	mΩ	
		V _{GS} = 10V, I _D = 80A, T _J = 150°C		2.9 3.8			
9fs	Forward Transconductance	V _{DS} = 10V, I _D = 80A		3.4		S	
Dynamic	Characteristics						
C _{iss}	Input Capacitance			9200	12240	pF	
C _{oss}	Output Capacitance	──V _{DS} = 15V, V _{GS} = 0V, f = 1MHz		1700	2260	pF	
C _{rss}	Reverse Transfer Capacitance			1060	1590	pF	
R _g	Gate Resistance	f = 1MHz		1.7		Ω	
Switching	g Characteristics						
t _{d(on)}	Turn-On Delay Time			35	56	ns	
t _r	Rise Time	$V_{DD} = 15V, I_D = 80A$		135	216	ns	
t _{d(off)}	Turn-Off Delay Time	$-V_{GS}$ = 5V, R_{GEN} = 3 Ω		64	103	ns	
t _f	Fall Time			59	95	ns	
Q _{g(TOT)}	Total Gate Charge at 10V	$V_{GS} = 0V$ to 10V		158	222	nC	
Q _{g(5)}	Total Gate Charge at 5V	$\frac{V_{GS} = 0V \text{ to } 10V}{V_{GS} = 0V \text{ to } 5V} V_{DD} = 15V I_D = 80A$		81	114	nC	
Q _{gs}	Gate to Source Gate Charge	I _D = 80A		27		nC	
Q _{gd}	Gate to Drain "Miller" Charge			33		nC	
Drain-So	urce Diode Characteristics						
		V _{GS} = 0V, I _S = 80A		0.88	1.25		
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0V, I _S = 40A		0.81	1.2	V	
t _{rr}	Reverse Recovery Time	I _F = 80A, di/dt = 100A/μs		60	90	ns	

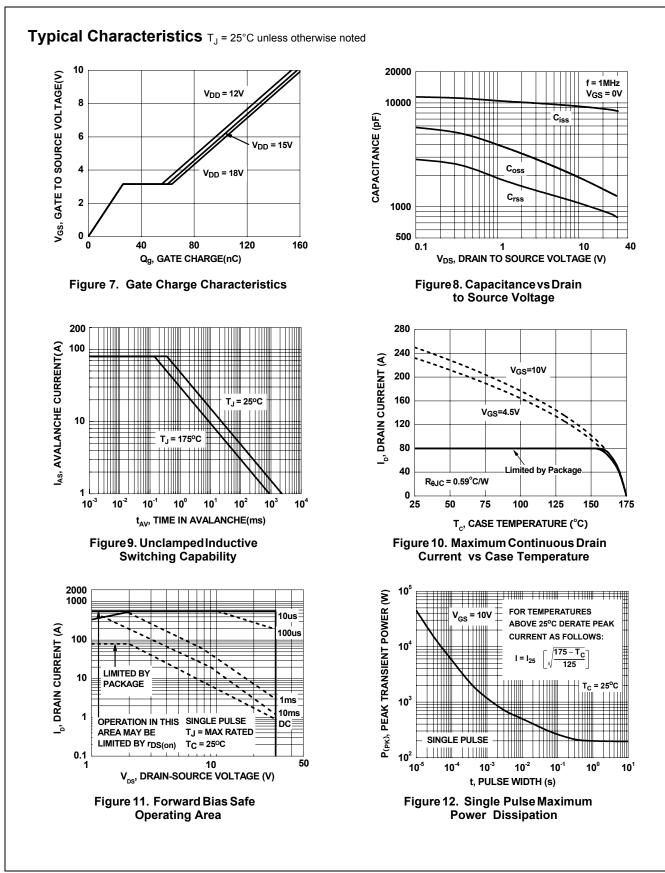
 Q_{rr}

Notes: 1: Pulse Test: Pulse Width < 80μ s, Duty cycle < 0.5%. **2:** Starting T_J =25°C, L= 0.3mH, I_{AS} = 67A, V_{DD} = 27V, V_{GS} = 10V.



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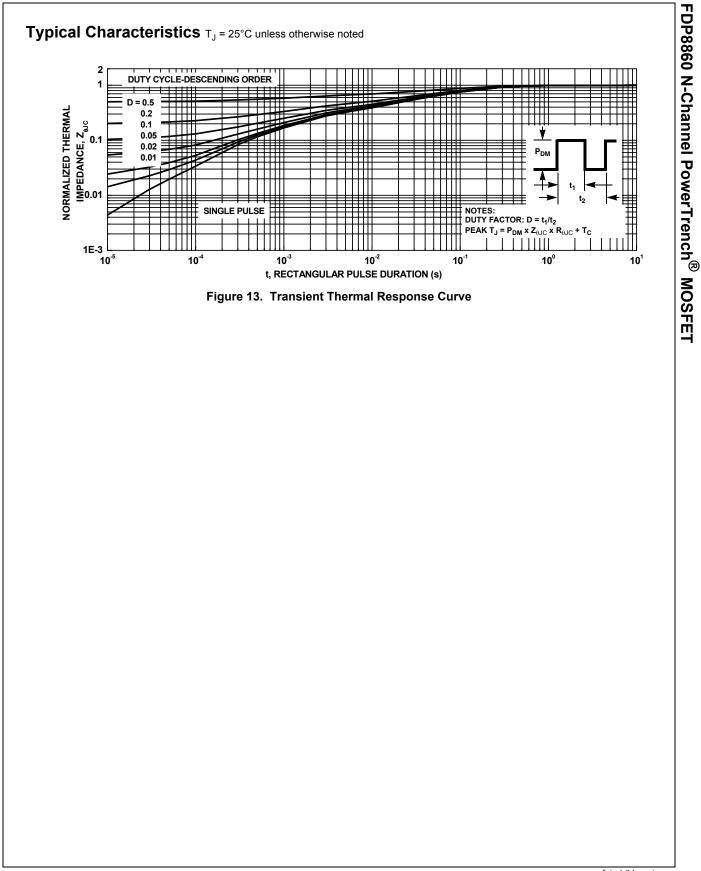




FDP8860 Rev.B

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