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October 2013

FDP3651U

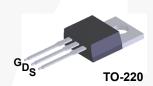
N-Channel PowerTrench[®] MOSFET 100 V, 80 A, 18 m Ω

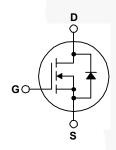
Features

- $R_{DS(on)}$ = 15 $m\Omega$ (Typ.) @ V_{GS} = 10 V, I_D = 80 A
- High Performance Trench Technology for Extremely Low $R_{\mbox{\scriptsize DS(on)}}$
- · Low Miller Charge
- UIS Capability (Single Pulse and Repetitive Pulse)

Applications

- · Consumer Appliances
- · Synchronous Rectification
- · Battery Protection Circuit
- · Motor drives and Uninterruptible Power Supplies
- · Micro Solar Inverter





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter	FDP3651U	Unit
V_{DSS}	Drain to Source Voltage	100	V
V_{GSS}	Gate to Source Voltage	±20	V
	Drain Current - Continuous	80	Α
ID	- Pulsed (Note 1)	320	A
P_{D}	Power Dissipation	255	W
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	266	mJ
T _J , T _{STG}	Operating and Storage Temperature	-55 to 175	°C
T _L	Maximum lead temperature soldering purposes, 1/8" from case for 5 seconds	300	°C

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance , Junction to Ambient, Max.	62	°C/W
$R_{\theta JC}$	Thermal Resistance , Junction to Case, Max.	0.59	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
FDP3651U	FDP3651U	Tube	N/A	50 units

Electrical Characteristics $T_J = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit	
Off Chara	Off Characteristics						
BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A$, $V_{GS} = 0 V$	100	-	-	V	
1	Zero Gate Voltage Drain Current	V _{DS} = 80V	-	-	1	μΑ	
IDSS	Zero Gate Voltage Drain Current	$V_{GS} = 0V$ $T_C = 150$ °C	-	-	250	μΑ	
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±20V	-	-	±100	nA	

On Characteristics

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = -250 \mu A$	3.5	4.5	5.5	V
		$V_{GS} = 10V$, $I_{D} = 80A$	-	15	18	
r _{DS(on)}	Drain to Source On Resistance	$V_{GS} = 10V$, $I_{D} = 40A$	-	13	15	mΩ
		V _{GS} =10V, I _D =40A,T _J =175°C	-	32	37	

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05)/// 0)/	-	4152	5522	pF
C _{oss}	Output Capacitance	V _{DS} = 25V,V _{GS} = 0V f=1MHz	-	485	728	pF
C _{rss}	Reverse Transfer Capacitance	— I – IIVIMZ		89	118	pF
$Q_{g(TOT)}$		V _{GS} = 0V to 10V	1	49	69	nC
$Q_{g(TH)}$	Threshold Gate Charge	$V_{GS} = 0V \text{ to } 2V$ $V_{DD} = 50V$ $I_{D} = 80A$	-	7	9.8	nC
Q_{gs}	Gate to Source Gate Charge	ID - 80A	-	23		nC
Q_{gd}	Gate to Drain Charge		-	16	-	nC

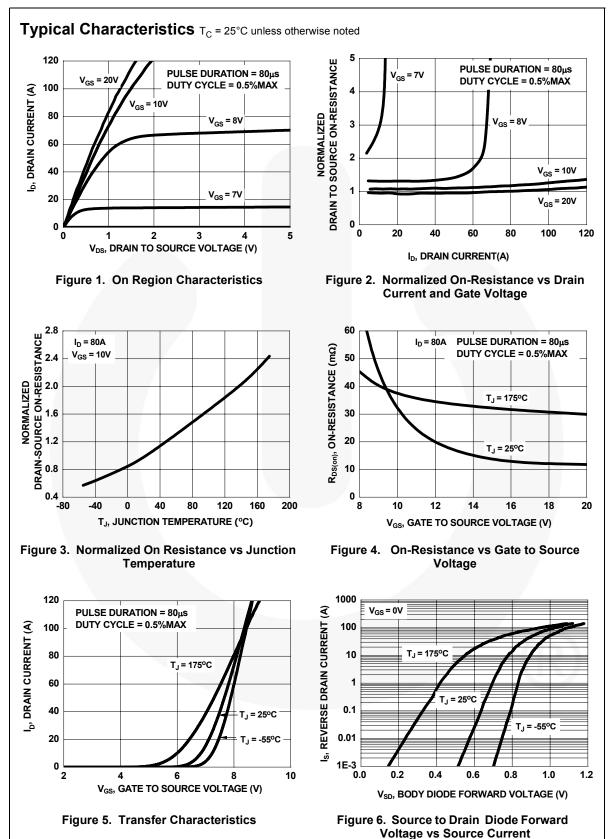
Resistive Switching Characteristics

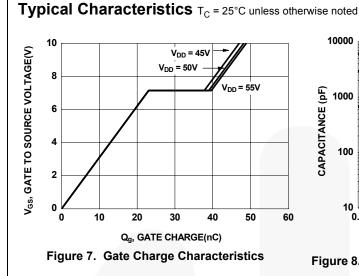
t _(on)	Turn-On Time		-	-	64	ns
t _{d(on)}	Turn-On Delay Time		-	15	27	ns
t _r	Rise Time	$V_{DD} = 50V, I_{D} = 80A$ $V_{GS} = 10V, R_{GS} = 5.0\Omega$	-	16	29	ns
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10V, K _{GS} = 5.012	-	32	52	ns
t _f	Fall Time		-	14	26	ns
t _(off)	Turn-Off Time		- /	-	78	ns

Drain-Source Diode Characteristics

V	Source to Drain Diode Forward Voltage I _{SD} = 80	I _{SD} = 80A	-	0.99	1.25	V
	I _{SD} = 40A	-	0.88	1.0	V	
t _{rr}	Reverse Recovery Time	I _s = 40 A, di/dt = 100A/μs	-	70	105	ns
Q _{rr}	Reverse Recovery Charge	1 _s = 40 A, αναί = 100Ανμs	-	202	303	nC

- Notes: 1. Repetitive Rating : Pulse width limited by maximum junction temperature 2. L=0.13mH, I_{AS} = 64A, V_{DD} =50V, R_{G} =25 Ω , Starting T_{J} =25°C





10000

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Coss

1000

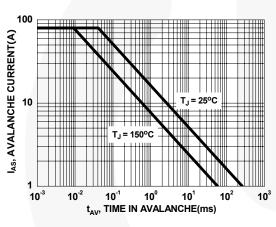
F = 1MHz

V_{GS} = 0V

10

V_{DS}, DRAIN TO SOURCE VOLTAGE (V)

Figure 8. Capacitance vs Drain to Source Voltage



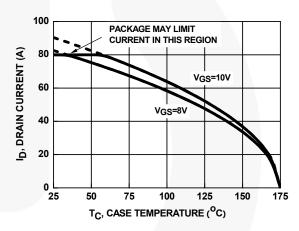
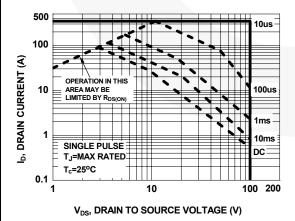


Figure 9. Unclamped Inductive Switching Capability

Figure 10. Maximum Continuous Drain Current vs
Ambient Temperature



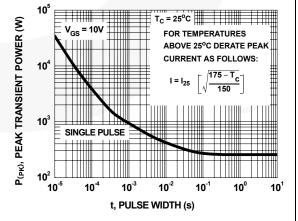


Figure 11. Forward Bias Safe Operating Area

Figure 12. Single Pulse Maximum Power Dissipation



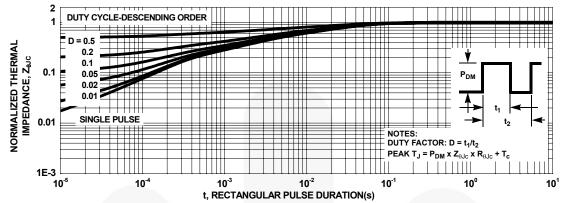


Figure 13. Transient Thermal Response Curve

Mechanical Dimensions

TO-220 3L

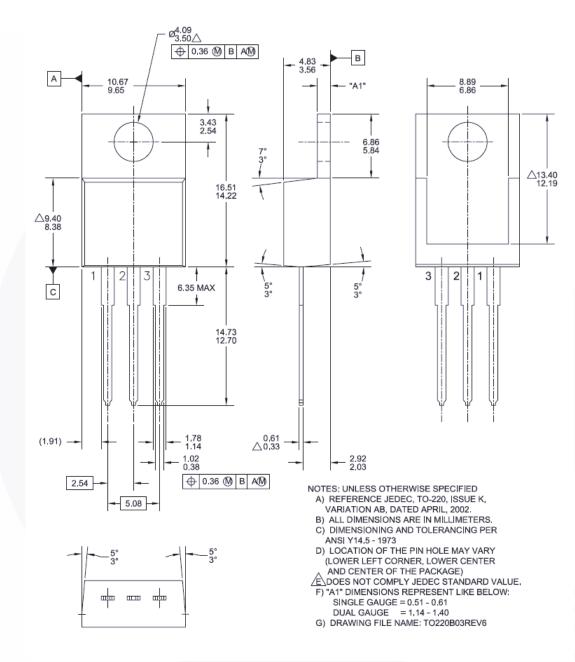


Figure 14. TO-220, Molded, 3Lead, Jedec Variation AB

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Dimension in Millimeters





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