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FDMS8570SDC

July 2013

N-Channel PowerTrench® SyncFETTM

25 V, 60 A, 2.8 m Ω

Features

- Dual CoolTM PQFN package
- Max $r_{DS(on)}$ = 2.8 m Ω at V_{GS} = 10 V, I_D = 28 A
- Max $r_{DS(on)} = 3.3 \text{ m}\Omega$ at $V_{GS} = 4.5 \text{ V}$, $I_D = 25 \text{ A}$
- High performance technology for extremely low r_{DS(on)}
- SyncFETTM Schottky Body Diode
- RoHS Compliant



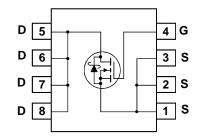
General Description

This N-Channel SyncFETTM is produced using Fairchild Semiconductor's advanced PowerTrench® process. Advancements in both silicon and package technologies have been combined to offer the lowest r_{DS(on)} while maintaining excellent switching performance by extremely low Junction-to-Ambient thermal resistance. This device has the added benefit of an efficient monolithic Schottky body diode.

Applications

- Synchronous Rectifier for DC/DC Converters
- Telecom Secondary Side Rectification
- High End Server/Workstation Vcore Low Side





MOSFET Maximum Ratings $T_A = 25$ °C unless otherwise noted

Symbol	Parameter			Ratings	Units
V_{DS}	Drain to Source Voltage			25	V
V_{GS}	Gate to Source Voltage			12	V
	Drain Current -Continuous (Package limited)	T _C = 25 °C		60	
I_D	-Continuous	T _A = 25 °C	(Note 1a)	28	Α
	-Pulsed			100	
E _{AS}	Single Pulse Avalanche Energy		(Note 3)	45	mJ
D	Power Dissipation	T _C = 25 °C		59	W
P_{D}	Power Dissipation	T _A = 25 °C	(Note 1a)	3.3	VV
T _J , T _{STG}	Operating and Storage Junction Temperature Ra	ange		-55 to +150	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Top Source)	4.4	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Bottom Drain)	2.1	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	38	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	81	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1i)	16	*C/VV
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1j)	23	
R _{e.IA}	Thermal Resistance, Junction to Ambient	(Note 1k)	11	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
10DC	FDMS8570SDC	Power 56	13"	12 mm	3000 units

Electrical Characteristics $T_J = 25$ °C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	acteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$	25			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25 °C		23		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 20 V, V _{GS} = 0 V			500	μΑ
I _{GSS}	Gate to Source Leakage Current	V _{GS} = +12 V/-8 V, V _{DS} = 0 V			±100	nA

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1 \text{ mA}$	1.1	1.5	2.2	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25 °C		-3		mV/°C
		$V_{GS} = 10 \text{ V}, I_D = 28 \text{ A}$		2.1	2.8	
r _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 4.5 \text{ V}, I_D = 25 \text{ A}$		2.4	3.3	mΩ
		$V_{GS} = 10 \text{ V}, I_D = 28 \text{ A}, T_J = 125 \text{ °C}$		2.9	3.9	1
g _{FS}	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_{D} = 28 \text{ A}$		215		S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 42 V V 0 V	2825	pF
Coss	Output Capacitance	V _{DS} = 13 V, V _{GS} = 0 V, f = 1 MHz	662	pF
C _{rss}	Reverse Transfer Capacitance	- 1 1011 12	94	pF
R_g	Gate Resistance		0.8	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		11	ns
t _r	Rise Time	V _{DD} = 13 V, I _D = 28 A,	4	ns
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10 V, R_{GEN} = 6 Ω	33	ns
t _f	Fall Time		3	ns
Q_{q}	Total Gate Charge	V _{GS} = 0 V to 10 V	42	nC
Q_q	Total Gate Charge	$V_{GS} = 0 \text{ V to } 4.5 \text{ V}$ $V_{DD} = 13 \text{ V}$	22	nC
Q_{gs}	Gate to Source Gate Charge	I _D = 28 A	6.4	nC
Q_{gd}	Gate to Drain "Miller" Charge		4.4	nC

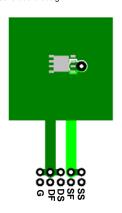
Drain-Source Diode Characteristics

V		$V_{GS} = 0 \text{ V}, I_{S} = 2 \text{ A}$ (Note 2)	0.6	0.8	V
V _{SD}	Source to Drain blode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = 28 \text{ A}$ (Note 2)	0.8	1.2	V
t _{rr}	Reverse Recovery Time	-I _F = 28 A, di/dt = 300 A/μs	22		ns
Q _{rr}	Reverse Recovery Charge	I _F = 28 A, α//αι = 300 A//μS			nC

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Top Source)	4.4	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Bottom Drain)	2.1	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	38	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	81	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1c)	27	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1d)	34	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1e)	16	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1f)	19	C/VV
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1g)	26	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1h)	61	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1i)	16	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1j)	23	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1k)	11	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1I)	13	

1. R_{0,1A} is determined with the device mounted on a FR-4 board using a specified pad of 2 oz copper as shown below. R_{0,1C} is guaranteed by design while R_{0,CA} is determined by the user's board design.



a. 38 °C/W when mounted on a 1 in² pad of 2 oz copper



b. 81 °C/W when mounted on a minimum pad of 2 oz copper

- c. Still air, 20.9x10.4x12.7mm Aluminum Heat Sink, 1 in² pad of 2 oz copper
- d. Still air, 20.9x10.4x12.7mm Aluminum Heat Sink, minimum pad of 2 oz copper
- e. Still air, 45.2x41.4x11.7mm Aavid Thermalloy Part # 10-L41B-11 Heat Sink, 1 in² pad of 2 oz copper
- f. Still air, 45.2x41.4x11.7mm Aavid Thermalloy Part # 10-L41B-11 Heat Sink, minimum pad of 2 oz copper
- g. 200FPM Airflow, No Heat Sink,1 in² pad of 2 oz copper
- h. 200FPM Airflow, No Heat Sink, minimum pad of 2 oz copper
- i. 200FPM Airflow, 20.9x10.4x12.7mm Aluminum Heat Sink, 1 in² pad of 2 oz copper
- j. 200FPM Airflow, 20.9x10.4x12.7mm Aluminum Heat Sink, minimum pad of 2 oz copper
- k. 200FPM Airflow, 45.2x41.4x11.7mm Aavid Thermalloy Part # 10-L41B-11 Heat Sink, 1 in² pad of 2 oz copper
- I. 200FPM Airflow, 45.2x41.4x11.7mm Aavid Thermalloy Part # 10-L41B-11 Heat Sink, minimum pad of 2 oz copper
- 2. Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%. 3. E_{AS} of 45 mJ is based on starting T_J = 25 °C, L = 0.4 mH, I_{AS} = 15 A, V_{DD} = 23 V, V_{GS} = 10 V. 100% test at L = 0.1 mH, I_{AS} = 23.8 A.

Typical Characteristics $T_J = 25$ °C unless otherwise noted

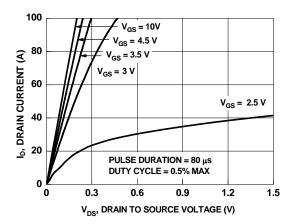


Figure 1. On Region Characteristics

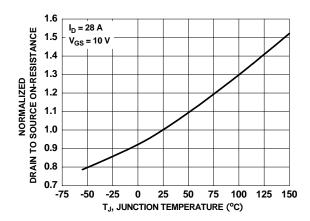


Figure 3. Normalized On Resistance vs Junction Temperature

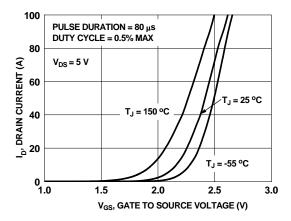


Figure 5. Transfer Characteristics

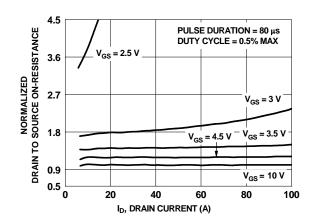


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

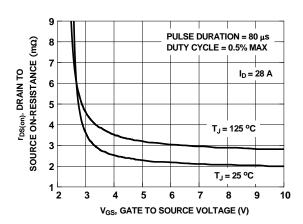


Figure 4. On-Resistance vs Gate to Source Voltage

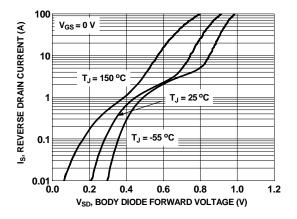


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

Typical Characteristics T_J = 25 °C unless otherwise noted

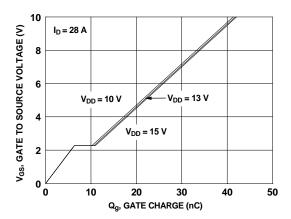


Figure 7. Gate Charge Characteristics

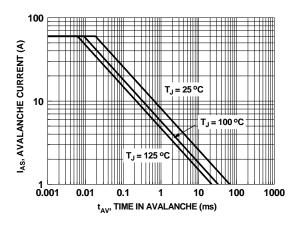


Figure 9. Unclamped Inductive Switching Capability

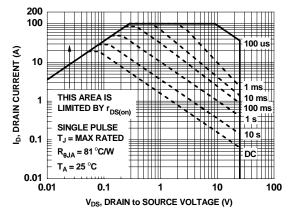


Figure 11. Forward Bias Safe Operating Area

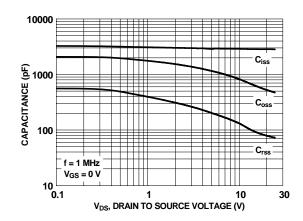


Figure 8. Capacitance vs Drain to Source Voltage

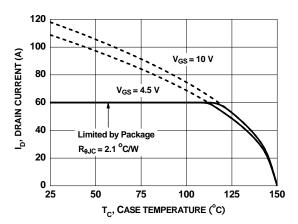


Figure 10. Maximum Continuous Drain Current vs Ambient Temperature

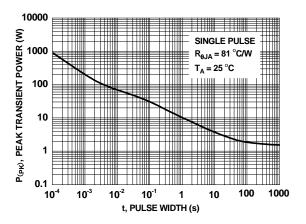


Figure 12. Single Pulse Maximum Power Dissipation

Typical Characteristics T_J = 25 °C unless otherwise noted

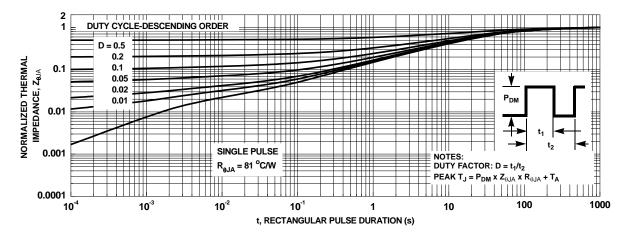


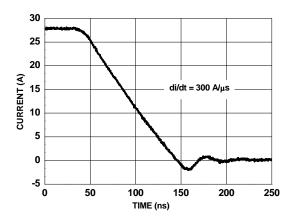
Figure 13. Junction-to-Ambient Transient Thermal Response Curve

Typical Characteristics (continued)

SyncFETTM Schottky body diode Characteristics

Fairchild's SyncFETTM process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 14 shows the reverse recovery characteristic of the FDMS8570SDC.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.



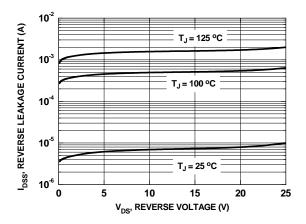


Figure 14. FDMS8570SDC SyncFETTM body diode reverse recovery characteristic

Figure 15. SyncFETTM body diode reverse leakage versus drain-source voltage

Dimensional Outline and Pad Layout 5.10 5.10 A (2.60)3.91 -1.27(0.90) -PKG 0.77 В KEEP-OUT 4.52 3.75 PKG Q-AREA 6.25 5.90 (3.30)6.61 1.27 (0.82)PIN #1 **TOP VIEW** IDENT MAY SEE APPEAR AS 1.27 0.61 **DETAIL A OPTIONAL** 3.81-LAND PATTERN RECOMMENDATION FRONT VIEW OPTIONAL DRAFT ANGLE 3.81 MAY APPEAR ON FOUR 1.27 SIDES OF THE PACKAGE 0.50 0.40 (8X) (0.34) -0.44 ◆ 0.10M C A B CHAMFER CORNER 5 65 (3.44)AS PIN #1 **IDENT MAY** 4.01±0.30 APPEAR AS **OPTIONAL** SIDE VIEW 0.65 3.86 NOTES: UNLESS OTHERWISE SPECIFIED 0.45 A) PACKAGE STANDARD REFERENCE: JEDEC MO-240, ISSUE A, VAR. AA, **BOTTOM VIEW** DATED OCTOBER 2002. B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONS DO NOT INCLUDE BURRS -0.1 MAX OR MOLD FLASH. MOLD FLASH OR 0.10 C BURRS DOES NOT EXCEED 0.10MM. D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994. E) IT IS RECOMMENDED TO HAVE NO TRACES OR VIAS WITHIN THE KEEP OUT AREA. ○ 0.08 C 0.30₀ 0.05 C 0.00 1.05 SEATING 0.95 **DETAIL A** PLANE





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