

MOSFET - N-Channel, SUPERFET[®] II

800 V, 2.2 A, 4.3 Ω

FCPF4300N80Z

Description

SUPERFET II MOSFET is **onsemi**'s brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SUPERFET II MOSFET is very suitable for the switching power applications such as Audio, Laptop adapter, Lighting, ATX power and industrial power applications.

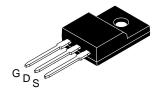
Features

- $R_{DS(on)} = 3.4 \text{ m}\Omega \text{ (Typ.)}$
- Ultra Low Gate Charge (Typ. $Q_g = 6.8 \text{ nC}$)
- Low E_{oss} (Typ. 0.8 μJ @ 400 V)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = 36 pF)
- 100% Avalanche Tested
- ESD Improved Capability
- RoHS Compliant

Applications

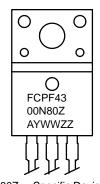
- AC-DC Power Supply
- LED Lighting

V _{DSS}	R _{DS(on)} MAX	I _D MAX
800 V	4.3 Ω @ 10 V	2.2 A



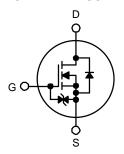
TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT

MARKING DIAGRAM



FCPF4300N80Z = Specific Device Code
A = Assembly Location
YWW = Date Code (Year & Week)
ZZ = Assembly Lot

N-CHANNEL MOSFET



ORDERING INFORMATION

Part Number	Package	Shipping	
FCPF4300N80Z	TO-220F	1000 Units / Tube	

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ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$, unless otherwise noted)

Symbol	Parameter		FCPF4300N80Z	Unit	
V _{DSS}	Drain to Source Voltage		800	V	
V _{GSS}	Gate to Source Voltage	- DC	±20	V	
		- AC (f > 1 Hz)	±30		
I _D	Drain Current	– Continuous (T _C = 25°C)	2.2*	Α	
		- Continuous (T _C = 100°C)	1.4*		
I _{DM}	Drain Current	- Pulsed (Note 1)	3.2*	Α	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		8.2	mJ	
I _{AR}	Avalanche Current (Note 1)		0.32	Α	
E _{AR}	Repetitive Avalanche Energy (Note 1)		0.19	mJ	
dv/dt	MOSFET dv/dt		100	V/ns	
	Peak Diode Recovery dv/dt (Note 3)	20	1		
P_{D}	Power Dissipation	(T _C = 25°C)	19.2	W	
		– Derate Above 25°C	0.15	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
*Drain current limited by maximum junction temperature, with heatsink.

THERMAL CHARACTERISTICS

Symbol	Parameter	FCPF4300N80Z	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	6.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

^{1.} Repetitive rating: pulse width limited by maximum junction temperature, with reasonix.
2. $I_{AS} = 0.32 \text{ A}$, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$.
3. $I_{SD} \le 2.2 \text{ A}$, di/dt $\le 200 \text{ A/}\mu\text{s}$, $V_{DD} \le \text{BV}_{DSS}$, starting $T_J = 25^{\circ}\text{C}$.

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHAR	ACTERISTICS		•	•		•
BV _{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 25^{\circ}\text{C}$	800	_	-	V
$\Delta BV_{DSS}/ \Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 1 mA, Referenced to 25°C	-	0.85	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 800 V, V _{GS} = 0 V	_	-	25	μΑ
		V _{DS} = 640 V, V _{GS} = 0 V, T _C = 125°C	_	_	250	1
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
ON CHARA	CTERISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 0.16 \text{ mA}$	2.5	_	4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 0.8 A	_	3.4	4.3	Ω
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 0.8 A	_	0.52	_	S
DYNAMIC (CHARACTERISTICS					
C _{iss}	Input Capacitance	V _{DS} = 100 V, V _{GS} = 0 V, f = 1 MHz	_	267	355	pF
C _{oss}	Output Capacitance	1	_	12	16	pF
C _{rss}	Reverse Transfer Capacitance	1	_	0.78	_	pF
C _{oss}	Output Capacitance	V _{DS} = 480 V, V _{GS} = 0 V, f = 1 MHz	_	6.2	_	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 480 V, V _{GS} = 0 V	-	36	-	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 640 V, I _D = 1.6 A, V _{GS} = 10 V	_	6.8	8.8	nC
Q _{gs}	Gate to Source Gate Charge	(Note 4)	_	1.38	_	nC
Q _{gd}	Gate to Drain "Miller" Charge]	_	3.0	_	nC
ESR	Equivalent Series Resistance	f = 1 MHz	_	2.9	_	Ω
SWITCHING	CHARACTERISTICS		•		•	•
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, I_D = 1.6 \text{ A}, V_{GS} = 10 \text{ V},$	_	10	30	ns
t _r	Turn-On Rise Time	$R_g = 4.7 \Omega \text{ (Note 4)}$	_	6.5	23	ns
t _{d(off)}	Turn-Off Delay Time	1	_	21	52	ns
t _f	Turn-Off Fall Time	1	_	16	42	ns
DRAIN-SO	JRCE DIODE CHARACTERISTICS	•	•	•	•	•
I _S	Maximum Continuous Drain to Source Diode Forward Current		_	-	2.2	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		_	-	3.2	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 1.6 A	_	_	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 1.6 A,	_	209	-	ns
Q _{rr}	Reverse Recovery Charge	dl _F /dt = 100 A/μs		1.2		μС

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

ID, Drain Current (A)

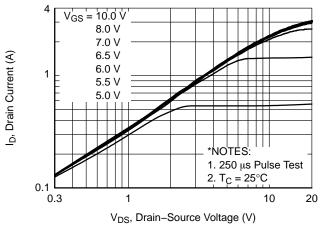


Figure 1. On-Region Characteristics

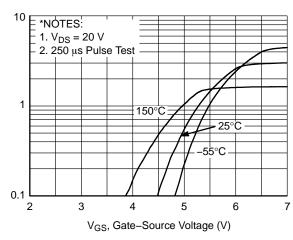


Figure 2. Transfer Characteristics

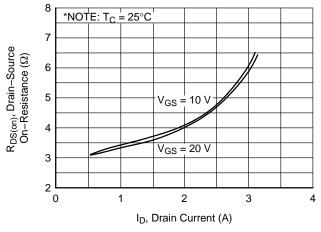


Figure 3. On–Resistance Variation vs. Drain Current and Gate Voltage

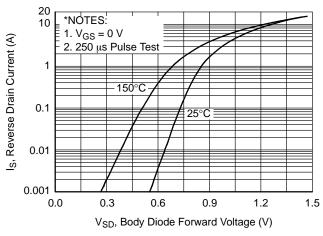


Figure 4. Body Diode Forward Voltage Variation vs.
Source Current and Temperature

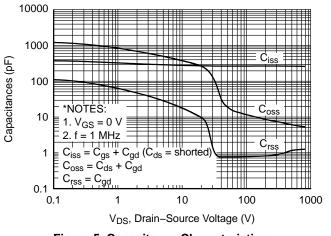


Figure 5. Capacitance Characteristics

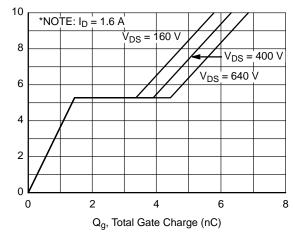


Figure 6. Gate Charge Characteristics

V_{GS}, Gate-Source Voltage (V)

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

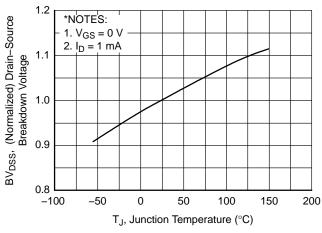


Figure 7. Breakdown Voltage Variation vs. Temperature

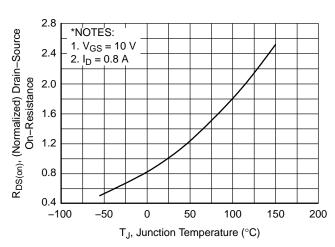


Figure 8. On-Resistance Variation vs. Temperature

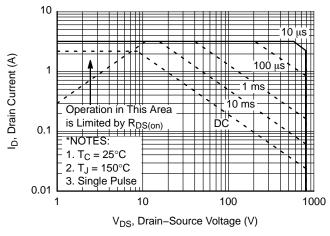


Figure 9. Maximum Safe Operating Area

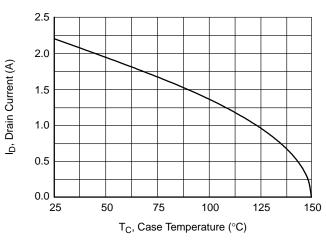


Figure 10. Maximum Drain Current vs.

Case Temperature

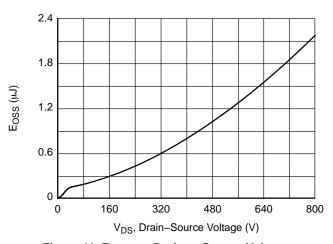


Figure 11. E_{OSS} vs. Drain to Source Voltage

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

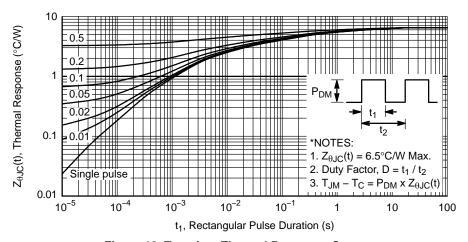


Figure 12. Transient Thermal Response Curve

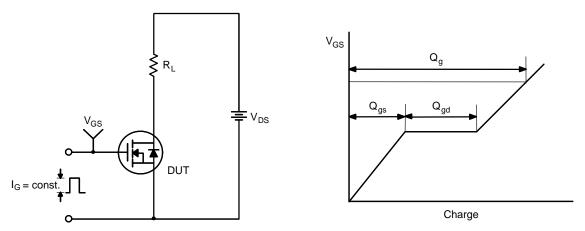


Figure 13. Gate Charge Test Circuit & Waveform

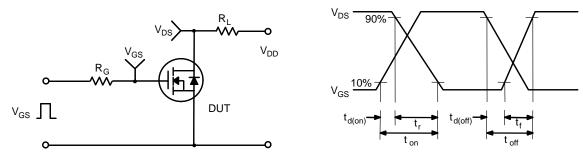


Figure 14. Resistive Switching Test Circuit & Waveforms

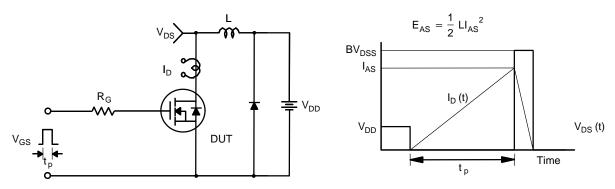
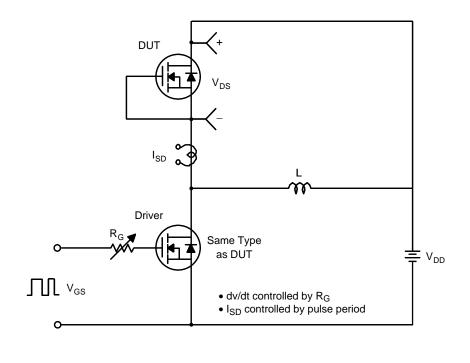


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms



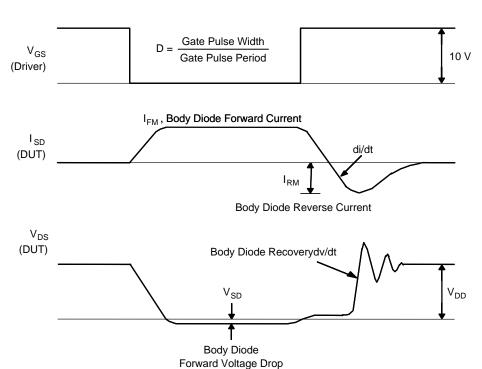
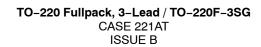


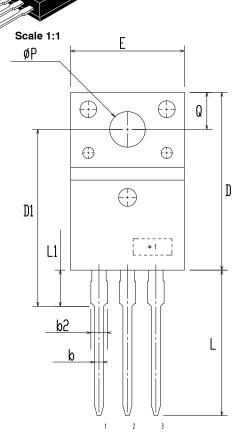
Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

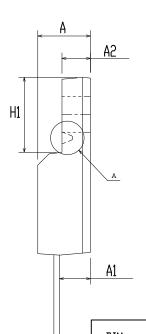
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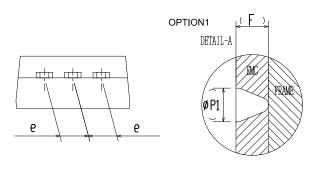




DATE 19 JAN 2021







DIM	HILLIHITENS			
ויונע	MIN	NDM	MAX	
Α	4.50	4.70	4.90	
A1	2.56	2.76	2.96	
A2	2.34	2.54	2.74	
b	0.70	0.80	0.90	
b2	~	2	1.47	
С	0.45	0.50	0.60	
D	15.67	15.87	16.07	
D1	15.60	15.80	16.00	
E	9.96	10.16	10.36	
е	2.34	2.54	2.74	
F	~	0.84	~	
H1	6.48	6.68	6.88	
L	12.78	12.98	13.18	
L1	3.03	3.23	3.43	
øΡ	2.98	3.18	3.38	
ø P1	~	1.00	~	
Q	3.20	3.30	3.40	

MILL IMITERS

NOTES:

- A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCSIONS.

C

C. OPTION 1 - WITH SUPPORT PIN HOLE OPTION 2 - NO SUPPORT PIN HOLE

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