

1.0 A Ultra Fast Recovery Rectifier

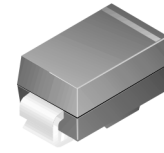
ES1A-ES1D

Features

- For Surface Mount Applications
- Glass Passivated Junction
- Low Profile Package
- Easy Pick and Place
- Built-in Strain Relief
- Superfast Recovery Times for High Efficiency
- This Device is Pb-Free and is RoHS Compliant

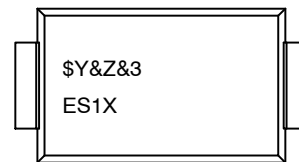
Applications

- This Product is General Usage and Suitable for Many Different Applications



**SMA
CASE 403AE**
(COLOR BAND DENOTES CATHODE)

MARKING DIAGRAM



\$Y	= ON Semiconductor Logo
&Z	= Assembly Plant Code
&3	= Data Code (Year & Week)
ES1X	= Specific Device Code
X	= A/B/C/D

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ES1A–ES1D

ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$ Unless Otherwise Noted

Symbol	Parameter	Value				Units
		1A	1B	1C	1D	
V_{RRM}	Maximum Repetitive Reverse Voltage	50	100	150	200	V
$I_{F(AV)}$	Average Rectified Forward Current, @ $T_A = 120^\circ\text{C}$	1.0				A
I_{FSM}	Non-repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine-Wave	30				A
T_{stg}	Storage Temperature Range	-50 to +150				$^\circ\text{C}$
T_{Jm}	Operating Junction Temperature	-50 to +150				$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Units
P_D	Power Dissipation	1.47	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient*	85	$^\circ\text{C}/\text{W}$
$R_{\theta JL}$	Thermal Resistance, Junction to Lead*	35	$^\circ\text{C}/\text{W}$

*Device mounted on FR-4 PCB 0.013 mm.

ELECTRICAL CHARACTERISTICS $T_J = 25^\circ\text{C}$ Unless Otherwise Noted

Symbol	Parameter	Device				Units
		1A	1B	1C	1D	
V_F	Forward Voltage @ 1.0 A	0.92				V
t_{rr}	Reverse Recovery Time $I_F = 0.5\text{ A}$, $I_R = 1.0\text{ A}$, $I_{RR} = 0.25\text{ A}$	15				ns
I_R	Reverse Current @ rated V_R $T_A = 25^\circ\text{C}$ $T_A = 100^\circ\text{C}$	5.0 100				μA
C_T	Total Capacitance $V_R = 4.0\text{ V}$, $f = 1.0\text{ MHz}$	7.0				pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PACKAGE MARKING AND ORDERING INFORMATION TBD

Device Marking	Device	Package	Quantity [†]
ES1A	ES1A	SMA	7500 / Tape & Reel
ES1B	ES1B	SMA	7500 / Tape & Reel
ES1C	ES1C	SMA	7500 / Tape & Reel
ES1D	ES1D	SMA	7500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ES1A-ES1D

TYPICAL CHARACTERISTICS

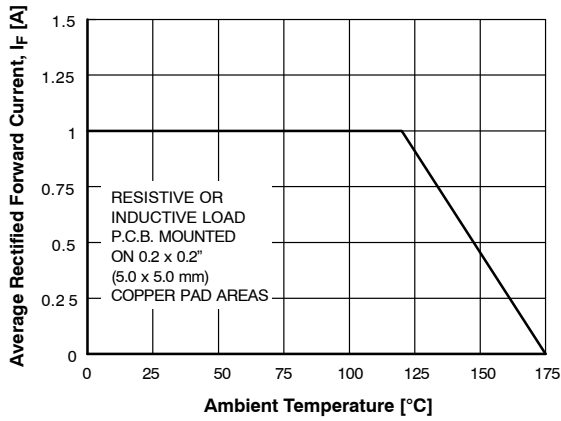


Figure 1. Forward Current Derating Curve

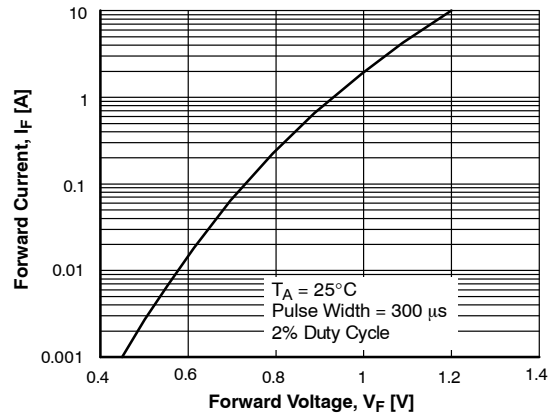


Figure 2. Forward Voltage Characteristics

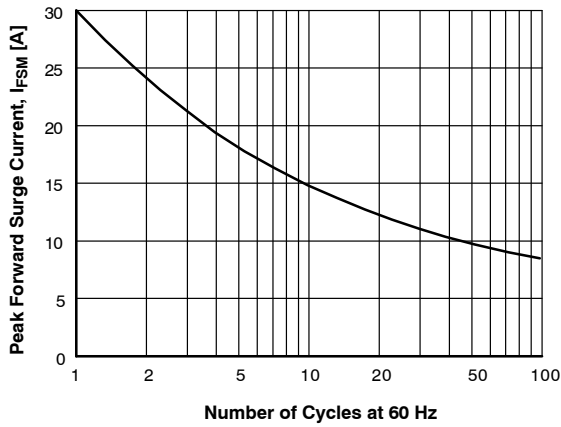


Figure 3. Non-Repetitive Surge Current

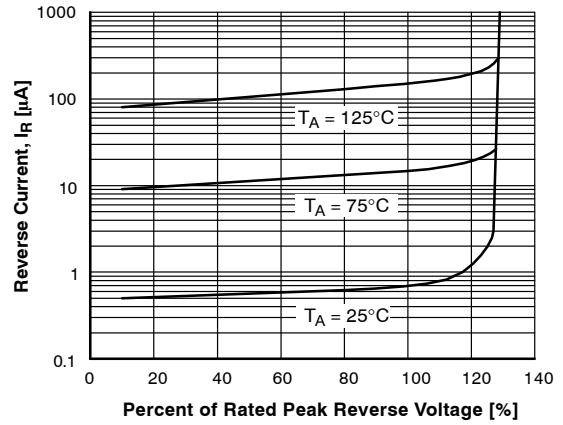


Figure 4. Reverse Current vs Reverse Voltage

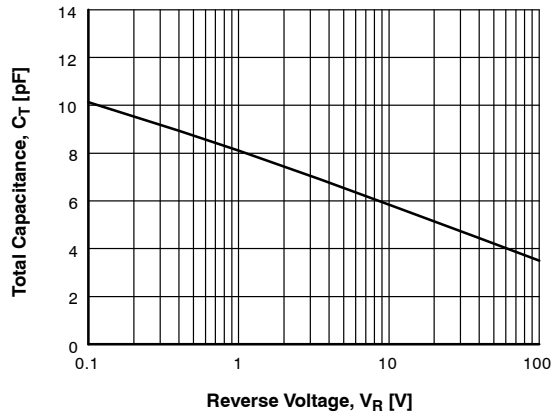
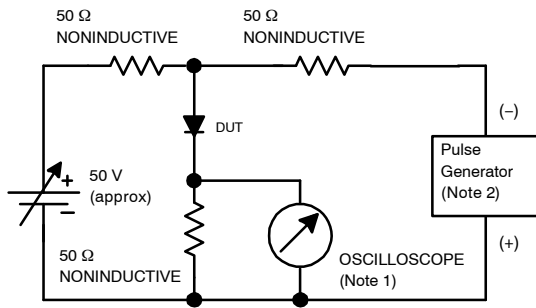


Figure 5. Total Capacitance

ES1A-ES1D

TYPICAL CHARACTERISTICS (continued)



NOTES:

1. Rise time = 7.0 ns max; Input impedance = 1.0 MΩ 22 pf.
2. Rise time = 10 ns max; Source impedance = 50 Ω.

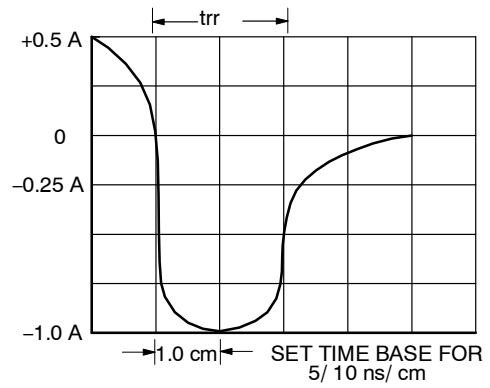
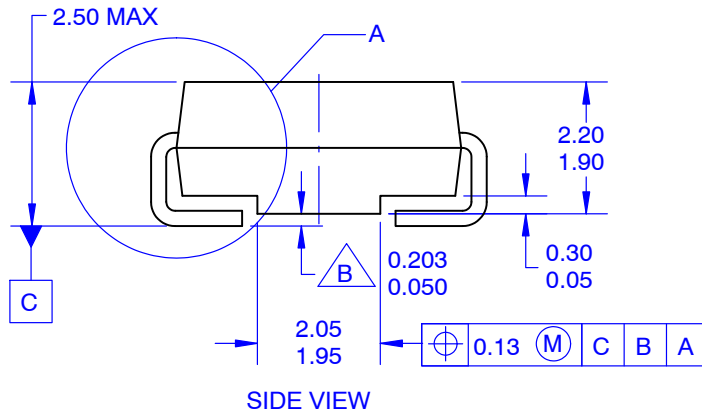
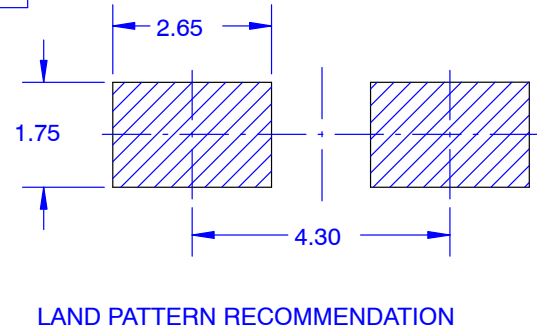
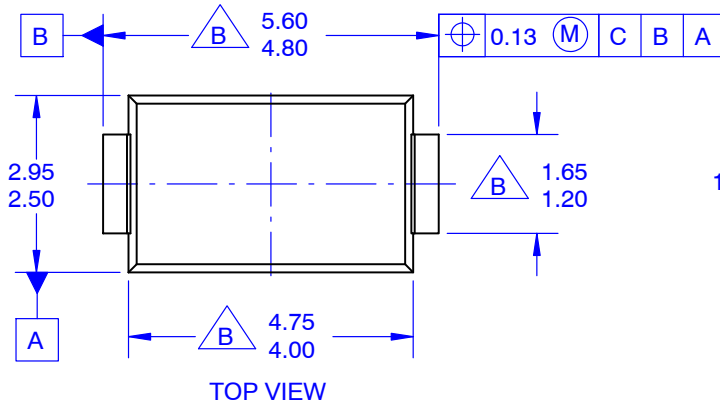


Figure 6. Reverse Recovery Time Characteristic and Test Circuit Diagram

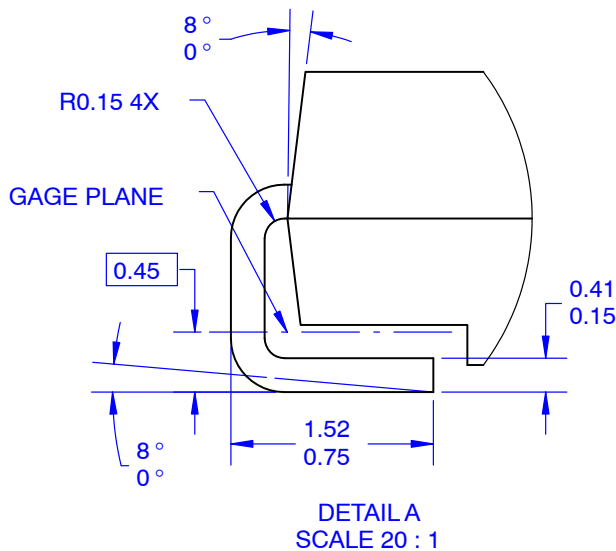
SMA
CASE 403AE
ISSUE O

DATE 31 AUG 2016



NOTES:

- A. EXCEPT WHERE NOTED, CONFORMS TO JEDEC DO214 VARIATION AC.
- B. DOES NOT COMPLY JEDEC STANDARD VALUE.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- E. DIMENSIONS AND TOLERANCE AS PER ASME Y14.5-2009.
- E. LAND PATTERN STD. DIOM5025X231M



DOCUMENT NUMBER: 98AON13440G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION: SMA	PAGE 1 OF 1

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation
onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at www.onsemi.com/support/sales