

# 80SQ045N

Preferred Device

## Axial Lead Rectifier

These devices employ the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlap contact. Ideally suited for use as rectifiers in low-voltage, high-frequency inverters, free wheeling diodes, and polarity protection diodes.

### Features

- High Current Capability
- Low Stored Charge, Majority Carrier Conduction
- Low Power Loss/High Efficiency
- Highly Stable Oxide Passivated Junction
- Guard-Ring for Stress Protection
- Low Forward Voltage
- High Surge Capacity
- These are Pb-Free Devices\*

### Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 1.1 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 220°C Max. for 10 Seconds, 1/16" from Case
- Polarity: Cathode indicated by Polarity Band
- ESD Protection: Human Body Model > 4000 V (Class 3)  
Machine Model > 400 V (Class C)

### MAXIMUM RATINGS

Rating	Symbol	Max	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	$V_{RRM}$ $V_{RWM}$ $V_R$	45	V
Average Rectified Forward Current $T_L = 75^\circ\text{C}$ ( $\text{Psi}_{JL} = 12^\circ\text{C/W}$ , P.C. Board Mounting, Note 2)	$I_O$	8.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	$I_{FSM}$	140	A
Operating and Storage Junction Temperature Range (Reverse Voltage Applied)	$T_J, T_{stg}$	-65 to +125	$^\circ\text{C}$
Voltage Rate of Change (Rated $V_R$ )	dv/dt	10	V/ns

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

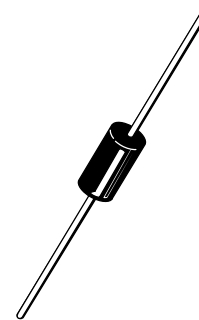
\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



ON Semiconductor®

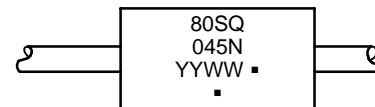
<http://onsemi.com>

## SCHOTTKY BARRIER RECTIFIER 8.0 AMPERES



AXIAL LEAD  
CASE 267-05  
(DO-201AD)  
STYLE 1

### MARKING DIAGRAM



YY = Year  
WW = Work Week  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

### ORDERING INFORMATION

Device	Package	Shipping†
80SQ045N	Axial Lead*	500 Units/Box
80SQ045NG	Axial Lead*	500 Units/Box
80SQ045NRL	Axial Lead*	1500/Tape & Reel
80SQ045NRLG	Axial Lead*	1500/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*This package is inherently Pb-Free.

Preferred devices are recommended choices for future use and best overall value.

# 80SQ045N

## THERMAL CHARACTERISTICS

Characteristic	Symbol	0.9 in x 0.9 in Copper Pad Size	6.75 in x 6.75 in Copper Pad Size	Unit
Thermal Resistance, Junction-to-Lead (See Note 2 – Mounting Data)	$R_{\theta JL}$	13	12	$^{\circ}C/W$
Thermal Resistance, Junction-to-Ambient (See Note 2 – Mounting Data)	$R_{\theta JA}$	50	40	

## ELECTRICAL CHARACTERISTICS ( $T_L = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Max	Unit
Maximum Instantaneous Forward Voltage (Note 1) ( $i_F = 8.0 A$ , $T_L = 25^{\circ}C$ )	$V_F$	0.55	V
Maximum Instantaneous Reverse Current @ Rated dc Voltage (Note 1) $T_L = 25^{\circ}C$ $T_L = 100^{\circ}C$	$i_R$	1.0 50	mA

1. Pulse Test: Pulse Width = 300  $\mu s$ , Duty Cycle = 2.0%.

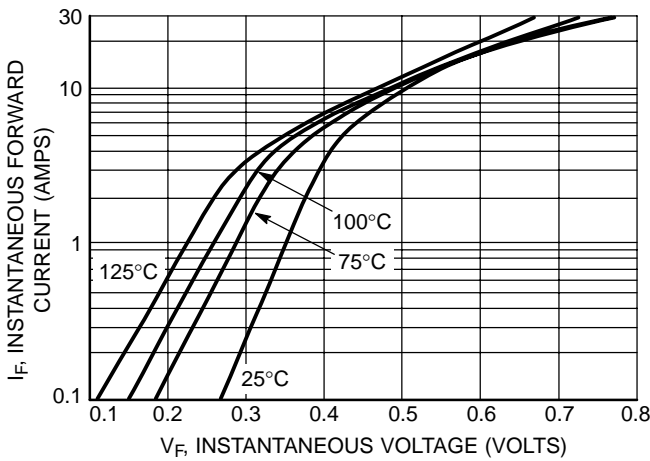


Figure 1. Typical Forward Voltage

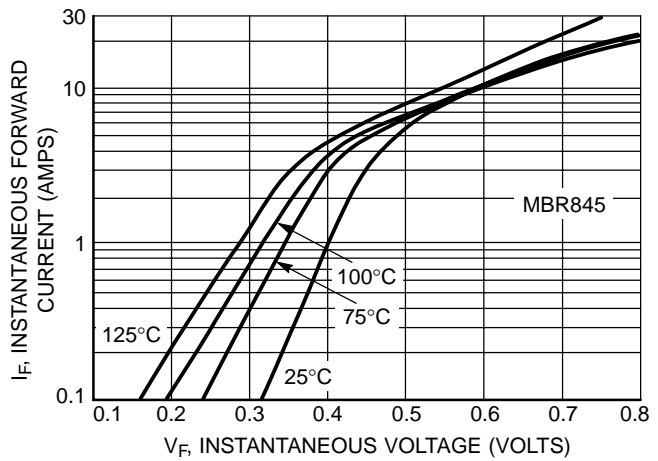


Figure 2. Maximum Forward Voltage

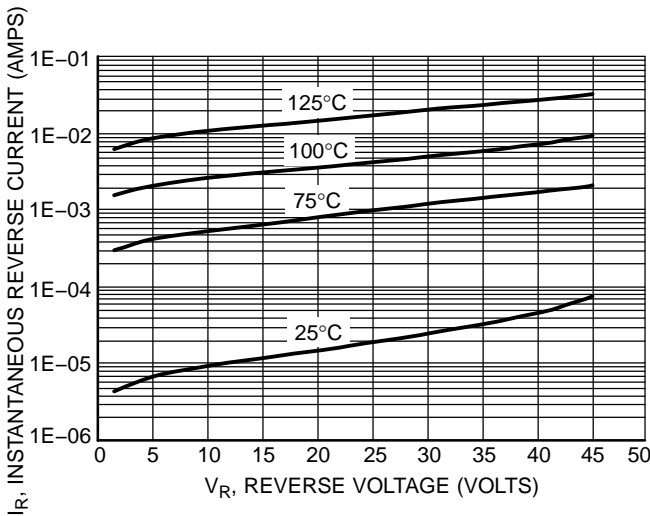


Figure 3. Typical Reverse Current

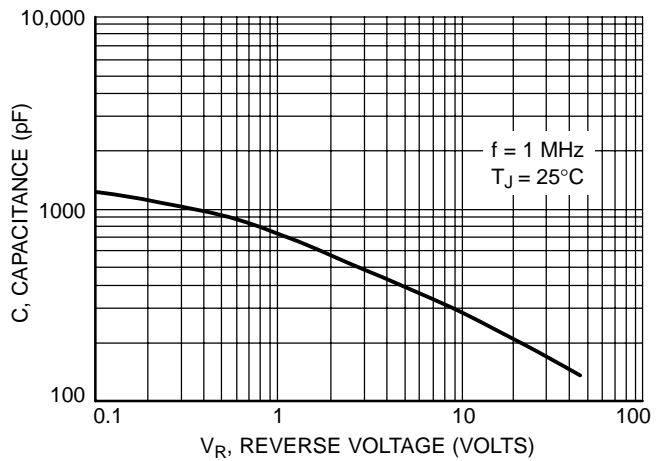


Figure 4. Typical Capacitance

# 80SQ045N

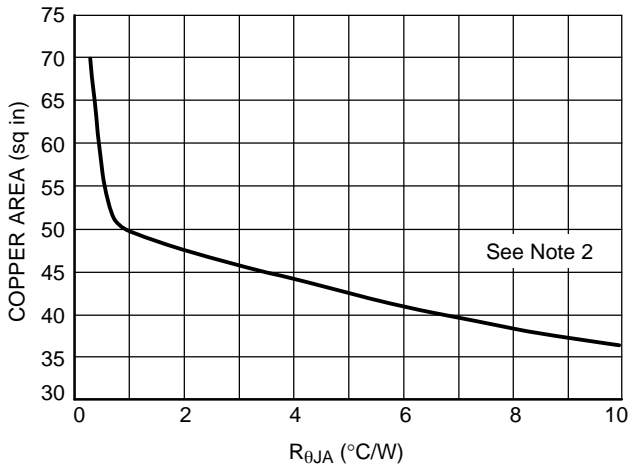


Figure 5.  $R_{\theta JA}$  versus Copper Area

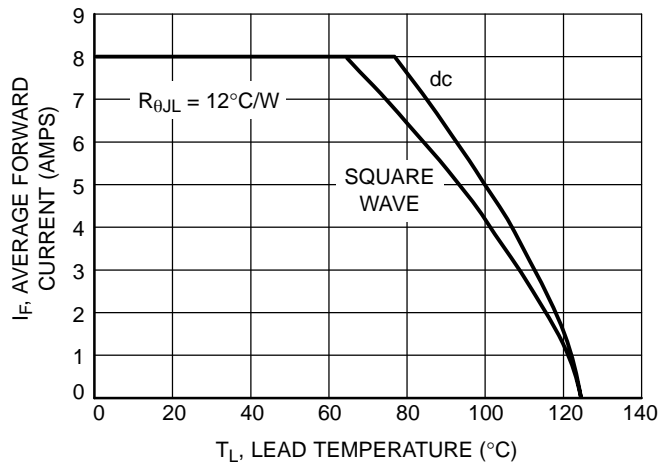


Figure 6. Current Derating - Lead

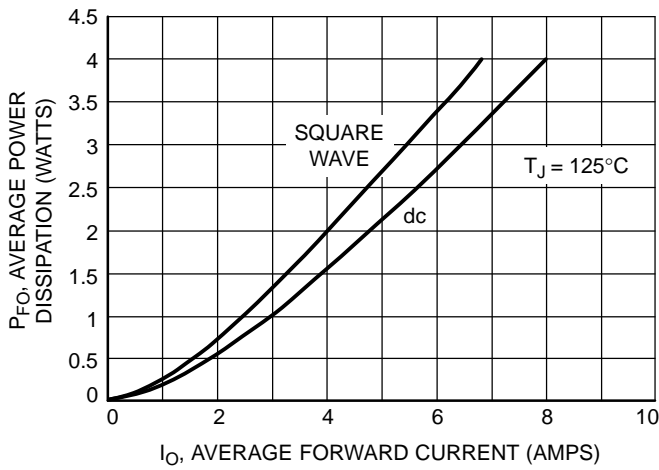


Figure 7. Forward Power Dissipation

## NOTE 2 — MOUNTING DATA

### Mounting Method

P.C. Board with 6.75 sq. in. copper surface.

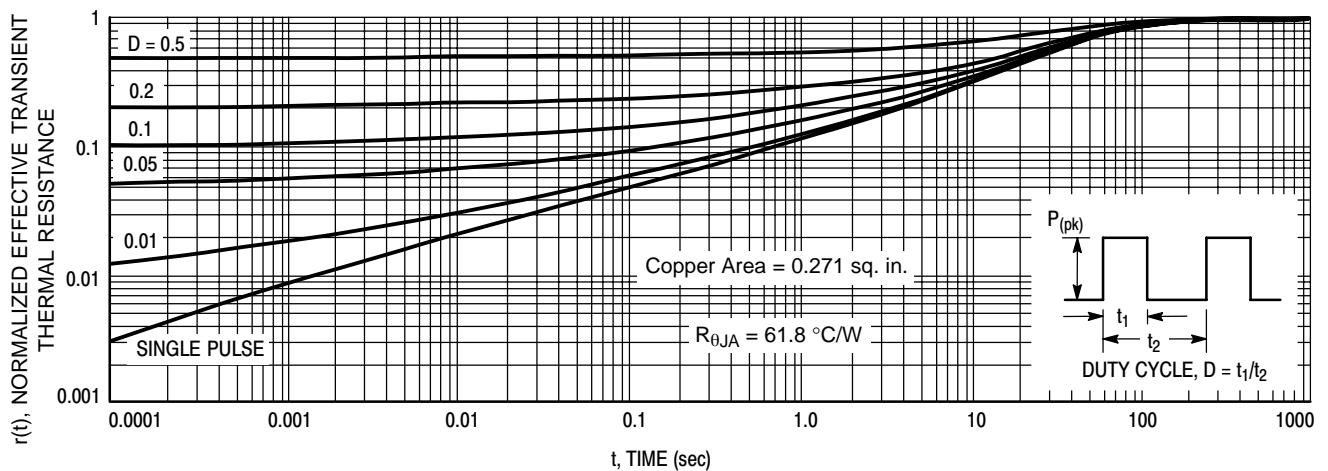
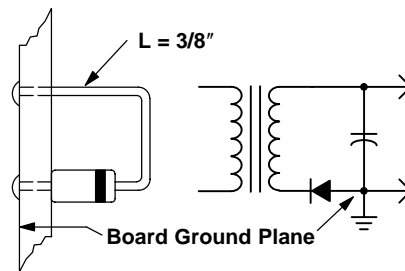


Figure 8. Thermal Response, Junction-to-Ambient

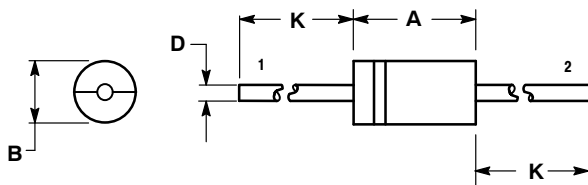


**AXIAL LEAD**  
**CASE 267-05**  
**ISSUE G**

**DATE 06/06/2000**



SCALE 1:1



- NOTES:
1. DIMENSIONS AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. 267-04 OBSOLETE, NEW STANDARD 267-05.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.287	0.374	7.30	9.50
B	0.189	0.209	4.80	5.30
D	0.047	0.051	1.20	1.30
K	1.000	---	25.40	---

STYLE 1:  
 PIN 1. CATHODE (POLARITY BAND)  
 2. ANODE

STYLE 2:  
 NO POLARITY

<b>DOCUMENT NUMBER:</b>	<b>98ASB42170B</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>AXIAL LEAD</b>	<b>PAGE 1 OF 1</b>

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Email Requests to: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**onsemi Website:** [www.onsemi.com](http://www.onsemi.com)

### TECHNICAL SUPPORT

**North American Technical Support:**

Voice Mail: 1 800-282-9855 Toll Free USA/Canada

Phone: 011 421 33 790 2910

**Europe, Middle East and Africa Technical Support:**

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative