# 2SA1576ART1

# **General Purpose Amplifier Transistors**

# **PNP Surface Mount**

## Features

- Moisture Sensitivity Level: 1
- Pb–Free Package is Available

# **MAXIMUM RATINGS** ( $T_A = 25^{\circ}C$ )

Rating	Symbol	Value	Unit
Collector-Base Voltage	V <sub>(BR)CBO</sub>	60	Vdc
Collector-Emitter Voltage	V <sub>(BR)CEO</sub>	50	Vdc
Emitter-Base Voltage	V <sub>(BR)EBO</sub>	7.0	Vdc
Collector Current – Continuous	Ι <sub>C</sub>	100	mAdc
Collector Current – Peak	I <sub>C(P)</sub>	200	mAdc

# THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation	PD	200	mW
Junction Temperature	TJ	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

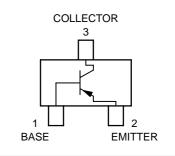
Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Breakdown Voltage $(I_C = 2.0 \text{ mAdc}, I_B = 0)$	V <sub>(BR)CEO</sub>	50	-	Vdc
Collector–Base Breakdown Voltage $(I_C = 10 \ \mu Adc, I_E = 0)$	V <sub>(BR)CBO</sub>	60	-	Vdc
Emitter–Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$	V <sub>(BR)EBO</sub>	7.0	-	Vdc
Collector-Base Cutoff Current ( $V_{CB}$ = 60 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	0.1	μAdc
$\label{eq:constraint} \begin{array}{ll} \mbox{Collector-Emitter Cutoff Current} & I_{CEO} \\ (V_{CE} = 10 \mbox{ Vdc}, I_B = 0) \\ (V_{CE} = 30 \mbox{ Vdc}, I_B = 0) \\ (V_{CE} = 30 \mbox{ Vdc}, I_B = 0, T_A = 80^{\circ}\mbox{C}) \end{array}$			0.1 2.0 1.0	μAdc μAdc mAdc
DC Current Gain (Note 1) ( $V_{CE}$ = 6.0 Vdc, I <sub>C</sub> = 2.0 mAdc)	h <sub>FE</sub>	180	390	-
Collector–Emitter Saturation Voltage $(I_C = 100 \text{ mAdc}, I_B = 10 \text{ mAdc})$	V <sub>CE(sat)</sub>	-	0.5	Vdc

1. Pulse Test: Pulse Width  $\leq$  300  $\mu s,\, D.C. \leq$  2%.



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# MARKING DIAGRAM





M = Date Code\*= Pb-Free Package

(Note: Microdot may be in either location) \*Date Code orientation may vary depending upon manufacturing location.

# **ORDERING INFORMATION**

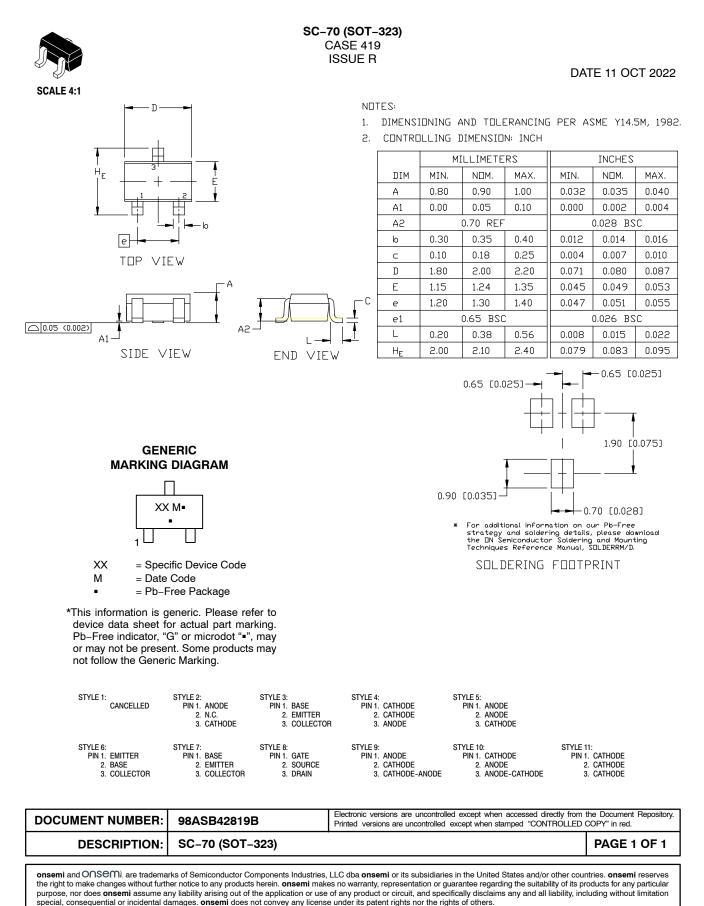
Device*	Package	Shipping <sup>†</sup>
2SA1576ART1	SC-70	3000/Tape & Reel
2SA1576ART1G	SC–70 (Pb–Free)	3000/Tape & Reel

\*The "T1" suffix refers to a 7 inch reel.

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

# onsemi



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